TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7USB31FK

Dual SPST USB Switch

TC7USB31 is high-speed CMOS dual SPST USB Switch. The low ON- resistance and the low capacitance of the switch allow connections to USB application.

The TC7USB31 requires the output enable (\overline{OE}) input to be set high to place the output into the high impedance.

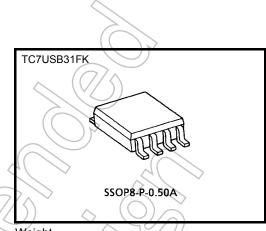
All inputs are equipped with protection circuits against static discharge.

Features

- Operating voltage: VCC = 2.3 to 3.6 V
- ON-capacitance: C_{I/O} = 4 pF Switch ON (typ.)@V_{CC} = 3.3 V
- ON-resistance: $R_{ON} = 4.5 \Omega$ (typ.) @V_{CC} = 3 V, $V_{I/O} = 0$ V
- Ron Flatness: $Ron(flat) = 1.2 \Omega (typ.) @V_{CC} = 3 V$
- Delta Ron: $\Delta Ron = 0.5 \Omega$ (typ.) @V_{CC} = 3 V
- ESD performance: Machine model $\geq \pm 200V$

Human body model $\geq \pm 2000$ V

- Power-down protection for inputs (OE and I/O)
- Package : US8

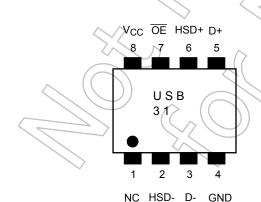


Weight SSOP8-P-0.50A

: 0.01 g (typ)

Pin Assignment (top view)

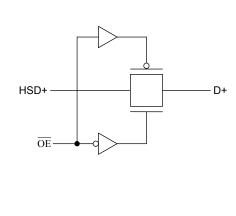
FK (SSOP8-P-0.50A)

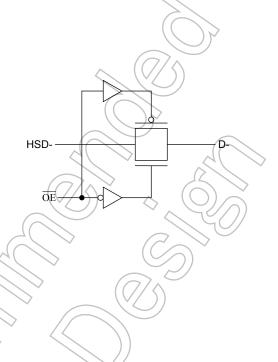


Truth Table

Inputs	Function		
ŌĒ	Function		
L	A port = B port		
Н	Disconnect		

System Diagram





Absolute Maximum Ratings (Note)

Charact	eristic	Symbol	Rating	Unit
Power supply range		V _{CC}	-0.5 to 4.6	V
Control pin input voltage	(OE)	V _{IN} -0.5 to 4.6		V
Switch terminal I/O voltage	V _{CC} =0V or Switch=Off	\/-	-0.5 to 4.6	
Switch terminal I/O voltage	Switch=On	V _S	-0.5 to V _{CC} +0.5	× ×
Clump diode current	Control input	luz	-50	ma
	Switch	I _{IK}	±50	mA
Switch I/O current		IS	50	mA
Power dissipation		P _D	200	mW
DC V _{CC} /GND current		I _{CC} /I _{GND}	±100	mA
Storage temperature		T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction. Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

Characteristic	Symbol	Rating	Unit
Power supply voltage	V _{CC}	2.3 to 3.6	V
Control pin input voltage (OE)	V _{IN}	0 to 3.6	٧
Switch I/O voltage VCC=0V or Switch=Off	Vs	0 to 3.6	٧
Switch=On	vs(/	0 to V _{CC}	V
Operating temperature	Topr	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Electrical Characteristics

DC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Character	istics	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Unit
Input voltage "H" level	"H" level	V _{IH}	_	2.3 to 3.6	0.46 × V _C C	_	_	V
(\overline{OE})	"L" level	V _{IL}	_	2.3 to 3.6	\mathcal{L}) }	0.25 × V _{CC}	V
Input leakage curr (OE)	rent	I _{IN}	V _{IN} = 0 to 3.6 V	2.3 to 3.6	(2)	_	±1.0	μА
Power-off leakage	current	loff	V _{IN} = 0 to 3.6 V	Ø	\ <u></u>	_	±5.0	μΑ
Off-state leakage (switch off)	current	I _{SZ}	$V_{IS} = 0$ to V_{CC} , $\overline{OE} = V_{CC}$	2.3 to 3.6)	-6	±5.0	μА
ON resistance	(Note 2)	R _{ON}	$V_{IS} = 0 \text{ V}, I_{IS} = 30 \text{ mA}$ (Note 1) $V_{IS} = 0.4 \text{ V}, I_{IS} = 30 \text{ mA}$ (Note 1)	3.0	_ _ (4.5	9.5	Ω
	, ,		$V_{IS} = 3.0 \text{ V}, I_{IS} = 30 \text{ mA}$ (Note 1)	3.0	_\	11//))18	
Delta R _{ON}		ΔR _{ON}	$V_{IS} = 0.4 \text{ V}, 1.0 \text{ V}, I_{IS} = 30 \text{ mA}$	3.0	_	0.5	/ _	Ω
On-Resistance Fl	atness	R _{ON(flat)}	$V_{IN} = 0 \text{ V to } 1.0 \text{ V, } I_{IS} = 30 \text{ mA}$	3.0	(G)	1.2	_	Ω
Quiescent supply	current	Icc	$V_{IN} = V_{CC}$ or GND, $I_{OUT} = 0$	3.6			2.0	μΑ
Increase in I _{CC} pe	er input	ΔI _{CC}	V _{IN} = 1.8 V	3.6	\(\frac{1}{2}\)	_	10.0	μΑ

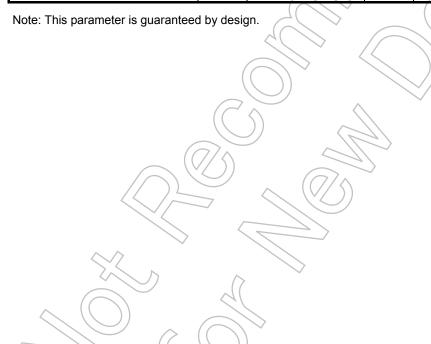
Note 1: All typical values are at Ta = 25°C.

Note 2: Measured by the voltage drop between D+/D- and HSD+/HSD- pins at the indicated current through the switch. ON resistance is determined by the lower of the voltages on the two pins.



AC Characteristics V_{CC} = 3.3V \pm 10% (Ta = –40 to 85°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Unit
Propagation Delay Time (Note)	tpd	C _L =5pF	3.3 ± 0.3	_	0.25	_	ns
Turn ON Time (OE to Output)	t _{ON}	R _L =50Ω, C _L =5pF	3.3 ± 0.3	_	4	10	ns
Turn OFF Time (OE to Output)	tOFF	R _L =50Ω, C _L =5pF	3.3 ± 0.3	-6	3.2	9	ns
Output skew between center port to any other port (Note)	tsk(0)	C _L =5pF	3.3 ± 0.3		0.1	_	ns
Skew of Opposite Transitions of the same output (t _{pHL} - t _{pLH}) (Note)	t _{SK(P)}	C _L =5pF	3.3 ± 0.3		0.1		ns
Off Isolation (Non-Adjacent)	OIRR	R _T =50Ω, f=240MHz	3.3 ± 0.3	> -	-27	7	dB
Crosstalk(Non-Adjacent)	XTalk	R _T =50Ω, f=240MHz	3.3 ± 0.3	_	-60	9	dB
-3dB Bandwidth	BW	R _T =50Ω, C _L =0pF	3.3 ± 0.3	_((1100	_	MHz



Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Тур.	Unit
Control pin input capacitance ($\overline{\sf OE}$)	C _{IN}	$V_{IN} = 0 V$	(Note)	3.3	4	pF
Switch terminal Off capacitance	C _{I/O}	$V_{IS} = 0 V$, $\overline{OE} = V_{CC}$	(Note)	3.3	2	pF
Switch terminal On capacitance	C _{I/O}	$V_{IS} = 0 V, \overline{OE} = GND$	(Note)	3.3	4	pF



AC Test Circuit Load / Waveform

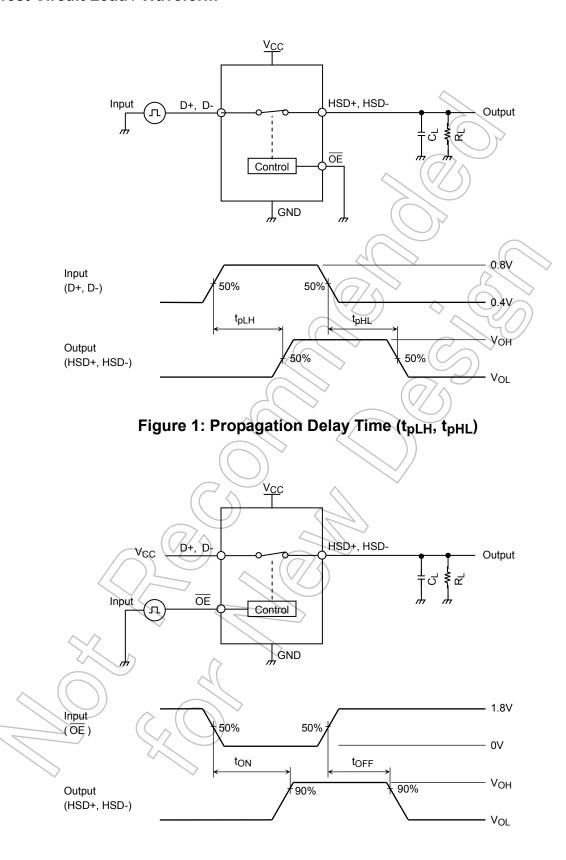


Figure 2: Turn ON/Turn OFF (ton, toff)

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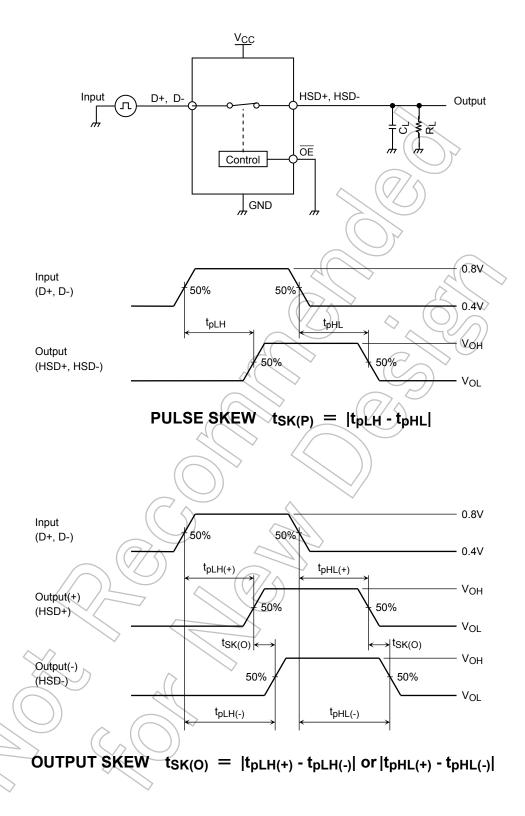


Figure 3: Skew of Opposite Transitions of the same output, Output skew

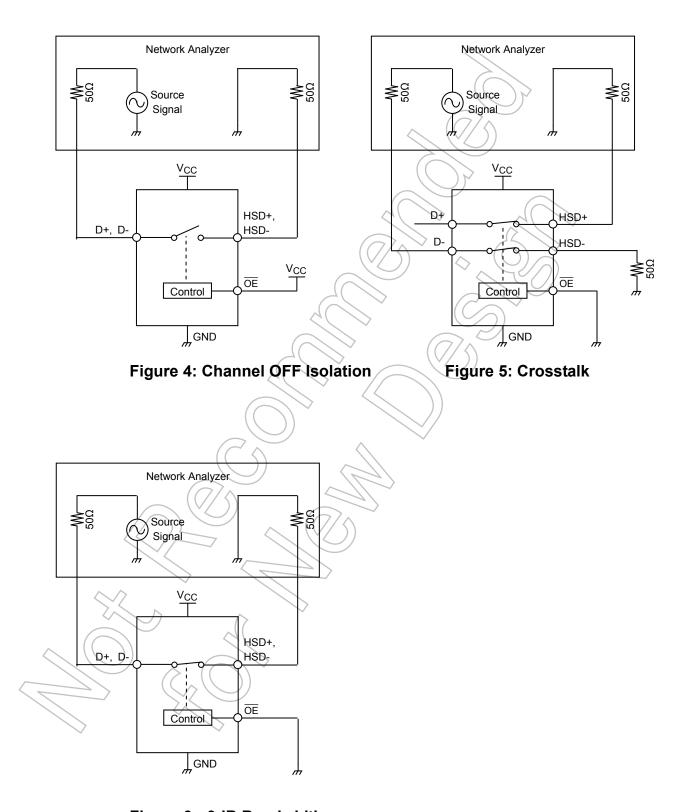
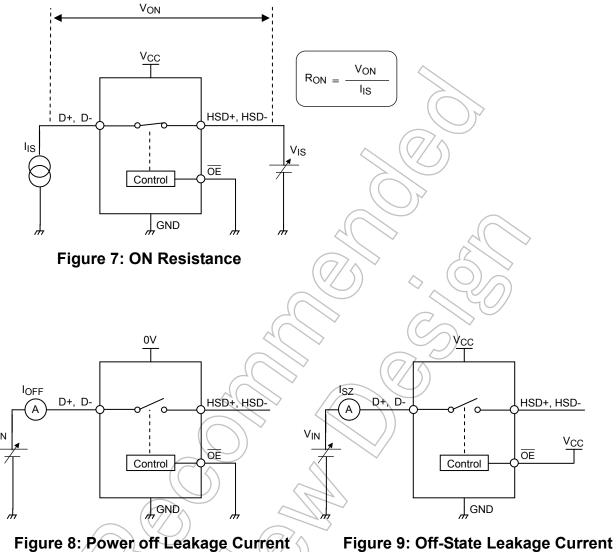
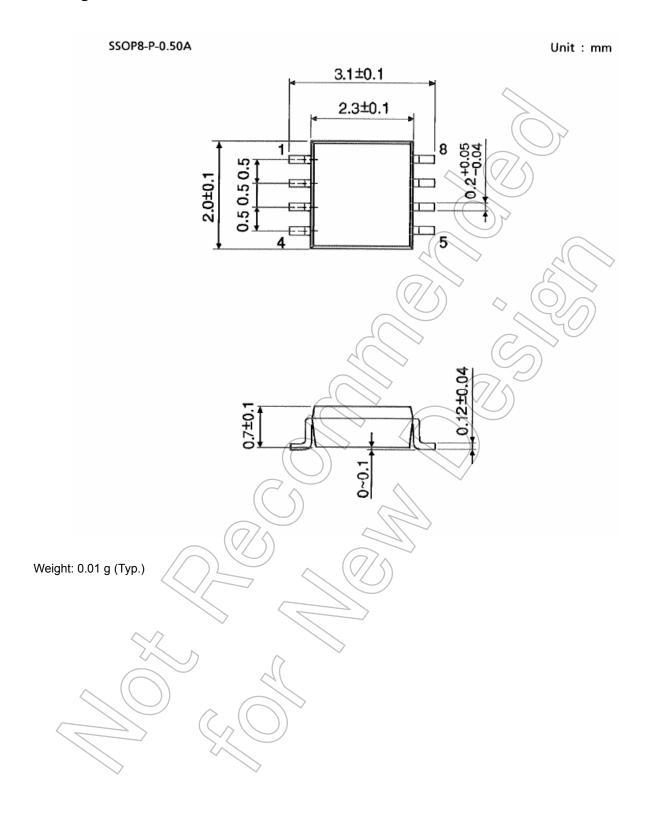


Figure 6: -3dB Bandwidth



Package Dimension



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