TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7W08FU, TC7W08FK

### **Dual 2-Input AND Gate**

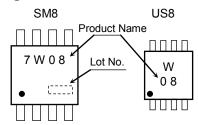
#### **Features**

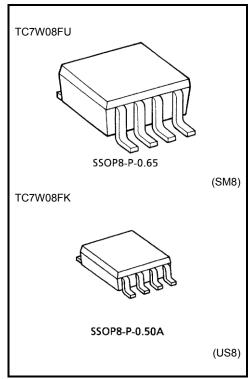
High Speed : t<sub>pd</sub> = 6ns (typ.) at V<sub>CC</sub> = 5V
 Low power dissipation : I<sub>CC</sub> = 1μA (max) at Ta = 25°C
 High noise immunity : V<sub>NIH</sub> = V<sub>NIL</sub> = 28% V<sub>CC</sub> (min)

Output drive capability : 10 LSTTL Loads
 Symmetrical Output Impedance : |I<sub>OH</sub>| = I<sub>OL</sub>= 4mA (min)

Balanced propagation delays : t<sub>pLH</sub> ≈ t<sub>pHL</sub>
 Wide operating voltage range : V<sub>CC</sub> = 2 to 6V

### Marking

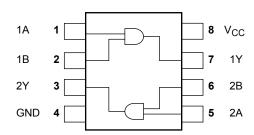




Weight

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

### Pin Assignment (top view)



Start of commercial production 1991-09

## Absolute Maximum Ratings (Ta = 25°C)

**TOSHIBA** 

Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	−0.5 to 7.0	V	
DC input voltage	V <sub>IN</sub>	−0.5 to V <sub>CC</sub> + 0.5	V	
DC output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> + 0.5	V	
Input diode current	I <sub>IK</sub>	±20	mA	
Output diode current	lok	±20	mA	
DC output current	I <sub>OUT</sub>	±25	mA	
DC V <sub>CC</sub> /ground current	Icc	±25	mA	
Dower dissination	D-	300 (SM8)	mW	
Power dissipation	P <sub>D</sub>	200 (US8)	] ''''	
Storage temperature	T <sub>stg</sub>	−65 to 150	°C	
Lead temperature (10 s)	TL	260	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

## **IEC Logic Symbol**



#### **Truth Table**

Α	В	Υ
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

### **Operating Ranges**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	2.0 to 6.0	V
Input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	V
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	−40 to 85	°C
		0 to 1000 (V <sub>CC</sub> = 2.0 V)	
Input rise and fall time	$t_r$ , $t_f$	0 to 500 (V <sub>CC</sub> = 4.5 V)	ns
		0 to 400 $(V_{CC} = 6.0 \text{ V})$	



## **Electrical Characteristics**

## **DC Characteristics**

Characteristics Symbol Test Condition		0		Ta = 25°C			Ta = -40 to 85°C		1.124	
		Test	lest Condition		Min	Тур.	Max	Min	Max	Unit
			2.0	1.5	_	_	1.5	_		
High-level input voltage V <sub>IH</sub>		_	4.5	3.15	_	_	3.15	_		
		,		6.0	4.2	1	1	4.2	ı	V
			2.0	_	1	0.5	_	0.5		
Low-level input voltage V <sub>IL</sub>		_		_	ı	1.35	_	1.35		
				_	ı	1.8	_	1.8		
			I <sub>OH</sub> = -20 μA	2.0	1.9	2.0		1.9	1	
		$V_{IN} = V_{IH}$		4.5	4.4	4.5	_	4.4	-	
High-level output voltage	V <sub>OH</sub>			6.0	5.9	6.0	_	5.9	-	
			$I_{OH} = -4 \text{ mA}$	4.5	4.18	4.31	1	4.13		
			$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80	ı	5.63	1	
Low-level output voltage V <sub>OL</sub>			I <sub>OL</sub> = 20 μA	2.0	_	0.0	0.1	_	0.1	
				4.5	_	0.0	0.1	_	0.1	
	$V_{OL}$	OF VIL		6.0	_	0.0	0.1	_	0.1	
			I <sub>OL</sub> = 4 mA	4.5	_	0.17	0.26	_	0.33	
			$I_{OL} = 5.2 \text{ mA}$	6.0	_	0.18	0.26	_	0.33	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	_	±0.1	_	±1.0	μΑ
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	_	1.0	_	10.0	μΑ

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## AC Characteristics (C<sub>L</sub>= 15pF, V<sub>CC</sub> = 5V, Ta = 25°C)

Characteristics	Symbol	Took Condition		Llmit		
		Test Condition	Min	Тур.	Max	Unit
Output Transition Time	t <sub>TLH</sub>	_	_	4	8	ns
Propagation Delay Time	t <sub>pLH</sub>	-	_	6	12	ns

## AC Characteristics ( $C_L$ = 50pF, Input: $t_r$ = $t_f$ = 6 ns)

Characteristics	Currente ed	Symbol Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
Characteristics Symb	Symbol		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
Output Transition Time			2.0	_	25	75	_	95	
	t <sub>TLH</sub>	_	4.5	_	7	15	_	19	ns
	t <sub>THL</sub>		6.0	_	6	13	_	16	
Propagation delay time	t <sub>pLH</sub>	-	2.0	_	27	75	_	95	
			4.5	_	8	15	_	19	ns
			6.0	_	7	13	_	16	
Input capacitance	C <sub>IN</sub>			_	5	10	_	10	pF
Power dissipation capacitance	$C_{PD}$		(Note 1)	_	19	_	_	_	pF

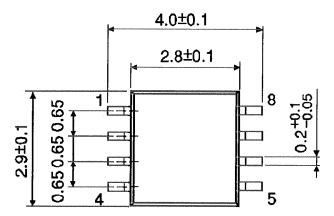
Note 1: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

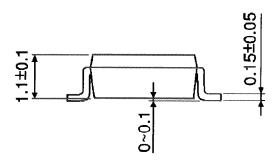
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$ 

## **Package Dimensions**

SSOP8-P-0.65 Unit: mm



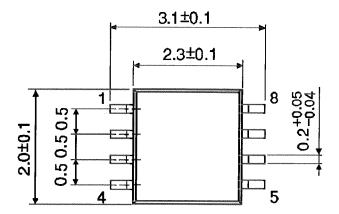


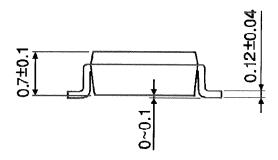
5

Weight: 0.02 g (typ.)

# **Package Dimensions**

SSOP8-P-0.50A Unit: mm





6

Weight: 0.01 g (typ.)

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