TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7W14FU, TC7W14FK

#### Schmitt Inverter

The TC7W14 is high speed  $\rm C^2MOS$  Schmitt Inverter fabricated with silicon gate  $\rm C^2MOS$  technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the  $C^2MOS$  low power dissipation.

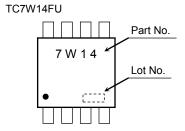
Pin configuration and function are the same as the TC7WU04 but the inputs have 25% VCC hysteresis and with its Schmitt trigger function, the TC7W14 can be used as a line receivers which will receive slow input signals.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

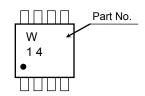
#### **Features**

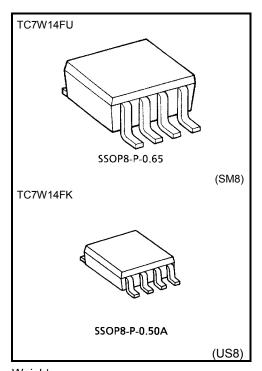
- High speed: t<sub>pd</sub> = 11 ns (typ.) at V<sub>CC</sub> = 5 V
- Low power dissipation:  $I_{CC} = 1\mu A$  (max) at Ta = 25°C
- High noise immunity: V<sub>H</sub> = 1.1 V at V<sub>CC</sub> = 5V
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: |I<sub>OH</sub>| = I<sub>OL</sub> = 4mA (min)
- Balanced propagation delays: t<sub>pLH</sub> ≃ t<sub>pHL</sub>
- Wide operating voltage range: V<sub>CC</sub> (opr) = 2 to 6V

## Marking



TC7W14FK





Weight SSOP8-P-0.65: 0.02 g (typ.) SSOP8-P-0.50A: 0.01 g (typ.)

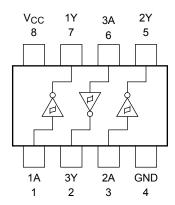
## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	–0.5 to 7	V
DC input voltage	V <sub>IN</sub>	$-0.5$ to $V_{CC}$ + $0.5$	٧
DC output voltage	V <sub>OUT</sub>	$-0.5$ to $V_{CC}$ + $0.5$	٧
Input diode current	I <sub>IK</sub>	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /ground current	Icc	±25	mA
Dower discipation	PD	300 (SM8)	mW
Power dissipation	FD	200 (US8)	IIIVV
Storage temperature range	T <sub>stg</sub>	-65 to 150	°C
Lead temperature (10 s)	TL	260	°C

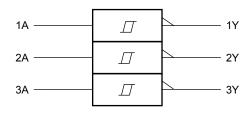
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Pin Configuration (top view)



#### **Logic Diagram**



## **Truth Table**

Α	Y
L	Н
Н	L

# **Operating Ranges**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	2 to 6	V
Input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	V
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V
Operating temperature range	T <sub>opr</sub>	-40 to 85	°C

# **Electrical Characteristics**

### **DC Electrical Characteristics**

Characteristics Symbol Test Condition			-	Ta = 25°C			Ta = -40 to 85°C				
		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit			
					2.0	1.0	1.25	1.5	1.0	1.5	
High level	High level	V <sub>P</sub>	_		4.5	2.3	2.7	3.15	2.3	3.15	
Threshold					6.0	3.0	3.5	4.2	3.0	4.2	V
voltage						0.3	0.65	0.9	0.3	0.9	V
	Low level	V <sub>N</sub>	_		4.5	1.13	1.6	2.0	1.13	2.0	
					6.0	1.5	2.3	2.6	1.5	2.6	
					2.0	0.3	0.6	1.0	0.3	1.0	
Hysteresis voltage V <sub>H</sub>			_		0.6	1.1	1.4	0.6	1.4	V	
						0.8	1.2	1.7	8.0	1.7	
		High level V <sub>OH</sub>	$V_{IN} = V_{IL}$	I <sub>OH</sub> = -20 μA	2.0	1.9	2.0	_	1.9	_	
High leve					4.5	4.4	4.5	_	4.4	_	
	High level				6.0	5.9	6.0	_	5.9	_	
				I <sub>OH</sub> = -4 mA	4.5	4.18	4.31	_	4.13	_	
Output				$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80	_	5.63	_	\ /
voltage  Low level				2.0	_	0	0.1	_	0.1	V	
		ow level $V_{OL}$ $V_{IN} = V_{OL}$		$I_{OL} = 20 \mu A$	4.5	_	0	0.1	_	0.1	- - -
	Low level		$V_{IN} = V_{IH}$		6.0	_	0	0.1	_	0.1	
				I <sub>OL</sub> = 4 mA	4.5	_	0.17	0.26	_	0.33	
				I <sub>OL</sub> = 5.2 mA	6.0		0.18	0.26	_	0.33	
Input leakage	current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	_	±0.1	_	±1.0	μА
Quiescent sup	ply current	y current I <sub>CC</sub> V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	_	1.0	_	10.0	μА	



# AC Electrical Characteristics (C<sub>L</sub> = 15 pF, $V_{CC}$ = 5 V, Ta = 25°C)

Characteristics	Symbol	Toot Condition	•	Limit		
		Test Condition	Min	Тур.	Max	Unit
Output transition time	t <sub>TLH</sub> t <sub>THL</sub>	_		4	8	ns
Propagation delay time	t <sub>pLH</sub> t <sub>pHL</sub>	_		11	21	ns

### AC Electrical Characteristics ( $C_L = 50 \text{ pF}$ , input $t_r = t_f = 6 \text{ ns}$ )

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	
Output transition time		_	2.0	_	30	75	_	95	
	t <sub>TLH</sub> t <sub>THL</sub>		4.5	_	8	15	_	19	ns
	THL		6.0	_	7	13	_	16	
Propagation delay time	t <sub>pLH</sub>	_	2.0	_	42	125	_	155	
			4.5	_	14	25	_	31	ns
			6.0	_	12	21	_	26	
Input capacitance	C <sub>IN</sub>	_		_	5	10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub>		(Note)	_	28	_	_	_	pF

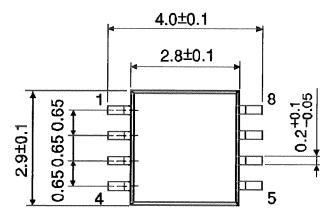
Note: C<sub>PD</sub> is defined as the value of internal equivalent capacitance of IC which is calculated from the operating current consumption without load.

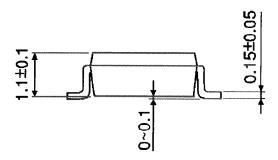
Average operating current can be obtained by the equation hereunder.

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3 \text{ (per gate)}$ 

# **Package Dimensions**

SSOP8-P-0.65 Unit: mm





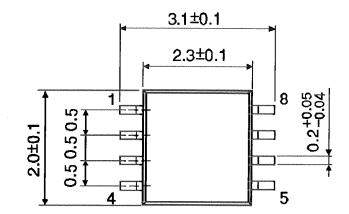
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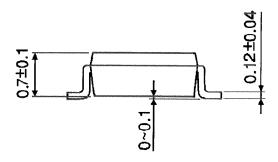
Weight: 0.02 g (typ.)

# **Package Dimensions**

SSOP8-P-0.50A







Weight: 0.01 g (typ.)

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