

TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

# **TCR1HF Series**

High voltage, Low quiescent current, Fast load transient CMOS Linear Regulator

## 1. Description

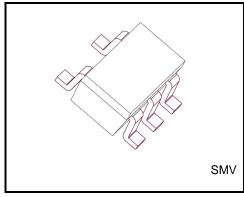
TCR1HF series are CMOS general-purpose single-output voltage regulators with 40 V high voltage, 1  $\mu$ A low quiescent current, high response load transient, an on/off control input.

These voltage regulators are available in fixed output voltages between 1.8 V and 5.0 V and capable of driving up to 150 mA. They feature Overcurrent protection, Thermal shutdown and Auto-discharge.

The TCR1HFxx series is offered in the standard small plastic mold package SMV (2.9 mm x 2.8 mm x 1.1 mm (Typ.)).

## 2. Applications

Power supply applications for mobile devices and home appliances that require low standby power with high voltage input



Weight:

SMV (SOT-25) (SC-74A): 16 mg (Typ.)

#### 3. Features

- High input voltage 40 V (Absolute Maximum ratings), 4 V to 36 V (Operation input voltage)
- Low quiescent current  $I_{B(ON)}$ : 1  $\mu A$  (Typ.) @  $I_{OUT} = 0$  mA
- · High response load transient

-60 mV / +50 mV @ 3.3 V output,  $I_{OUT}$  = 0 mA  $\leftrightarrow$  10 mA

- Wide range output voltage line up (V<sub>OUT</sub> = 1.8 V to 5.0 V)
- High accuracy output voltage ± 1 % (Ta = 25 °C)
- Auto-discharge (TCR1HFxxA series Only)
- Overcurrent protection
- Thermal shutdown
- In rush current reduction
- · Pull up connection between CONTROL and VIN
- · Ceramic capacitors can be used
- General package SMV (SOT-25) (2.8 mm x 2.9 mm x 1.1 mm)

Start of commercial production 2023-02



## 4. Absolute Maximum Ratings (Ta = 25 °C)

Characteristics	Symbol	Rating	Unit
Input voltage	V <sub>IN</sub>	-0.3 to 40	V
Control voltage	V <sub>CT</sub>	-0.3 to 40	V
Output voltage	V <sub>OUT</sub>	-0.3 to 6	V
Output current	I <sub>OUT</sub>	150	mA
Dower discipation	Б	200 (Note1)	
Power dissipation	P <sub>D</sub>	580 (Note2)	mW
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature range	T <sub>stg</sub>	−55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

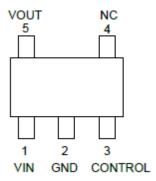
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Unit Rating

Note 2: Rating at mounting on a board

(FR4 board: 25.4 mm x 25.4 mm x 1.6 mm)

## 5. Pin Assignment (top view)



Start of commercial production 2023-01



## 6. Operating Ranges

Characteristics	Symbol	Rating		
Input voltage	V <sub>IN</sub>	V <sub>OUT</sub> ≤ 3.3 V 4.0 to 36.0 (Note 2)		V
		V <sub>OUT</sub> > 3.3 V	V <sub>OUT</sub> +1 to 36.0 (Note 2)	V
Control voltage	V <sub>CT</sub>		V	
Output voltage	V <sub>OUT</sub>	1.8 to 5.0		
Output current	I <sub>OUT</sub>	DC	150 (Note 3)	mA
Operation Temperature	T <sub>opr</sub>	-40 to 125		
Output Capacitance	C <sub>OUT</sub>	≥ 0.47		

Note 2: Please refer to Dropout Voltage table(Page 5) and use it within Absolute Maximum Ratings Junction temperature and Operation Temperature Ranges

Note 3: There is possibility for significant negative affect for reliability, if this product used long time on the state includes limit or very close condition on Operating ranges.

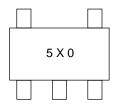
## 7. List of Products Number, Output voltage and Marking

Product No.	Output voltage(V)	Auto Discharge	Marking	
TCR1HF18A	1.8		1D8	
TCR1HF25A	2.5		2D5	
TCR1HF28A	2.8		2D8	
TCR1HF30A	3.0	Integrated	3D0	
TCR1HF31A	3.1		3D1	
TCR1HF32A	3.2		3D2	
TCR1HF33A	3.3		3D3	
TCR1HF50A	5.0		5D0	
TCR1HF18B	1.8		1X8	
TCR1HF25B	2.5		2X5	
TCR1HF28B	2.8		2X8	
TCR1HF30B	3.0	_	3X0	
TCR1HF31B	3.1		3X1	
TCR1HF32B	3.2		3X2	
TCR1HF33B	3.3		3X3	
TCR1HF50B	5.0		5X0	

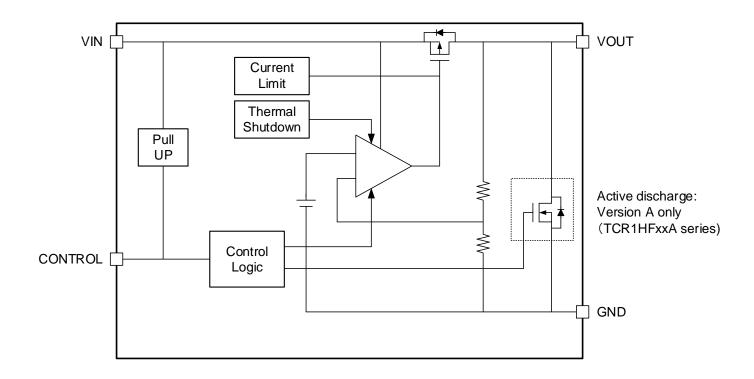


#### Marking (top view)

Example: TCR1HF50B (5.0 V output,)



#### **Block Diagram**





## 8. Electrical Characteristics

(Unless otherwise specified,  $V_{IN} = 24 \text{ V}$ ,  $I_{OUT} = 20 \text{ mA}$ ,  $C_{IN} = C_{OUT} = 0.47 \mu\text{F}$ )

Characteristics Symbol		Test Condition		T <sub>j</sub> = 25°C			T <sub>j</sub> = -40 to 125°C (Note 8)		Unit
				Min	Тур.	Max	Min	Max	
Output voltage accuracy	V <sub>OUT</sub>	I <sub>OUT</sub> = 10 mA (Note 4)		-1.0	_	+1.0	-3.5	2.0	%
Janut Valtana Danas	V	I <sub>OUT</sub> = 1 mA, V <sub>OUT</sub> :	≤ 3.3 V	4	_	36	4	36	V
Input Voltage Range	V <sub>IN</sub>	I <sub>OUT</sub> = 1 mA, V <sub>OUT</sub>	> 3.3 V	V <sub>IN</sub> + 1	_	36	V <sub>IN</sub> + 1	36	V
Line regulation	Reg·line	I <sub>OUT</sub> = 1 mA	(Note 5)	_	0	_	_	_	mV
Load regulation	Reg-load	0 mA ≤ I <sub>OUT</sub> ≤ 40 m	nA (Note 6)	_	3.5	_	_	_	mV
Quiescent current	I <sub>BON</sub>	I <sub>OUT</sub> = 0 mA		_	1	1.4	_	1.6	μΑ
Ctand by augrent	I <sub>B (OFF1)</sub>	$V_{CT} = 0 \text{ V}, V_{IN} = 4$	V	_	0.24	0.39	_	0.45	μΑ
Stand-by current	I <sub>B (OFF2)</sub>	V <sub>CT</sub> = 0 V, V <sub>IN</sub> = 24 V		_	0.27	0.42	_	0.48	μΑ
Control pull down current	I <sub>CT</sub>	-	_	_	60	_	_	_	nA
			V <sub>OUT</sub> = 1.8 V	_	760	_	_	1060	mV
			V <sub>OUT</sub> = 2.5 V	_	670	_	_	920	mV
			V <sub>OUT</sub> = 2.8 V	_	580	_	_	855	mV
Drop-out voltage	.,	450 4	V <sub>OUT</sub> = 3.0 V	_	535	_	_	820	mV
	$V_{DO}$	I <sub>OUT</sub> = 150 mA	V <sub>OUT</sub> = 3.1 V	_	520	_	_	790	mV
			V <sub>OUT</sub> = 3.2 V	_	520	_	_	780	mV
			V <sub>OUT</sub> = 3.3 V	_	510	_	_	760	mV
			V <sub>OUT</sub> = 5.0 V	_	415	_	_	660	mV
Output current limit	ICL	$V_{OUT} = V_{OUT}(NOM) * 90 \%$ ,		300	_	_	200	_	mA
Output noise voltage	V <sub>NO</sub>	I <sub>OUT</sub> = 10 mA, 10 Hz ≤ f ≤ 100 kHz, Ta = 25 °C (Note 6)		_	100	_	_	_	$\mu V_{rms}$
Ripple rejection ratio	R.R.	V <sub>IN</sub> = 4 V, I <sub>OUT</sub> = 10 mA, f = 1 kHz, V <sub>IN</sub> _Ripple = 100 mV p-p, Ta = 25 °C (Note 6)		_	60	_	_	_	dB
Output voltage slew rate	V <sub>OUTSR</sub>	_		_	10	_	_	_	mV / μs
Load transient response ΔV <sub>OUT</sub>		$I_{OUT} = 0 \text{ mA} \rightarrow 10 \text{ mA}, 1  \mu\text{s}$ $C_{OUT} = 1  \mu\text{F} \text{ (Note 7)}$		_	-60	_	_	_	mV
		$I_{OUT}$ = 10 mA $\rightarrow$ 0 mA, 1 $\mu$ s $C_{OUT}$ = 1 $\mu$ F (Note 7)		_	+50	_	_	_	mV
Control pin	V <sub>CT (ON)</sub>	_		0.9	_	V <sub>IN</sub>	1.0	V <sub>IN</sub>	V
threshold voltage	V <sub>CT (OFF)</sub>	_		0	_	0.47	0	0.4	V
Thermal shutdown threshold	T <sub>SDH</sub>	T <sub>j</sub> rising		_	155	_	_	_	°C
memiai shuluown threshold	T <sub>SDL</sub>	T <sub>j</sub> falling	falling		145	_	_	_	°C
Discharge on resistance	R <sub>SD</sub>	V <sub>OUT</sub> = 3.3 V, I <sub>OUT</sub> = -10 mA, Tj = 25 °C, TCR1HFxxA Only		_	60	_	_	_	Ω

Note 4: stable state with fixed  $I_{OUT}$  condition Note 5:  $4 \text{ V} \le V_{IN} \le 24 \text{ V } (V_{OUT} \le 3.3 \text{ V})$ 

 $V_{OUT} + 1V \le V_{IN} \le 24 \text{ V (}V_{OUT} > 3.3\text{V)}$ 

Note 6: Vout = 3.3 V

Note 7:  $V_{IN} = 4.0 \text{ V (V_{OUT} \le 3.3 \text{ V)}}$ 

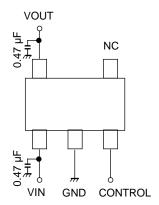
 $V_{IN} = V_{OUT} + 1 V (V_{OUT} > 3.3V)$ 

Note 8: This parameter is warranted by design.



## 9. Application note

### 9.1. Recommended Application Circuit



CONTROL voltage	Output voltage				
HIGH	ON				
LOW	OFF				
OPEN	ON				

The figure above shows the recommended configuration for using this devise. Insert a capacitor at VOUT and VIN pins for stable input / output operation. (Ceramic capacitors can be used).

### 9.2. Power Dissipation

Both unit and board-mounted power dissipation ratings for TCR1HF series are available in the Absolute Maximum Ratings table.

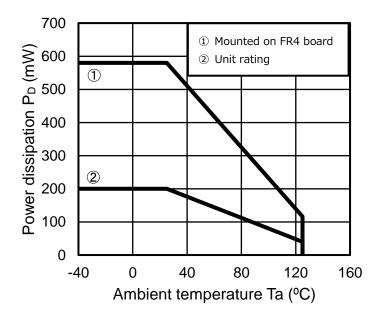
Power dissipation is measured on the board shown below.

#### **Board conditions**

\*Board material: FR4 board

Board dimension: 25.4 mm x 25.4 mm x 1.6 mm

Copper area: 645 mm<sup>2</sup>





#### 9.3. About VOUT output immediately after VIN is applied

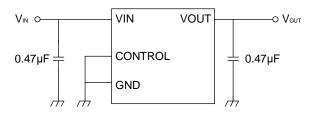
Depending on the VIN voltage and application speed, VOUT voltage may occur due to leakage current from VIN to VOUT even when CONTROL = L.

If VOUT voltage occurs and becomes a problem, it is necessary to take countermeasures such as changing the  $C_{\text{OUT}}$  capacitance or changing the power supply start-up speed.

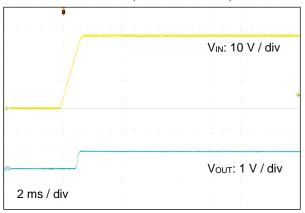
Is required. Make sure that there are no problems by performing thorough evaluations, including the temperature characteristics, in an actual application.

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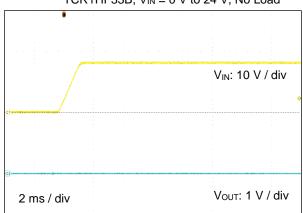
Example of VOUT output immediately after applying VIN



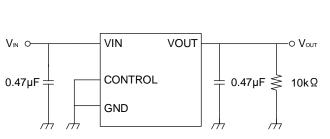
TCR1HF33B, V<sub>IN</sub> = 0 V to 36 V, No Load



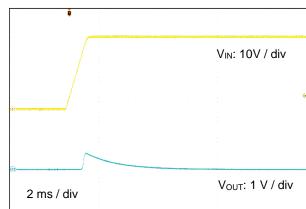
TCR1HF33B, V<sub>IN</sub> = 0 V to 24 V, No Load



In addition, when  $V_{IN}$  is changing, a leak current occurs and  $V_{OUT}$  is output. Therefore, if the output is loaded, the VOUT voltage will drop. (1 M $\Omega$ , etc.)



TCR1HF33B,  $V_{IN} = 0 \text{ V}$  to 36 V, No Load





#### 9.4. Attention in Use

#### Input/Output capacitors

Ceramic capacitors can be used with the IC, however some type capacitors may have very large temperature dependence. Please consider usage environment condition carefully to select the capacitors. Toshiba recommends under 10 ohm ESR capacitors.

Please use over  $0.47 \,\mu\text{F}$  capacitor as  $C_{\text{OUT}}$  for stable operation. If without any capacitors attached to  $C_{\text{OUT}}$ , there is a possibility to destroy or have a significant adverse effect on the IC.

The input capacitor C<sub>IN</sub> is not always required for stable operation of this device, however Toshiba recommends over 0.47 µF capacitor to improve the characteristics.

There is a possibility to generate some voltage on  $V_{OUT}$  in case  $V_{IN}$  rises from 0 V even in the state CONTROL = L. In such case on the products without discharge function, the voltage is on  $V_{OUT}$  until the consumption current is generated to the outside load.

Additionally, if the V<sub>IN</sub> is rapidly fluctuating the V<sub>OUT</sub> possibly to fluctuate transiently, regardless whether this device is stopped or operating.

In such case please confirm that there are no problems with sufficient evaluation includes temperature dependence in actual applications,

using bigger C<sub>IN</sub> to suppress the changing value on V<sub>IN</sub> rising, or using bigger C<sub>OUT</sub> to suppress the V<sub>OUT</sub> fluctuation.

#### Mounting

The long distance between IC and output capacitor might affect phase assurance by impedance in wire and inductor. For stable power supply, output capacitor need to mount near IC as much as possible. Also VIN and GND pattern need to be large and make the wire impedance small as possible.

#### Permissible Loss

Please have enough design patterns for expected maximum permissible loss. And under consideration of surrounding temperature, input voltage, and output current etc., we recommend proper dissipation ratings for maximum permissible loss; in general maximum dissipation rating is 70 to 80 %.

#### Over current Protection and Thermal shut down function

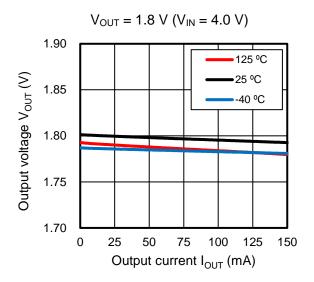
Over current protection and Thermal shut down function are designed in these products, but these are not designed to constantly ensure the suppression of the device within operation limits. Depending on the condition during actual usage, it could affect the electrical characteristic specification and reliability. Also note that if output pins and GND pins are not completely shorted out, these products might be break down.

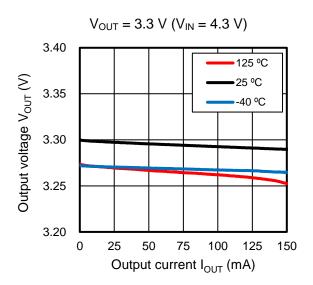
When using these products, please read through and understand the concept of dissipation for absolute maximum ratings from the above mention or our 'Semiconductor Reliability Handbook'. Then use these products under absolute maximum ratings in any condition. Furthermore, Toshiba recommend inserting failsafe system into the design.

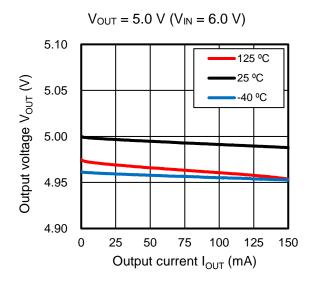


## 10. Representative Typical Characteristics

## 10.1. Output Voltage vs. Output Current

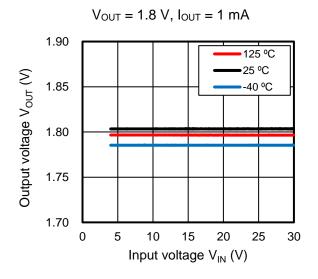


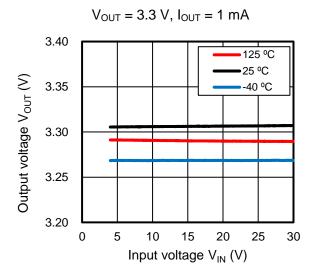


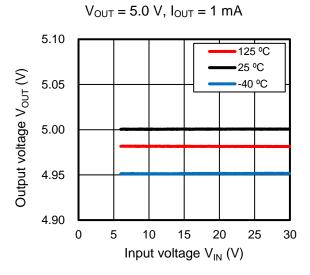




## 10.2. Output Voltage vs. Input Voltage

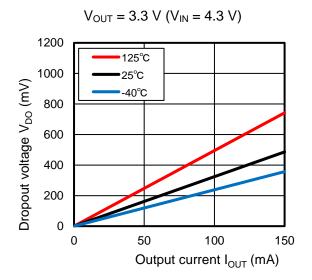


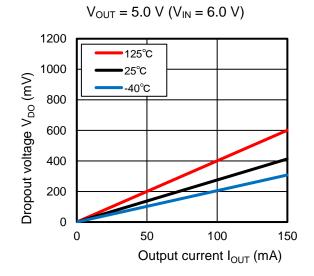




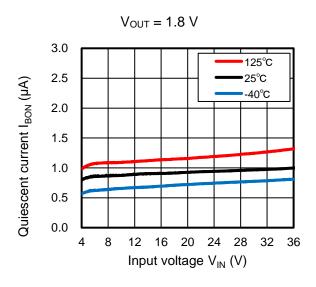


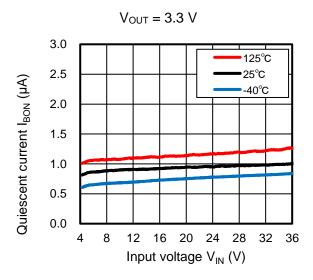
### 10.3. Dropout Voltage vs. Output Current

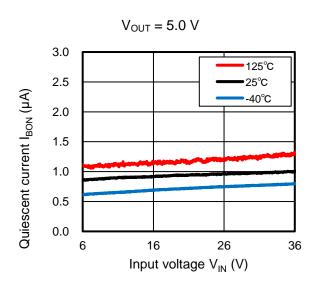




## 10.4. Quiescent Current vs. Input Voltage (Iout = 0 mA)

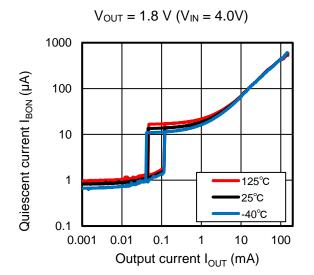


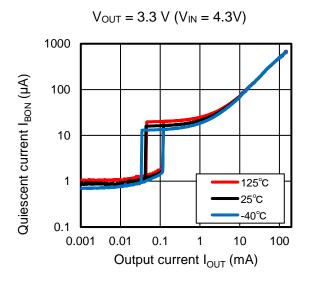


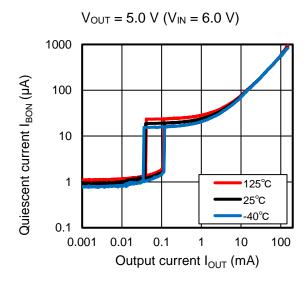




### 10.5. Quiescent Current vs. Output Current

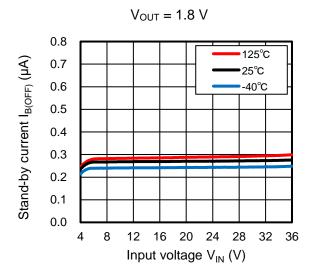


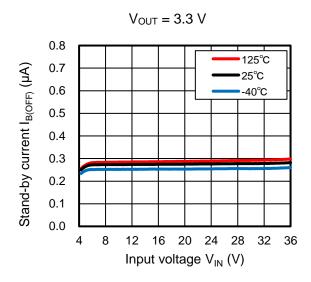


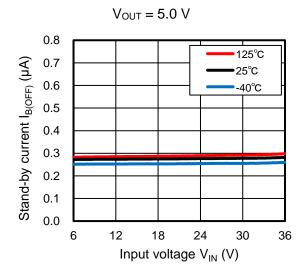




## 10.6. Standby Current vs. Input voltage



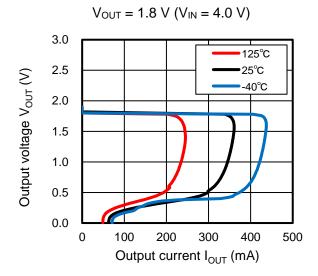


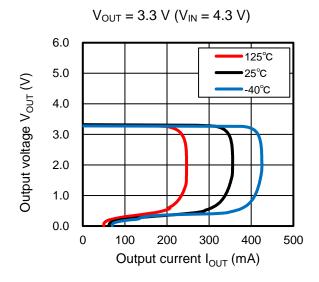


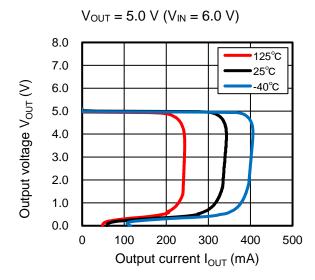
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## 10.7. Output Current Limit









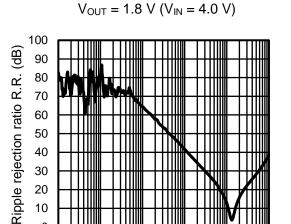
0

10

100

### 10.8. Ripple rejection Ratio vs. Frequency

 $(C_{IN} = none, C_{OUT} = 0.47 \mu F, V_{IN} Ripple = 100 mV p-p, I_{OUT} = 10 mA, Ta = 25 °C)$ 



 $V_{OUT} = 3.3 \text{ V } (V_{IN} = 4.3 \text{ V})$ 

$$V_{OUT} = 5.0 \text{ V } (V_{IN} = 6.0 \text{ V})$$

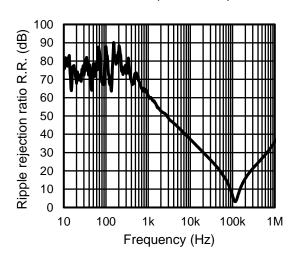
1k

Frequency (Hz)

10k

100k

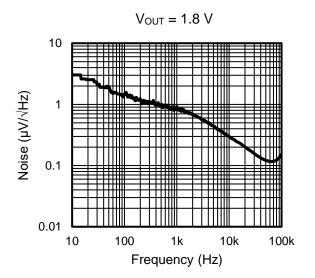
1M

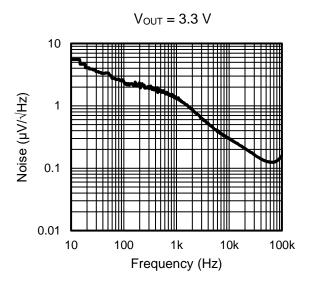


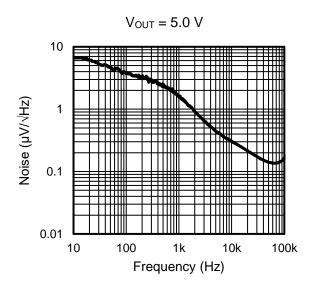


## 10.9. Output noise Voltage

(C<sub>IN</sub> = 0.47  $\mu$ F, C<sub>OUT</sub> = 0.47  $\mu$ F, V<sub>IN</sub> = 24 V, I<sub>OUT</sub> = 10 mA, Ta = 25 °C)



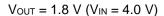


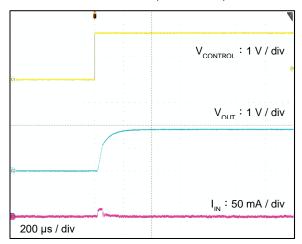




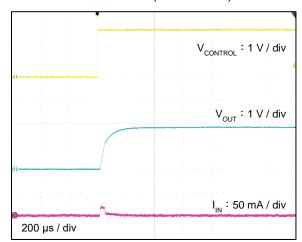
### 10.10. ton Response

( $C_{IN}$  = 0.47  $\mu$ F,  $C_{OUT}$  = 0.47  $\mu$ F,  $I_{OUT}$  = No load, Ta = 25 °C)

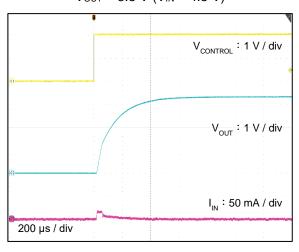




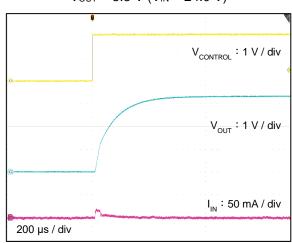
$$V_{OUT} = 1.8 \text{ V } (V_{IN} = 24.0 \text{ V})$$



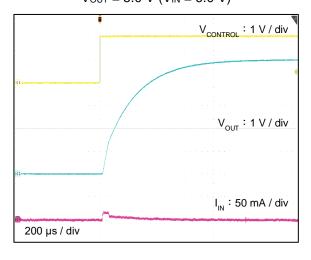
$$V_{OUT} = 3.3 \text{ V } (V_{IN} = 4.3 \text{ V})$$



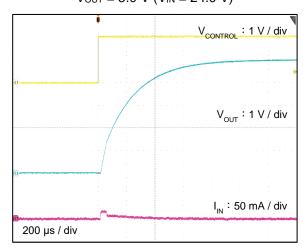
$$V_{OUT} = 3.3 \text{ V } (V_{IN} = 24.0 \text{ V})$$



$$V_{OUT} = 5.0 \text{ V } (V_{IN} = 6.0 \text{ V})$$



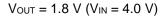
$$V_{OUT} = 5.0 \text{ V } (V_{IN} = 24.0 \text{ V})$$

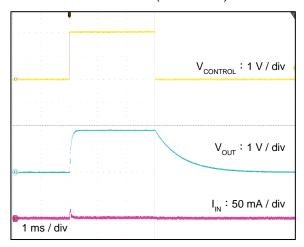




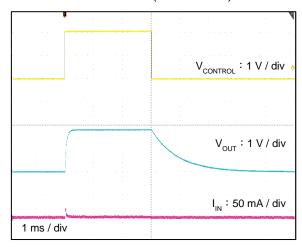
### 10.11. ton / toff Response

(C<sub>IN</sub> = 0.47  $\mu$ F, C<sub>OUT</sub> = 0.47  $\mu$ F, I<sub>OUT</sub> = 1 mA, Ta = 25 °C)

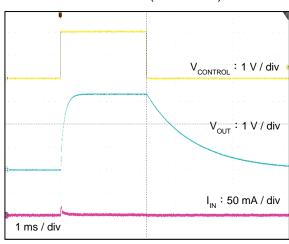




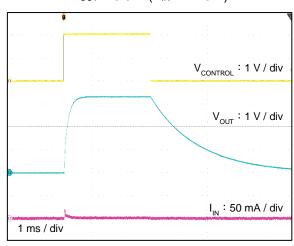
$$V_{OUT} = 1.8 \text{ V } (V_{IN} = 24.0 \text{ V})$$



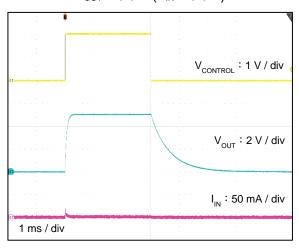
$$V_{OUT} = 3.3 \text{ V } (V_{IN} = 4.3 \text{ V})$$



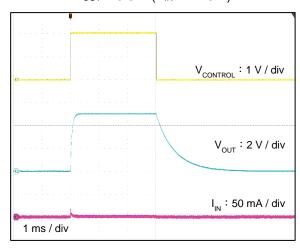
$$V_{OUT} = 3.3 \text{ V } (V_{IN} = 24.0 \text{ V})$$



$$V_{OUT} = 5.0 \text{ V } (V_{IN} = 6.0 \text{ V})$$



$$V_{OUT} = 5.0 \text{ V } (V_{IN} = 24.0 \text{ V})$$

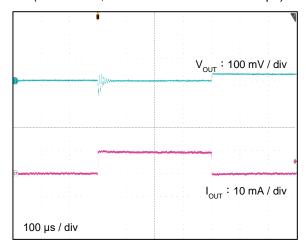




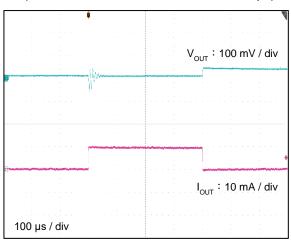
### 10.12. Load Transient Response

( $C_{\text{IN}}$  = 0.47  $\mu\text{F}$ ,  $C_{\text{OUT}}$  = 0.47  $\mu\text{F}$ , Ta = 25 °C)

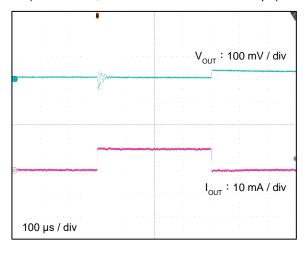
$$V_{\text{OUT}} = 1.8 \text{ V}$$
 (V<sub>IN</sub> = 4.0 V, I<sub>OUT</sub> = 0 mA  $\leftrightarrow$  10 mA at 1  $\mu s$ )



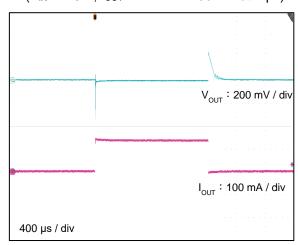
$$V_{OUT} = 3.3 \ V$$
 
$$(V_{IN} = 4.3 \ V, \ I_{OUT} = 0 \ mA \leftrightarrow 10 \ mA \ at \ 1 \ \mu s)$$



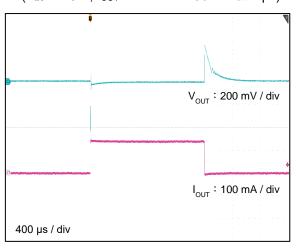
$$V_{OUT} = 5.0 \ V$$
 (V<sub>IN</sub> = 6.0 V, I<sub>OUT</sub> = 0 mA  $\leftrightarrow$  10 mA at 1  $\mu s$ )



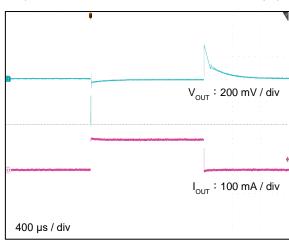
$$V_{OUT} = 1.8 \ V$$
 
$$(V_{IN} = 4.0 \ V, \ I_{OUT} = 1 \ mA \leftrightarrow 150 \ mA \ at \ 1 \ \mu s)$$



$$V_{\text{OUT}} = 3.3 \text{ V} \\ (V_{\text{IN}} = 4.3 \text{ V}, \text{ I}_{\text{OUT}} = 1 \text{ mA} \leftrightarrow 150 \text{ mA} \text{ at 1 } \mu\text{s})$$



$$V_{OUT} = 5.0 \text{ V} \\ (V_{IN} = 6.0 \text{ V}, \text{ } I_{OUT} = 1 \text{ mA} \leftrightarrow 150 \text{ mA} \text{ at 1 } \mu\text{s})$$



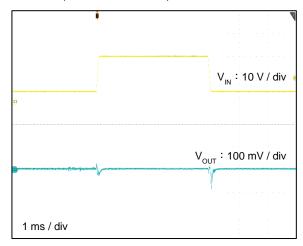
Rev.1.0



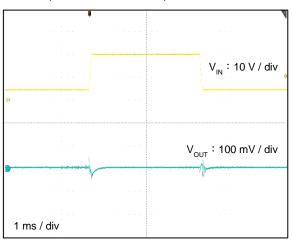
## 10.13. Line Transient Response

(C<sub>IN</sub> = 0.47 
$$\mu$$
F, C<sub>OUT</sub> = 0.47  $\mu$ F, Ta = 25 °C)

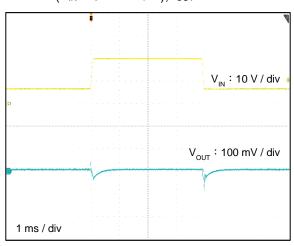
$$V_{OUT} = 1.8 \ V$$
 
$$(V_{IN} = 4 \ V \leftrightarrow 20 \ V), \ I_{OUT} = 1 \ mA$$



$$V_{OUT} = 3.3 \text{ V}$$
 $(V_{IN} = 4.3 \text{ V} \leftrightarrow 20 \text{ V}), I_{OUT} = 1 \text{ mA}$ 



$$V_{OUT} = 5.0 \ V$$
 
$$(V_{IN} = 6 \ V \leftrightarrow 20 \ V), \ I_{OUT} = 1 \ mA$$

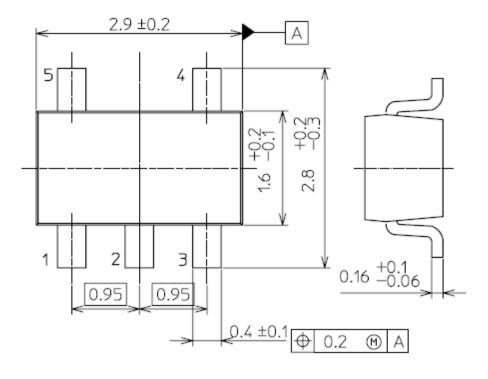


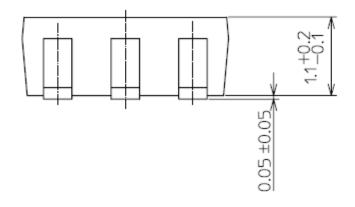


## 11. Package Information

SMV (SOT-25) (SC-74A)

Unit: mm





Weight: 16 mg (Typ.)



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