

Bipolar Transistors Silicon PNP Epitaxial Type

TDTA143Z

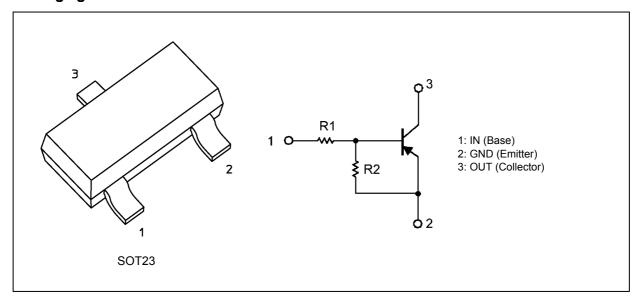
1. Applications

- · Switching
- · Inverter Circuits
- · Driver Circuits

2. Features

- (1) The integrated bias resistor reduces the number of external parts required, making it possible to reduce system size and assembly time.
- (2) Toshiba offers transistors with a wide range of resistance to accommodate various circuit designs.
- (3) Complementary to TDTC143Z

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) (Unless otherwise specified,, Ta = 25 °C)

| Characteristics | Symbol | Rating | Unit |
|----------------------|------------------|------------|------|
| Supply voltage | V _{CC} | -50 | V |
| Output current | Io | -100 | mA |
| Power dissipation | P _D | 320 | mW |
| Junction temperature | Tj | 150 | °C |
| Storage temperature | T _{stg} | -55 to 150 | °C |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

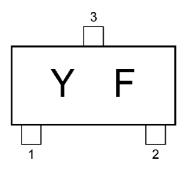
Start of commercial production



5. Electrical Characteristics (Unless otherwise specified, T_a = 25 °C)

| Characteristics | Symbol | Note | Test Condition | Min | Тур. | Max | Unit |
|----------------------|--------------------------------|------|--|------|------|--------|------|
| Input voltage (off) | $V_{I(off)}$ | | V_{CC} = -5 V, I_{O} = -0.1 mA | _ | _ | -0.5 | V |
| Input voltage (on) | $V_{I(on)}$ | | $V_{\rm O}$ = -0.3 V, $I_{\rm O}$ = -5 mA | -1.3 | _ | _ | V |
| Output voltage | V _{O(on)} | | I_{O} = -10 mA, I_{I} = -0.5 mA | _ | -0.1 | -0.3 | V |
| Input bias current | II | | V _I = -5 V | _ | _ | -0.138 | mA |
| Output current | I _{O(off)} | | V _{CC} = -50 V, V _I = 0 V | _ | _ | -500 | nA |
| DC current gain | G _I | | $V_O = -5 \text{ V}, I_O = -10 \text{ mA}$ | 80 | _ | _ | _ |
| Input resistance | R ₁ | | _ | 3.29 | 4.7 | 6.11 | kΩ |
| Resistance ratio | R ₂ /R ₁ | | _ | 8 | 10 | 12 | _ |
| Transition frequency | f _T | | $V_{CE} = -10 \text{ V}, I_{E} = 5 \text{ mA},$ f = 100 MHz | _ | 250 | _ | MHz |

6. Marking





7. Characteristics Curves (Note)

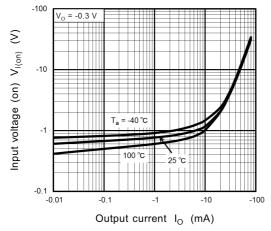


Fig. 7.1 V_{I(on)} - I_O

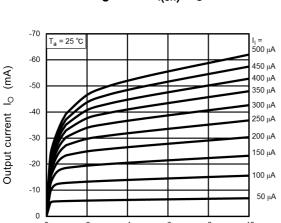


Fig. 7.3 I_O - V_O

Output voltage V_O (V)

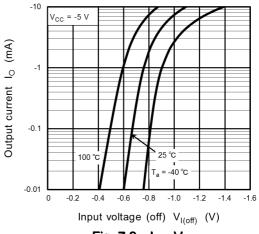


Fig. 7.2 I_O - V_{I(off)}

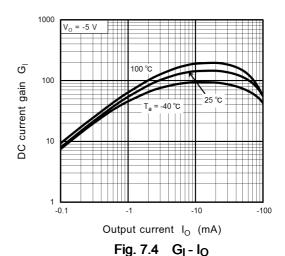


Fig. 7.5 $V_{O(on)}$ - I_O

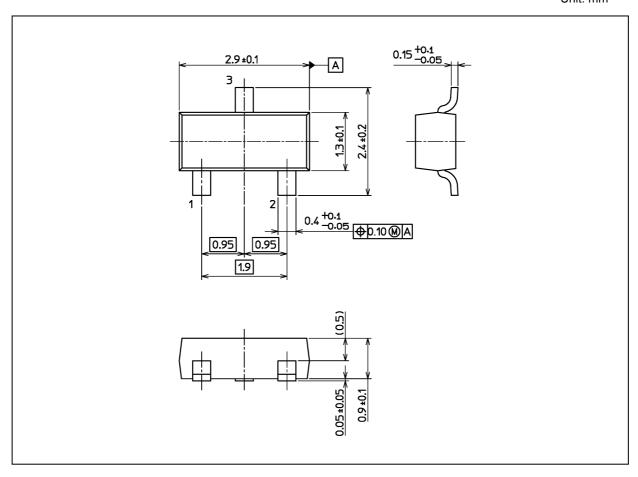
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Rev.2.0



Package Dimensions

Unit: mm



Weight: 9 mg (typ.)

| | Package Name(s) |
|------------------|-----------------|
| TOSHIBA: 2-3AB1A | |
| Nickname: SOT23 | |



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