

Bipolar Transistors Silicon NPN Epitaxial Type

# TDTC114E

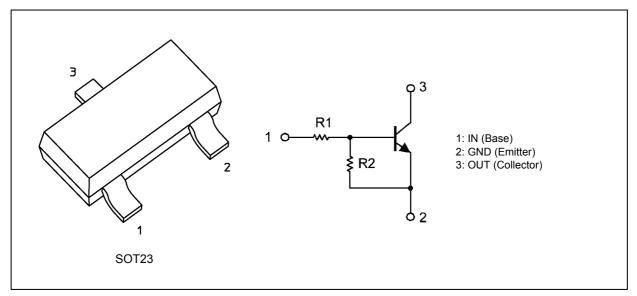
#### 1. Applications

- · Switching
- · Inverter Circuits
- · Driver Circuits

#### 2. Features

- (1) The integrated bias resistor reduces the number of external parts required, making it possible to reduce system size and assembly time.
- (2) Toshiba offers transistors with a wide range of resistance to accommodate various circuit designs.
- (3) Complementary to TDTA114E

#### 3. Packaging and Internal Circuit



## 4. Absolute Maximum Ratings (Note) (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	50	V
Output current	Io	100	mA
Power dissipation	P <sub>D</sub>	320	mW
Junction temperature	Tj	150	°C
Storage temperature	T <sub>stg</sub>	-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

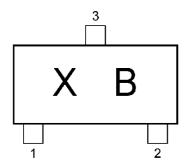
Start of commercial production



## 5. Electrical Characteristics (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Note	Test Condition	Min	Тур.	Max	Unit
Input voltage (off)	$V_{I(off)}$		V <sub>CC</sub> = 5 V, I <sub>O</sub> = 0.1 mA	_	_	1.0	V
Input voltage (on)	$V_{I(on)}$		V <sub>O</sub> = 0.3 V, I <sub>O</sub> = 10 mA	3.4		_	V
Output voltage	V <sub>O(on)</sub>		I <sub>O</sub> = 10 mA, I <sub>I</sub> = 0.5 mA	_	0.1	0.3	V
Input bias current	l <sub>l</sub>		V <sub>I</sub> = 5 V	_	_	0.36	mA
Output current	I <sub>O(off)</sub>		V <sub>CC</sub> = 50 V, V <sub>I</sub> = 0 V	_	_	500	nA
DC current gain	G <sub>l</sub>		V <sub>O</sub> = 5 V, I <sub>O</sub> = 5 mA	30	_	_	_
Input resistance	R <sub>1</sub>		_	7	10	13	kΩ
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>		_	0.9	1.0	1.1	_
Transition frequency	f <sub>T</sub>		V <sub>CE</sub> = 10 V, I <sub>E</sub> = -5 mA, f = 1MHz		250	_	MHz

## 6. Marking





### 7. Characteristics Curves (Note)

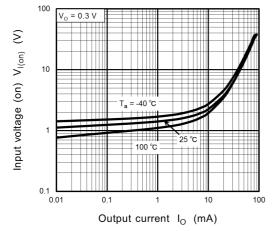
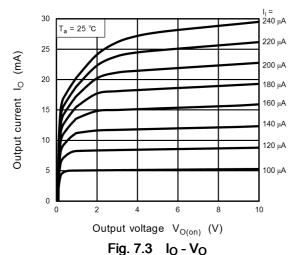
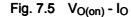


Fig. 7.1 V<sub>I(on)</sub> - I<sub>O</sub>





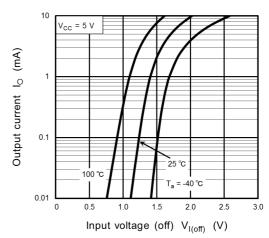
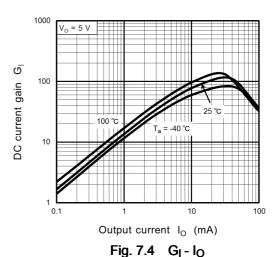
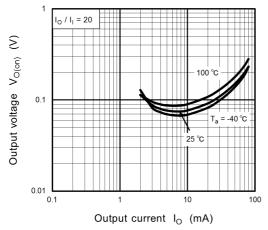


Fig. 7.2 Io - V<sub>I(off)</sub>





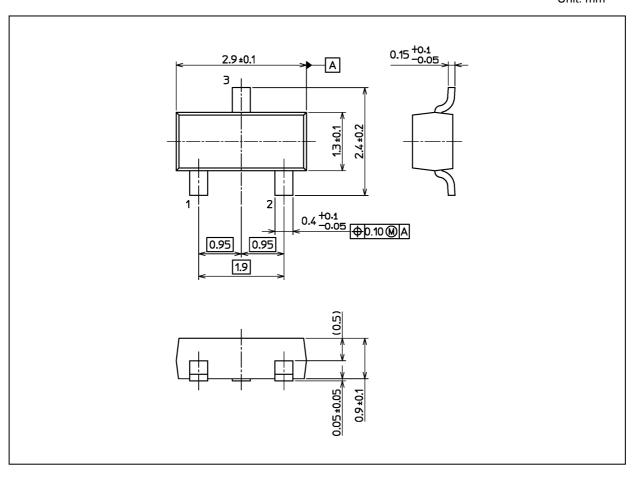
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Rev.3.0



### **Package Dimensions**

Unit: mm



Weight: 9 mg (typ.)

Package Name(s)				
Nickname: SOT23				



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