TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSVII)

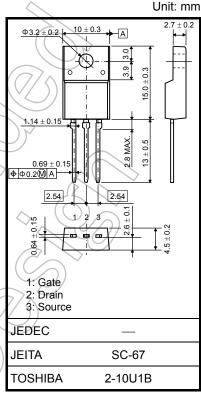
# TK4A60DB

#### **Switching Regulator Applications**

- Low drain-source ON-resistance: RDS (ON) = 1.6  $\Omega$ (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 2.2 \text{ S (typ.)}$
- Low leakage current:  $I_{DSS} = 10 \mu A (max) (V_{DS} = 600V)$
- Enhancement mode:  $V_{th} = 2.4 \text{ to } 4.4 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$

## Absolute Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	600	V
Gate-source voltage		$V_{GSS}$	±30	A
Drain current	DC (Note 1)	ΙD	3.7	A
	Pulse (Note 1)	I <sub>DP</sub>	14.8	A
Drain power dissipation	on (Tc = 25°C)	$P_{D}$	35	W
Single pulse avalanch	ne energy (Note 2)	E <sub>AS</sub>	173	mJ
Avalanche current		IAR	3.7	A
Repetitive avalanche	energy (Note 3)	EAR	3.5	mJ
Channel temperature		(T <sub>ch</sub>	150	)\°C
Storage temperature	range	T <sub>stg</sub>	-55 to 150	Ç

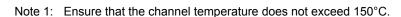


Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

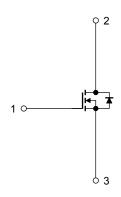
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	3.57	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W



Note 2:  $V_{DD} = 90 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}(\text{initial})$ , L = 22 mH,  $R_G = 25 \Omega$ ,  $I_{AR} = 3.7 \text{ A}$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



Start of commercial production 2009-04

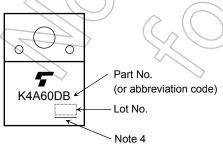
#### **Electrical Characteristics (Ta = 25°C)**

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μΑ	
Drain cut-off curre	ent	I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	_	_	10	μΑ	
Drain-source brea	akdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	600	_		V	
Gate threshold vo	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.4	_	4.4	V	
Drain-source ON	resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1.9 A	(F	) 1.6	2.0	Ω	
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1.9A	0.6	2.2		S	
Input capacitance		C <sub>iss</sub>		()	540	_		
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	3	_	pF	
Output capacitance		C <sub>oss</sub>		7 —	60	_		
Switching time	Rise time	t <sub>r</sub>	10 V ID = 1.9 A VOUT —		18	<i>//</i>		
	Turn-on time	t <sub>on</sub>	0 V	-6	40	> —		
	Fall time	t <sub>f</sub>	V <sub>DD</sub> ≈ 200 V			) _	ns	
	Turn-off time	t <sub>off</sub>	Duty ≤ 1%, t <sub>W</sub> = 10 μs	(A)	55	_		
Total gate charge Qg		Qg			11	_		
Gate-source charge Q <sub>gs</sub>		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3.7 \text{ A}$	) —	6	_	nC	
Gate-drain charge Q <sub>gd</sub>			_	5	_			

### Source-Drain Ratings and Characteristics (Ta = 25°C)

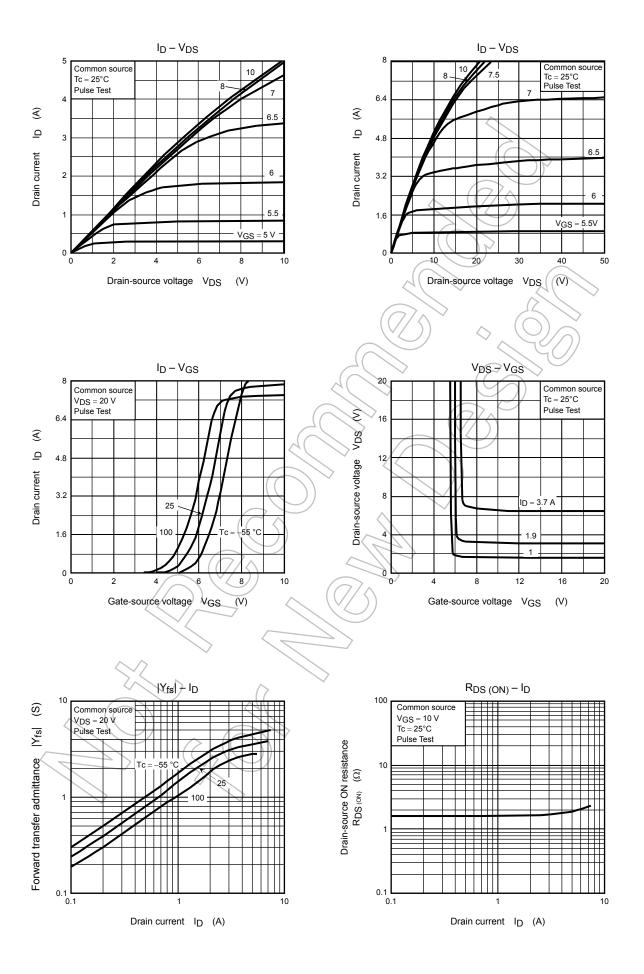
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note/1)	IDR		_	_	3.7	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	(7/\\ -	_	_	14.8	Α
Forward voltage (diode)	V <sub>DSF</sub>	$I_{DR} = 3.7 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse recovery time	trr	$I_{DR} = 3.7 \text{ A}, V_{GS} = 0 \text{ V},$	_	1000	_	ns
Reverse recovery charge	Qrr	dI <sub>DR</sub> /dt = 100 A/μs		5.5	_	μС

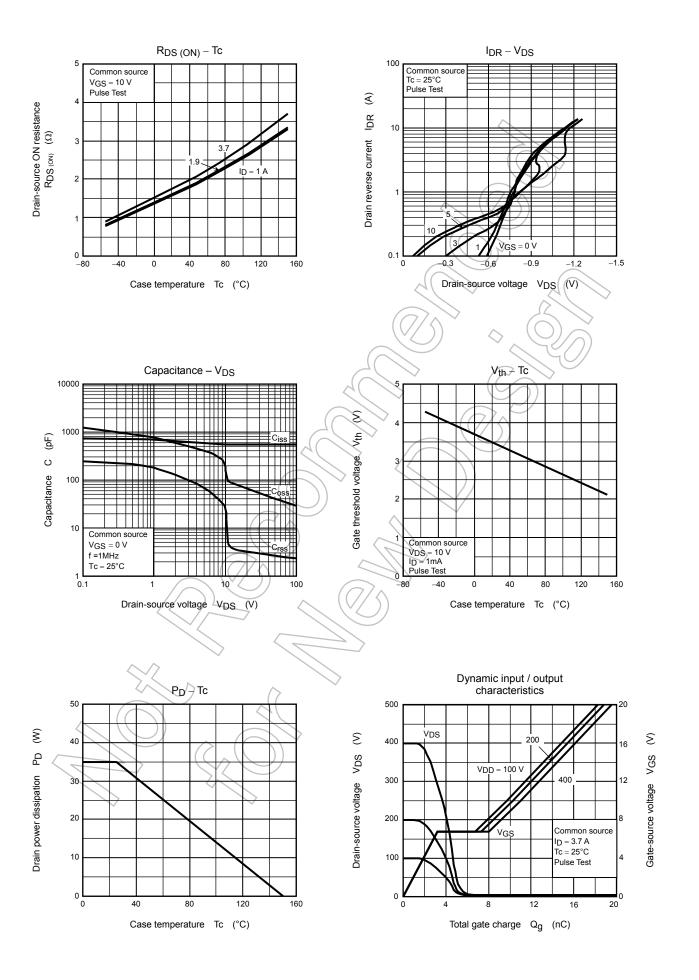
### Marking

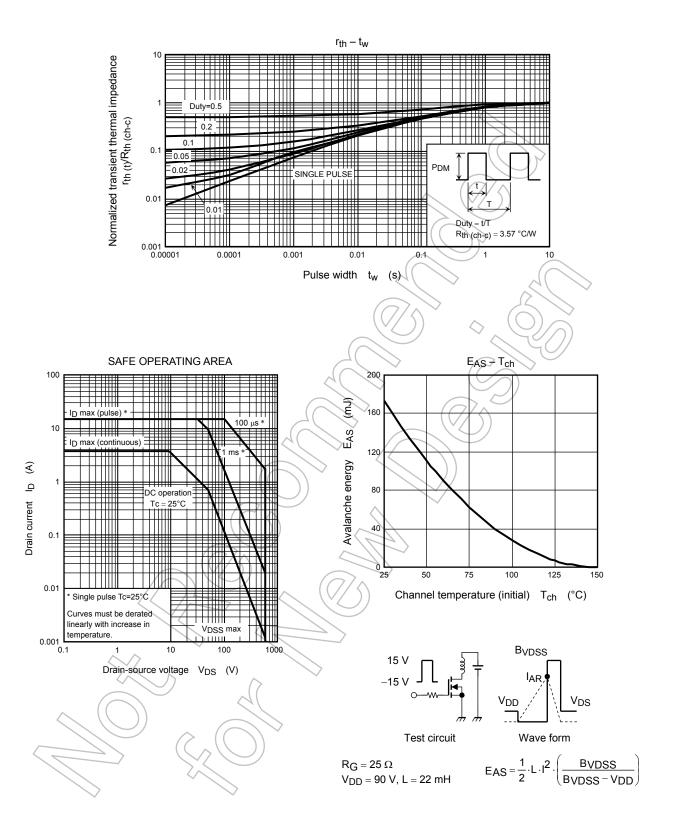


Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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