MOSFETs Silicon N-Channel MOS ( $\pi$ -MOSIX)

# TK650A60F

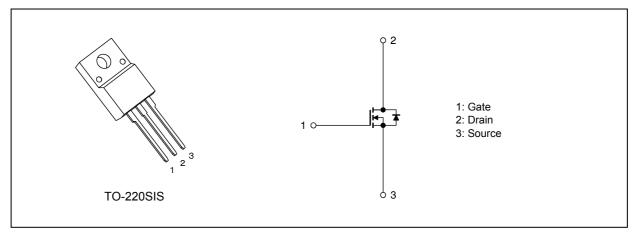
#### 1. Applications

• Switching Power Supplies

#### 2. Features

- (1) Easy to control Gate switching
- (2)Low drain-source on-resistance:  $R_{DS(ON)} = 0.54 \Omega$  (typ.)
- (3)Enhancement mode:  $V_{th} = 2$  to  $4 \text{ V} (V_{DS} = 10 \text{ V}, I_D = 1.16 \text{ mA})$

#### 3. Packaging and Internal Circuit



### 4. Absolute Maximum Ratings (Note) (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	600	V
Gate-source voltage		V <sub>GSS</sub>	±30	1
Drain current (DC)	(Note 1)	Ι <sub>D</sub>	11	A
Drain current (pulsed)	(Note 1)	I <sub>DP</sub>	44	1
Power dissipation $(T_c = 2)$	25 °C)	PD	45	W
Single-pulse avalanche energy	(Note 2)	E <sub>AS</sub>	224	mJ
Single-pulse avalanche current		I <sub>AS</sub>	11	A
Reverse drain current (DC)	(Note 1)	I <sub>DR</sub>	11	1
Reverse drain current (pulsed)	(Note 1)	I <sub>DRP</sub>	44	
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature		T <sub>stg</sub>	-55 to 150	1
Isolation voltage (RMS) (t = 1	.0 s)	V <sub>ISO(RMS)</sub>	2000	V
Mounting torque		TOR	0.6	N · m

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

> Start of commercial production 2017-12 2018-09-25

#### 5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R <sub>th(ch-c)</sub>	2.77	°C/W
Channel-to-ambient thermal resistance	R <sub>th(ch-a)</sub>	62.5	

Note 1: Ensure that the channel temperature does not exceed 150 °C. Note 2: V\_{DD} = 90 V, T\_{ch} = 25 °C (initial), L = 3.25 mH, I\_{AS} = 11 A

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

### 6. Electrical Characteristics

### 6.1. Static Characteristics (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±30 V, $V_{DS}$ = 0 V	_	_	±1	μA
Drain cut-off current	I <sub>DSS</sub>	$V_{DS}$ = 600 V, $V_{GS}$ = 0 V	_	_	10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	600	_	_	V
Gate threshold voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1.16 mA	2	_	4	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5.5 A	_	0.54	0.65	Ω

### 6.2. Dynamic Characteristics (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	$V_{DS}$ = 300 V, $V_{GS}$ = 0 V, f = 100 kHz	_	1320	_	pF
Reverse transfer capacitance	C <sub>rss</sub>		_	10	_	
Output capacitance	C <sub>oss</sub>		_	47		
Gate resistance	r <sub>g</sub>	V <sub>DS</sub> = OPEN , f = 1 MHz	_	6.2	_	Ω
Switching time (rise time)	tr	See Figure 6.2.1	_	25	_	ns
Switching time (turn-on time)	t <sub>on</sub>		_	55	_	
Switching time (fall time)	t <sub>f</sub>		_	22	_	
Switching time (turn-off time)	t <sub>off</sub>	]	_	85	_	
MOSFET dv/dt ruggedness	dv/dt	$V_{DS} \leq V_{(BR)DSS}, \ I_D \leq 5.5 \ A$	15		_	V/ns

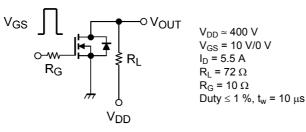


Fig. 6.2.1 Switching Time Test Circuit

### 6.3. Gate Charge Characteristics ( $T_a = 25$ °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS}$ = 10 V, I <sub>D</sub> = 11 A	—	34	—	nC
Gate-source charge 1	Q <sub>gs1</sub>		_	9.5	_	
Gate-drain charge	Q <sub>gd</sub>			15.5		

### 6.4. Source-Drain Characteristics (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Diode forward voltage	V <sub>DSF</sub>	I <sub>DR</sub> = 11 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time		V <sub>DD</sub> = 400 V,	_	600	_	ns
Reverse recovery charge	Q <sub>rr</sub>	I <sub>DR</sub> = 11 A, V <sub>GS</sub> = 0 V -dI <sub>DR</sub> /dt = 100 A/μs	_	3.8	_	μC
Peak reverse recovery current	l <sub>rr</sub>		_	12.5	_	А
Diode dv/dt ruggedness	dv/dt	$V_{DD} \leq 400~V,~I_{DR} \leq 11~A,~V_{GS}~$ = 0 $V$	5		_	V/ns

### 7. Marking (Note)

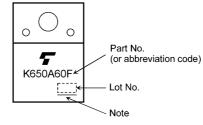
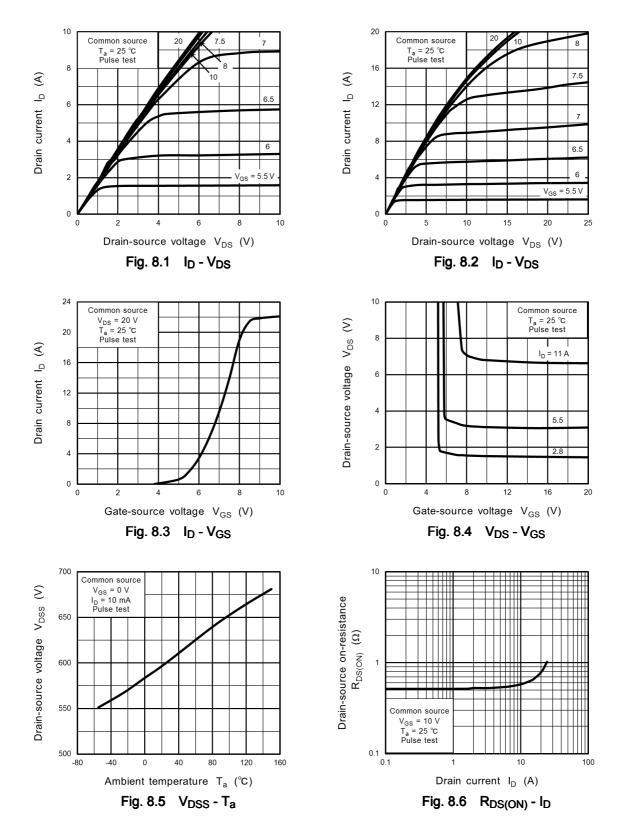
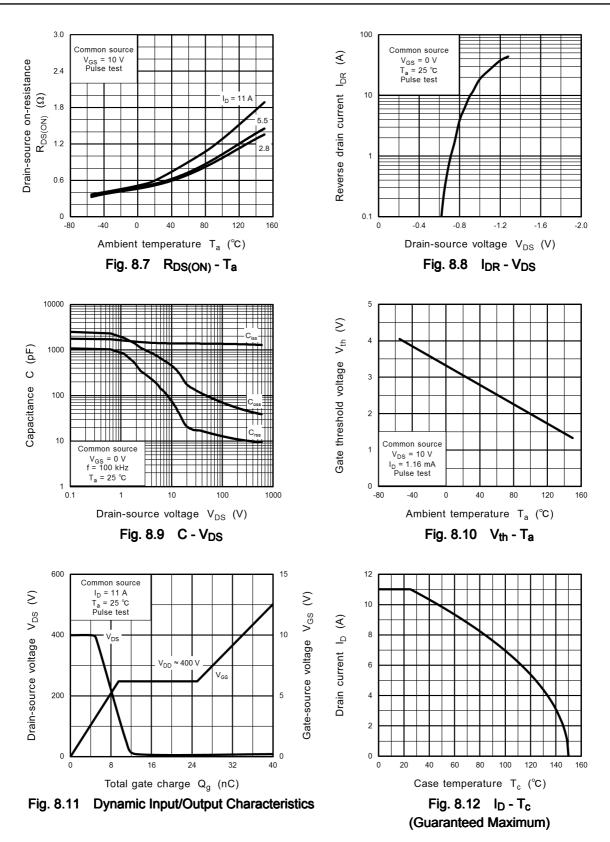


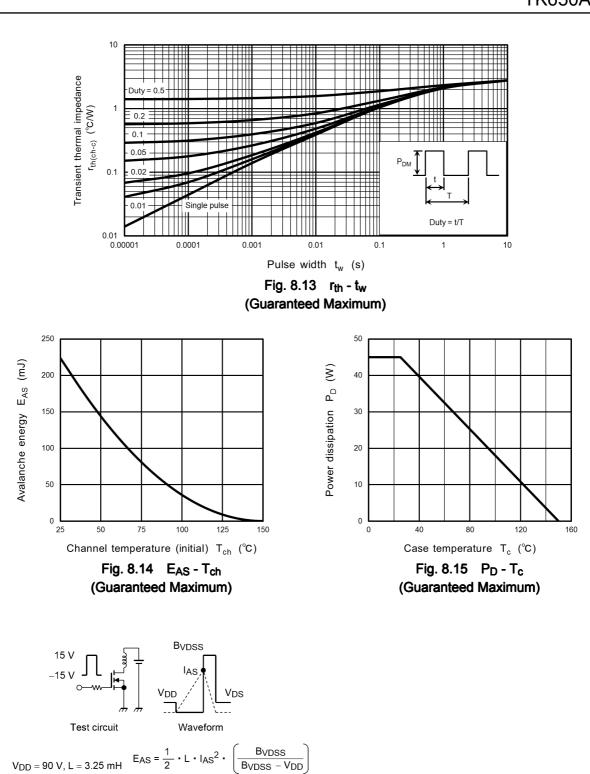
Fig. 7.1 Marking

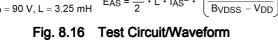
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### 8. Characteristics Curves (Note)

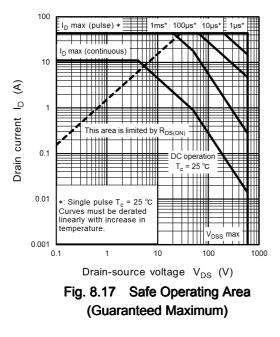










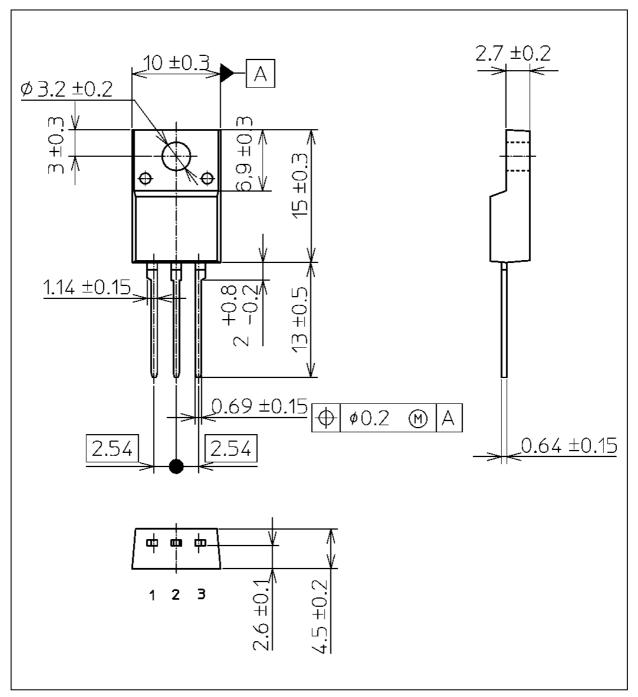


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

### TK650A60F

### Package Dimensions

Unit: mm



#### Weight: 1.7 g (typ.)

Package Name(s)	
JEITA: SC-67	
TOSHIBA: 2-10U1S	
Nickname: TO-220SIS	

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