MOSFETs Silicon N-Channel MOS (π -MOSIX)

TK650A60F

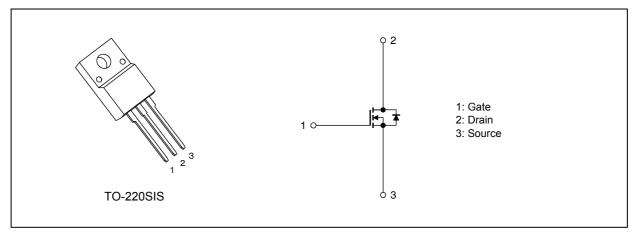
1. Applications

• Switching Power Supplies

2. Features

- (1) Easy to control Gate switching
- (2)Low drain-source on-resistance: $R_{DS(ON)} = 0.54 \Omega$ (typ.)
- (3)Enhancement mode: $V_{th} = 2$ to $4 \text{ V} (V_{DS} = 10 \text{ V}, I_D = 1.16 \text{ mA})$

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	600	V
Gate-source voltage		V _{GSS}	±30	1
Drain current (DC)	(Note 1)	Ι _D	11	A
Drain current (pulsed)	(Note 1)	I _{DP}	44	1
Power dissipation $(T_c = 2)$	25 °C)	PD	45	W
Single-pulse avalanche energy	(Note 2)	E _{AS}	224	mJ
Single-pulse avalanche current		I _{AS}	11	A
Reverse drain current (DC)	(Note 1)	I _{DR}	11	1
Reverse drain current (pulsed)	(Note 1)	I _{DRP}	44	
Channel temperature		T _{ch}	150	°C
Storage temperature		T _{stg}	-55 to 150	1
Isolation voltage (RMS) (t = 1	.0 s)	V _{ISO(RMS)}	2000	V
Mounting torque		TOR	0.6	N · m

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

> Start of commercial production 2017-12 2018-09-25

5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R _{th(ch-c)}	2.77	°C/W
Channel-to-ambient thermal resistance	R _{th(ch-a)}	62.5	

Note 1: Ensure that the channel temperature does not exceed 150 °C. Note 2: V_{DD} = 90 V, T_{ch} = 25 °C (initial), L = 3.25 mH, I_{AS} = 11 A

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

6. Electrical Characteristics

6.1. Static Characteristics (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V_{GS} = ±30 V, V_{DS} = 0 V	_	_	±1	μA
Drain cut-off current	I _{DSS}	V_{DS} = 600 V, V_{GS} = 0 V	_	_	10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	600	_	_	V
Gate threshold voltage	V _{th}	V _{DS} = 10 V, I _D = 1.16 mA	2	_	4	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 10 V, I _D = 5.5 A	_	0.54	0.65	Ω

6.2. Dynamic Characteristics (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V_{DS} = 300 V, V_{GS} = 0 V, f = 100 kHz	_	1320	_	pF
Reverse transfer capacitance	C _{rss}		_	10	_	
Output capacitance	C _{oss}		_	47		
Gate resistance	r _g	V _{DS} = OPEN , f = 1 MHz	_	6.2	_	Ω
Switching time (rise time)	tr	See Figure 6.2.1	_	25	_	ns
Switching time (turn-on time)	t _{on}		_	55	_	
Switching time (fall time)	t _f		_	22	_	
Switching time (turn-off time)	t _{off}]	_	85	_	
MOSFET dv/dt ruggedness	dv/dt	$V_{DS} \leq V_{(BR)DSS}, \ I_D \leq 5.5 \ A$	15		_	V/ns

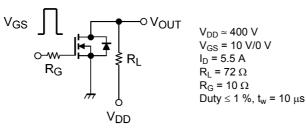


Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics ($T_a = 25$ °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS}$ = 10 V, I _D = 11 A	—	34	—	nC
Gate-source charge 1	Q _{gs1}		_	9.5	_	
Gate-drain charge	Q _{gd}			15.5		

6.4. Source-Drain Characteristics (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Diode forward voltage	V _{DSF}	I _{DR} = 11 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time		V _{DD} = 400 V,	_	600	_	ns
Reverse recovery charge	Q _{rr}	I _{DR} = 11 A, V _{GS} = 0 V -dI _{DR} /dt = 100 A/μs	_	3.8	_	μC
Peak reverse recovery current	l _{rr}		_	12.5	_	А
Diode dv/dt ruggedness	dv/dt	$V_{DD} \leq 400~V,~I_{DR} \leq 11~A,~V_{GS}~$ = 0 V	5		_	V/ns

7. Marking (Note)

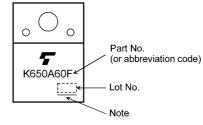
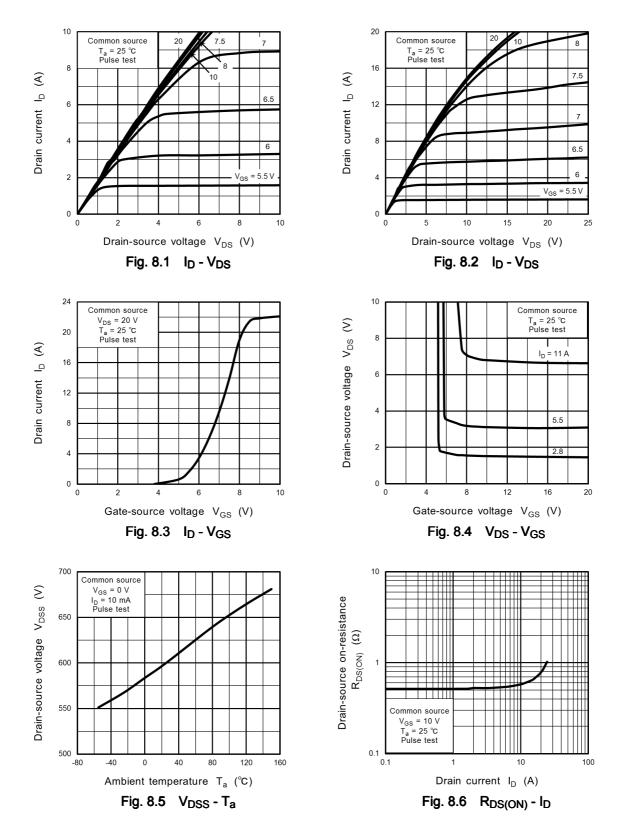
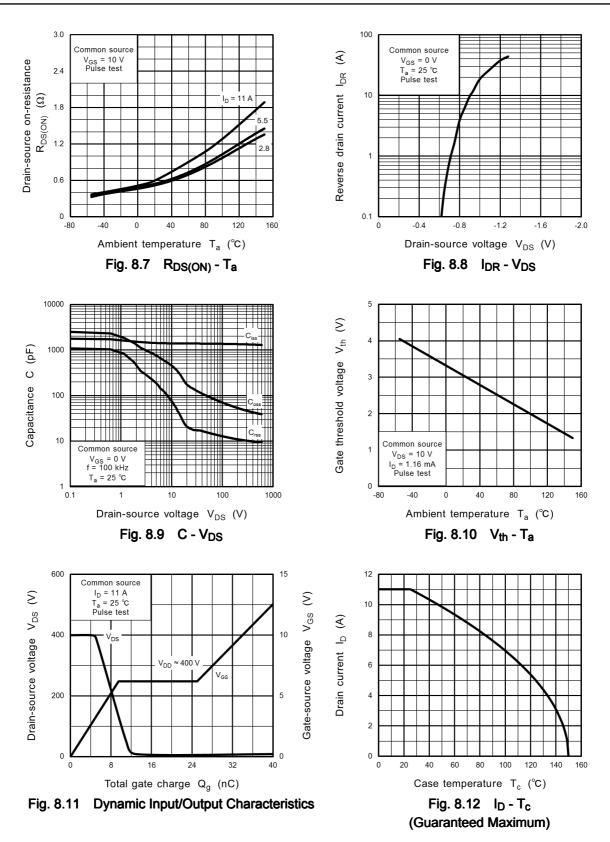


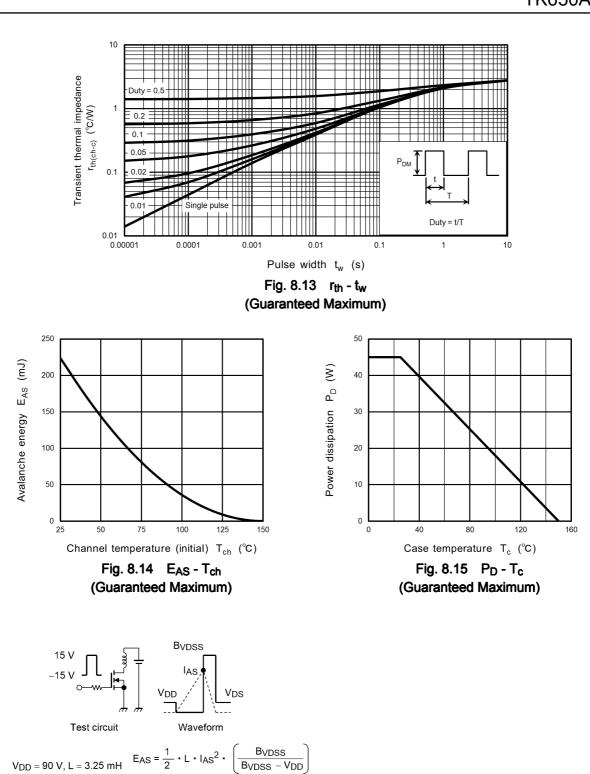
Fig. 7.1 Marking

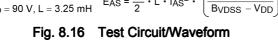
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8. Characteristics Curves (Note)

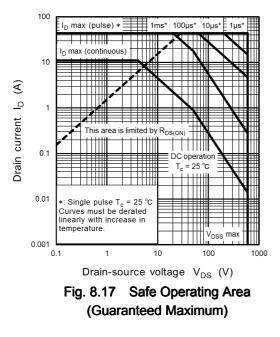










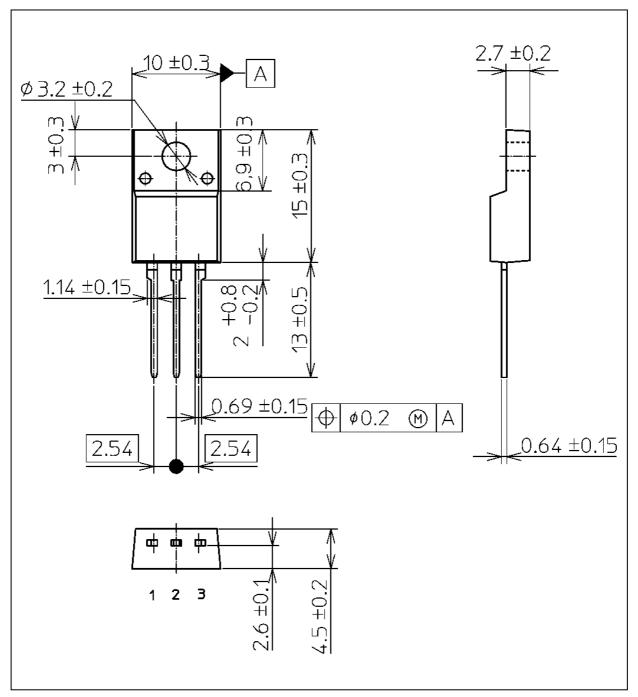


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

TK650A60F

Package Dimensions

Unit: mm



Weight: 1.7 g (typ.)

Package Name(s)	
JEITA: SC-67	
TOSHIBA: 2-10U1S	
Nickname: TO-220SIS	

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