MOSFETs Silicon N-Channel MOS (DTMOSIV)

# **TK7E80W**

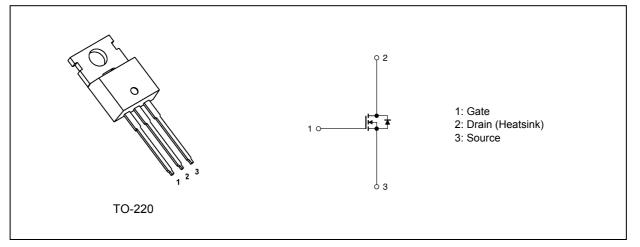
#### 1. Applications

Switching Voltage Regulators

#### 2. Features

- (1) Low drain-source on-resistance:  $R_{DS(ON)} = 0.795 \Omega$  (typ.) by using Super Junction Structure : DTMOS
- (2) Easy to control Gate switching
- (3) Enhancement mode:  $V_{th} = 3$  to  $4 V (V_{DS} = 10 V, I_D = 0.28 mA)$

#### 3. Packaging and Internal Circuit



### 4. Absolute Maximum Ratings (Note) (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Rating	Unit		
Drain-source voltage			V <sub>DSS</sub>	800	V
Gate-source voltage			V <sub>GSS</sub>	±20	1
Drain current (DC)		(Note 1)	Ι <sub>D</sub>	6.5	A
Drain current (pulsed)		(Note 1)	I <sub>DP</sub>	26	]
Power dissipation	(T <sub>c</sub> = 25°C)		PD	110	W
Single-pulse avalanche energy		(Note 2)	E <sub>AS</sub>	242	mJ
Single-pulse avalanche current			I <sub>AS</sub>	1.3	A
Reverse drain current (DC)		(Note 1)	I <sub>DR</sub>	6.5	]
Reverse drain current (pulsed)		(Note 1)	I <sub>DRP</sub>	26	]
Channel temperature			T <sub>ch</sub>	150	°C
Storage temperature			T <sub>stg</sub>	-55 to 150	1
Mounting torque			TOR	0.6	N · m

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production

#### 5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R <sub>th(ch-c)</sub>	1.13	°C/W
Channel-to-ambient thermal resistance	R <sub>th(ch-a)</sub>	83.3	

Note 1: Ensure that the channel temperature does not exceed 150 °C.

Note 2: V<sub>DD</sub> = 90 V, T<sub>ch</sub> = 25 °C (initial), L = 260 mH, R<sub>G</sub> = 25  $\Omega$ , I<sub>AS</sub> = 1.3 A

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

#### 6. Electrical Characteristics

### 6.1. Static Characteristics (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±20 V, $V_{DS}$ = 0 V	_	_	±1	μA
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 800 V, V <sub>GS</sub> = 0 V	_	_	10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	800	_	—	V
Gate threshold voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.28 mA	3	—	4	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3.3 A		0.795	0.95	Ω

### 6.2. Dynamic Characteristics (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 300 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	700	_	pF
Reverse transfer capacitance	C <sub>rss</sub>		_	1.2	—	
Output capacitance	C <sub>oss</sub>		_	20	_	
Effective output capacitance	C <sub>o(er)</sub>	V <sub>DS</sub> = 640 V, V <sub>GS</sub> = 0 V	_	22	_	
Gate resistance	r <sub>g</sub>	V <sub>DS</sub> = OPEN , f = 1 MHz	_	30	_	Ω
Switching time (rise time)	t <sub>r</sub>	See Figure 6.2.1	_	18	_	ns
Switching time (turn-on time)	t <sub>on</sub>		_	45	_	
Switching time (fall time)	t <sub>f</sub>		_	8	_	
Switching time (turn-off time)	t <sub>off</sub>	]	_	70	_	
MOSFET dv/dt ruggedness	dv/dt	V <sub>DS</sub> ≤V <sub>(BR)DSS</sub> , I <sub>D</sub> ≤6.5 A	50		_	V/ns

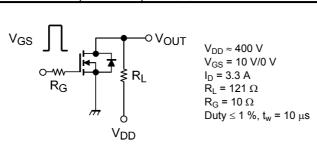


Fig. 6.2.1 Switching Time Test Circuit

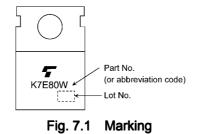
### 6.3. Gate Charge Characteristics ( $T_a = 25$ °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	V <sub>DD</sub> ≈640 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6.5 A		13	_	nC
Gate-source charge 1	Q <sub>gs1</sub>			4.5	_	
Gate-drain charge	Q <sub>gd</sub>		_	4	_	

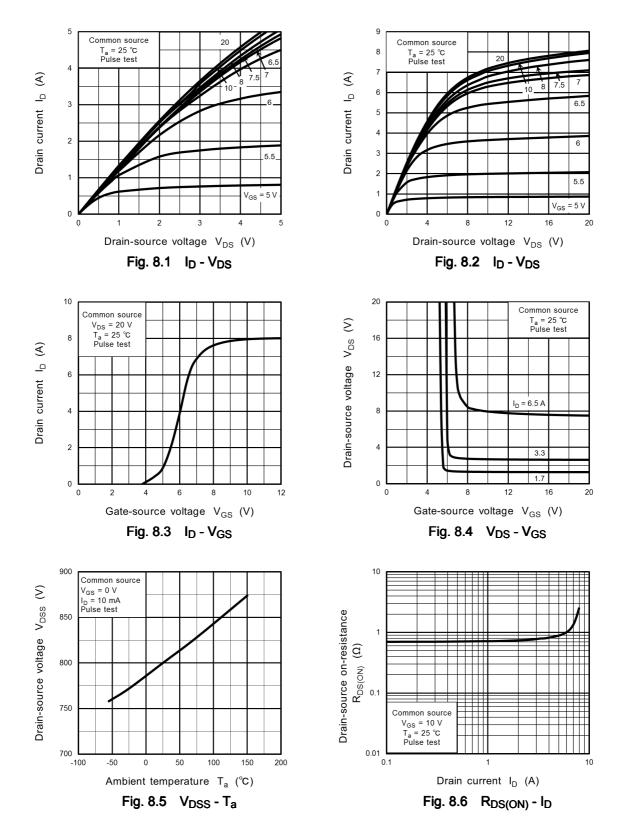
### 6.4. Source-Drain Characteristics ( $T_a = 25$ °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Diode forward voltage	V <sub>DSF</sub>	I <sub>DR</sub> = 6.5 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	V <sub>DD</sub> ≈640 V	_	330	_	ns
Reverse recovery charge	Q <sub>rr</sub>	I <sub>DR</sub> = 3.3 A, V <sub>GS</sub> = 0 V -dI <sub>DR</sub> /dt = 100 A/μs	_	2.9	_	μC
Peak reverse recovery current	l <sub>rr</sub>		_	16	_	А
Diode dv/dt ruggedness	dv/dt	$V_{DS} \le 640 \text{ V}, \text{ I}_{DR} = 3.3 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$	4.5			V/ns

7. Marking



### 8. Characteristics Curves (Note)



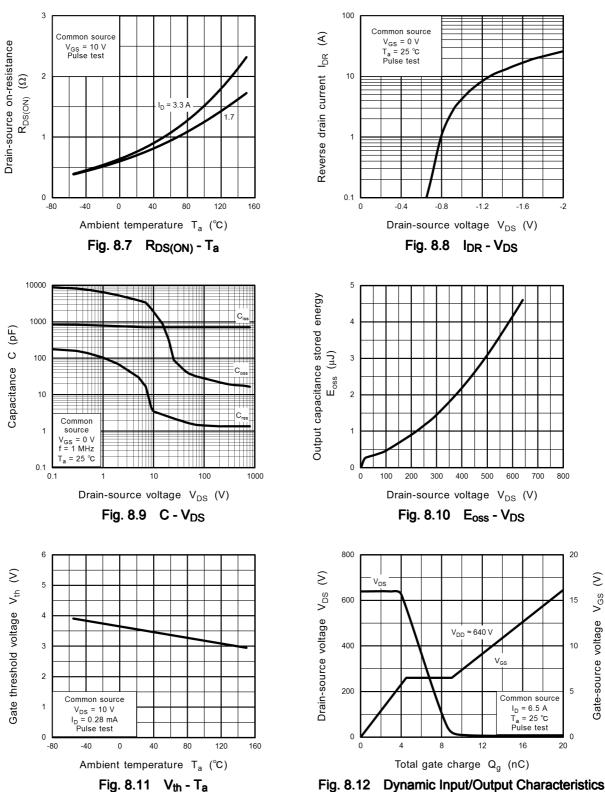


Fig. 8.12 Dynamic Input/Output Characteristics

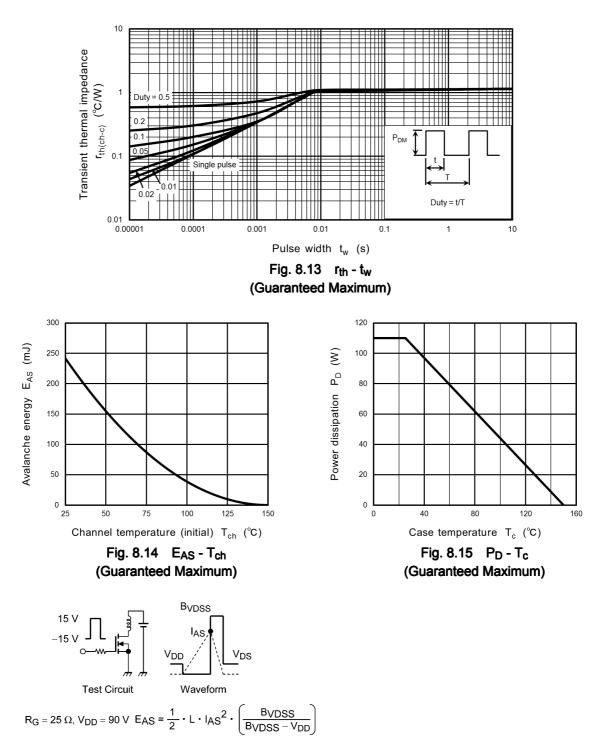
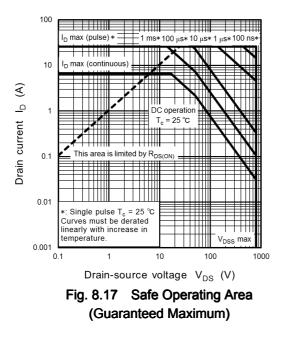


Fig. 8.16 Test Circuit/Waveform

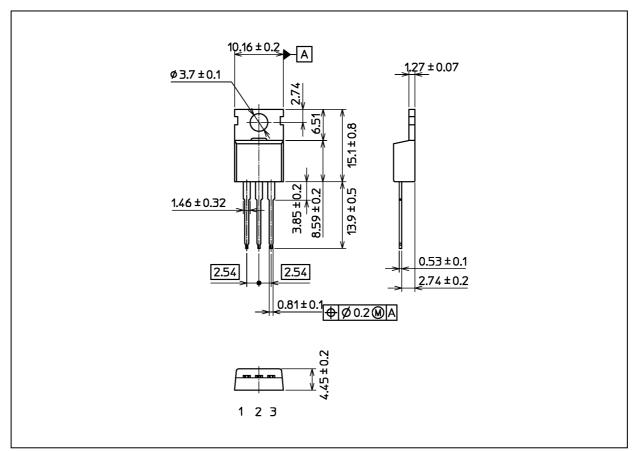


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

### TK7E80W

### Package Dimensions

Unit: mm



#### Weight: 1.93 g (typ.)

	Package Name(s)
TOSHIBA: 2-10X1A	
Nickname: TO-220	

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