TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSVII)

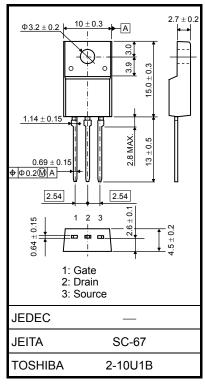
# TK8A50DA

#### Switching Regulator Applications

- Low drain-source ON-resistance:  $RDS(ON) = 0.76 \ \Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 4.1 \text{ S}$  (typ.)
- Low leakage current:  $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 500 \ V)$
- Enhancement-mode:  $V_{th} = 2.4$  to 4.4 V ( $V_{DS} = 10$  V,  $I_D = 1$  mA)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	500	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	7.5		
	Pulse (t = 1 ms) (Note 1)	I <sub>DP</sub>	30	A	
Drain power dissipation (Tc = 25°C)		PD	35	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	140	mJ	
Avalanche current		I <sub>AR</sub>	7.5	А	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	3.5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

#### Absolute Maximum Ratings (Ta = 25°C)



Weight : 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	3.57	°C/W	
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W	

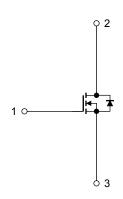
Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 4.2 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 7.5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.

#### Internal Connection



Start of commercial production 2009-07

Unit: mm

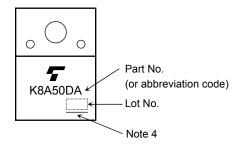
**Electrical Characteristics (Ta = 25°C)** 

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS}=\pm 30~V,~V_{DS}=0~V$	_		±1	μA
Drain cut-off current		I <sub>DSS</sub>	$V_{DS} = 500 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_		10	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	500			V
Gate threshold ve	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.4		4.4	V
Drain-source ON	-resistance	R <sub>DS (ON)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 3.8 \text{ A}$	_	0.76	1.04	Ω
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 3.8 \text{ A}$	1.0	4.1		S
Input capacitance		C <sub>iss</sub>		_	700		pF
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS}$ = 25 V, $V_{GS}$ = 0 V, f = 1 MHz	_	4		
Output capacitance		C <sub>oss</sub>			80		
Switching time	Rise time	tr	$\begin{array}{c} 10 \text{ V} \\ \text{V}_{GS} \\ 0 \text{ V} \\ 50 \Omega \\ \end{array} \begin{array}{c} \text{I}_{D} = 3.8 \text{ A} \text{ V}_{OUT} \\ \text{V}_{OUT} \\ \text{V}_{DD} \approx 200 \text{ V} \\ \text{V}_{DD} \approx 200 \text{ V} \\ \end{array}$		20		ns
	Turn-on time	t <sub>on</sub>			40		
	Fall time	t <sub>f</sub>			11		
	Turn-off time	t <sub>off</sub>			60		
Total gate charge		Qg		_	16	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD}\approx 400$ V, $V_{GS}=10$ V, $I_{D}=7.5$ A		10		nC
Gate-drain charge		Q <sub>gd</sub>			6	_	

#### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	7.5	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	—	_		30	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 7.5 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 7.5 \text{ A}, V_{GS} = 0 \text{ V},$	_	1200	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μμs	_	8.5	_	μC

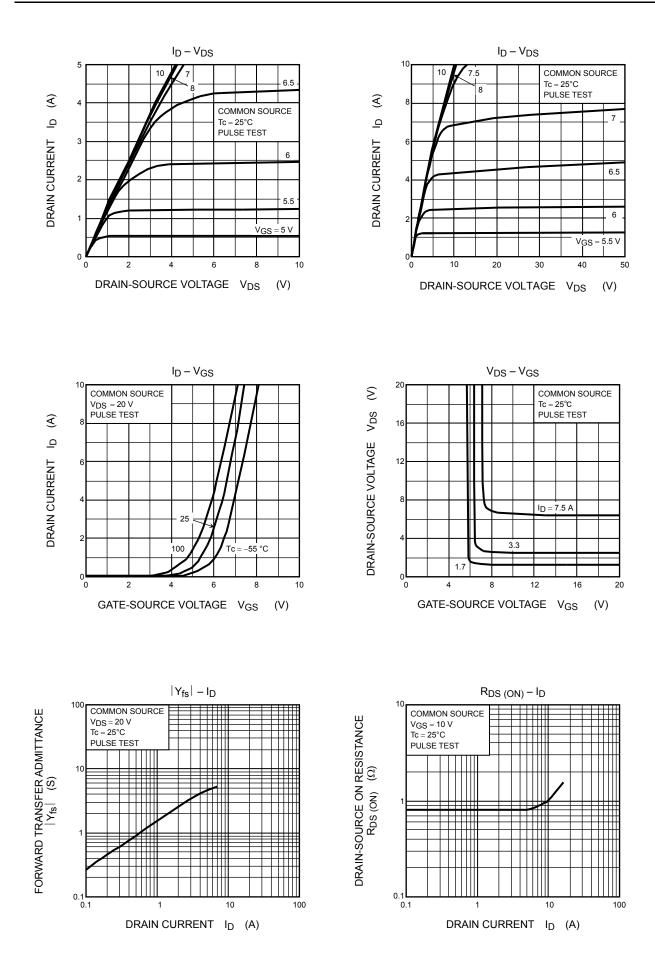
## Marking



Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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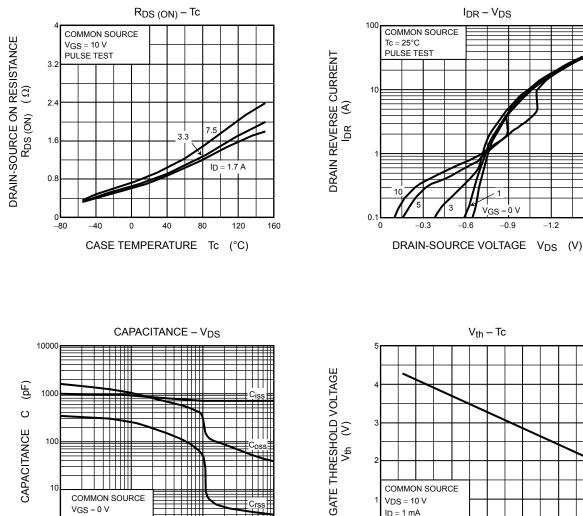
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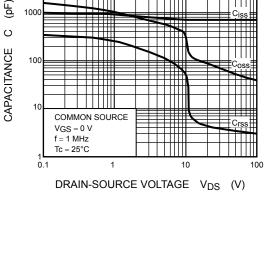


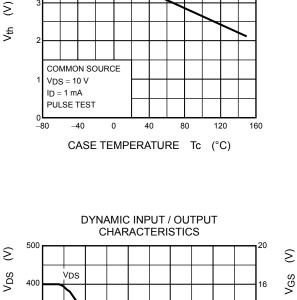
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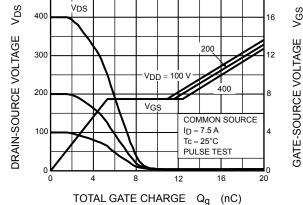
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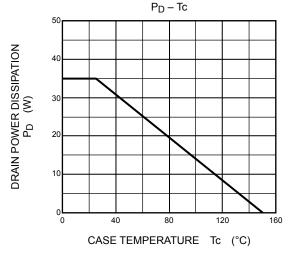
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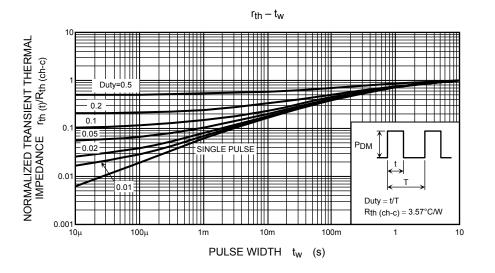




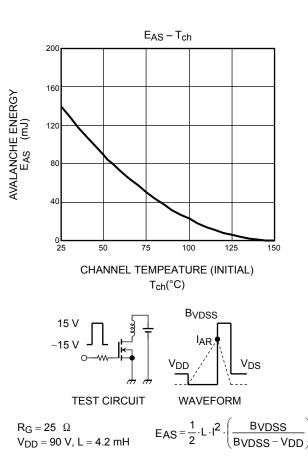








SAFE OPERATING AREA 100 (pulsed) nax (continu 100 µs 10 E <u>\_</u> DC operation Tc = 25°C DRAIN CURRENT 0.1 \*: Single nonrepetitive pulse Tc = 25°C 0.01 Curves must be derated linearly with increase in temperature. VDSS max 0.001 0.1 1 10 100 1000 DRAIN-SOURCE VOLTAGE VDS (V)



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