### TOSHIBA PHOTOCOUPLER GaAlAs IRED & PHOTO-IC

# **TLP104**

### IPM (Intelligent Power Module) Industrial Inverter Operate at high ambient temperatures up to 125°C

The Toshiba TLP104 consists of GaAlAs infrared light emitting diodes and integrated high gain, high-speed photodetectors. The TLP104 is housed in the SO6 package. The output stage is an open collector type.

The photodetector has an internal Faraday shield that provides a guaranteed common-mode transient immunity of ±15 kV/µs. TLP104 guarantees minimum and maximum of propagation delay time, switching speed dispersion, and high common mode transient immunity. Therefore TLP104 is suitable for isolation interface between IPM (Intelligent Power Module) in motor control application.

- Inverter logic type (Open collector output)
- Package type: SO6
- Guaranteed performance over temperature: -40 to 125°C
- Power supply voltage: -0.5 to 30 V
- Threshold Input Current: I<sub>FHL</sub> = 5.0 mA (max)
- Propagation delay time (t<sub>pHL</sub>/t<sub>pLH</sub>): t<sub>pHL</sub> = 400ns (max)
   t<sub>pLH</sub> = 550ns (max)
- Switching Time Dispersion(|t<sub>pHL</sub>-t<sub>pLH</sub>|): 400ns (max)
- Common mode transient immunity : ±15kV/μs (min)
- Isolation voltage : 3750Vrms (min)
- UL approved : UL1577, File No.E67349
- cUL approved :CSA Component Acceptance Service
   No. 5A, File No.E67349
- Option (V4) VDE approved : DIN EN60747-5-5 EN60065 EN60950-1 (Note 1) EN62368-1(Pending)
  - CQC-approved: GB4943.1, GB8898 Thailand Factory

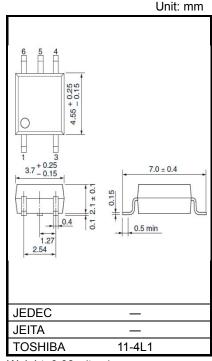


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Note1 : When a EN60747-5-5 approved type is needed, Please designate "Option(V4)"

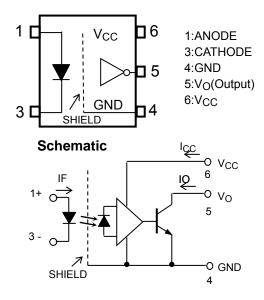
#### **Truth Table**

Input	LED	Output
Н	ON	L
L	OFF	Н



Weight: 0.08 g(typ.)

### Pin Configuration (Top View)



#### **Construction Mechanical Ratings**

Creepage distance	5.0 mm (min)
Clearance distance	5.0 mm (min)
Insulation thickness	0.4 mm (min)

Start of commercial production 2009-10 2017-05-25

Absolute Maximum Ratings (Ta = 25°C)

	Characteristic		Symbol	Rating	Unit
	Forward Current	١ <sub>F</sub>	25	mA	
	Forward Current Derating (Ta ≥ 110°C)		∆l <sub>F</sub> /°C	-0.67	mA/°C
	Pulse Forward Current (N	ote 1)	IFP	50	mA
LED	Pulse Forward Current Derating (Ta ≥ 110°C)		ΔI <sub>FP</sub> /°C	-1.34	mA/°C
	Reverse Voltage		V <sub>R</sub>	5	V
	Input Power Dissipation		PD	40	mW
	Input power Dissipation Derating (Ta $\ge$ 110°C)		∆P <sub>D</sub> /°C	-1.0	mW/°C
	Output Current (Ta $\leq$ 125°C)		IO	8	mA
or	Output Voltage		VO	-0.5 to 30	V
Detector	Supply Voltage		V <sub>CC</sub>	-0.5 to 30	V
ă	Output Power Dissipation		PO	80	mW
	Output Power Dissipation Derating (Ta $\geq$ 110°C)		∆P <sub>O</sub> /°C	-2.0	mW/°C
Oper	ating Temperature Range		T <sub>opr</sub>	-40 to 125	°C
Stora	ge Temperature Range		T <sub>stg</sub>	-55 to 125	°C
Lead	Lead Soldering Temperature (10 s)			260	°C
Isola	tion Voltage (AC,60 s, R.H. $\leq$ 60%,Ta=25°C) (N	ote 2)	BVs	3750	V <sub>rms</sub>

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 1: Pulse width  $\leq 10\mu s$ , duty=10%.

Note 2: This device is regarded as a two terminal device: pins 1 and 3 are shorted together, as are pins 4, 5 and 6.

Characteristic	Symbol	Min	Тур.	Max	Unit
Input Current , High Level	IFHL	7.5	-	15	mA
Input Voltage , Low Level	V <sub>FLH</sub>	0	-	0.8	V
Supply Voltage*	VCC	4.5	-	30	V
Operating Temperature	T <sub>opr</sub>	-40	-	125	°C

### **Recommended Operating Conditions**

\* This item denotes operating range, not meaning of recommended operating conditions.

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

# Electrical Characteristics (Unless otherwise specified, Ta = -40 to 125°C, V<sub>CC</sub> =4.5 to 30V)

	Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Мах	Unit
	Forward voltage	VF	_	I <sub>F</sub> = 10 mA, Ta=25°C	1.45	1.61	1.85	V
	Forward voltage temperature coefficient	$\Delta V_{F} / \Delta Ta$		I <sub>F</sub> = 10 mA	_	-1.8		mV /°C
LED	Reverse current	I <sub>R</sub>	—	V <sub>R</sub> = 5 V, Ta=25°C	_	_	10	μA
	Capacitance between terminals	CT	_	V = 0V, f = 1 MHz	_	60	_	pF
	High level output current	I <sub>OH</sub>	1	$V_{\rm F}$ = 0.8 V, $V_{\rm O}$ < $V_{\rm CC}$	_	_	50	μA
<u>ب</u>	Low level output voltage	V <sub>OL</sub>	2	I <sub>F</sub> = 10 mA, I <sub>O</sub> = 2.4mA	_	0.2	0.6	V
Detector	Low level supply current	ICCL	3	I <sub>F</sub> = 10 mA	-		1.3	mA
Dei	High level supply current	ICCH	4	I <sub>F</sub> = 0 mA	_		1.3	mA
	Output current	Ι <sub>Ο</sub>	_	I <sub>F</sub> = 10 mA, V <sub>O</sub> = 0.6V	4.0		_	mA
Inpu	ut current logic LOW output	IFHL	_	I <sub>O</sub> = 0.75mA, V <sub>O</sub> < 0.8 V	_	1.0	5	mA
Inpu	ut voltage logic HIGH output	V <sub>FLH</sub>	_	I <sub>O</sub> = 0.75mA, V <sub>O</sub> > 2.0 V	0.8	_	—	V

\*All typical values are at Ta=25°C, V<sub>CC</sub>=5 V unless otherwise specified

### Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Conditions	Min	Тур.	Max	Unit
Capacitance input to output	CS	V <sub>S</sub> = 0V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	R <sub>S</sub>	R.H. ≤ 60%, V <sub>S</sub> = 500 V	1×10 <sup>12</sup>	10 <sup>14</sup>		Ω
		AC,60 s	3750	_	_	V
Isolation voltage	BVS	AC,1 s, in oil	_	10000	_	V <sub>rms</sub>
		DC,60 s, in oil	_	10000	-	V <sub>dc</sub>

### Switching Characteristics (Unless otherwise specified, Ta = -40 to 125°C, V<sub>CC</sub>=15V)

Characteristic	Symbol	Test Circuit	Test Con	dition	Min	Тур.	Max	Unit
Propagation dology time (H-)	<b>+</b>			CL=100pF	30	150	400	
Propagation delay time $(H \rightarrow L)$	tpHL			CL=10pF	-	90	-	
Dranagation dology time (Last I)	H) $t_{pLH}$ I <sub>F</sub> = 10 mA, $C_L=10$	I <sub>F</sub> = 10 mA, 5 R <sub>L</sub> = 20kΩ (Note 1)	C <sub>L</sub> =100pF	150	350	550		
Propagation delay time $(L \rightarrow H)$			CL=10pF	_	100	_		
Switching Time Dispersion between ON and OFF	t <sub>pHL</sub> _t <sub>pLH</sub>			C <sub>L</sub> =100pF	_	_	400	ns
Propagation Delay Skew (Note 2)	t <sub>pLH</sub> -t <sub>pHL</sub>				-50	_	450	
Common mode transient immunity at high output level	CMH	V <sub>CM</sub> = 1500 V <sub>p</sub> -p, I <sub>F</sub> R <sub>L</sub> = 20 kΩ, Ta=25°		I <sub>F</sub> = 0 mA °C	15	_	_	kV/μs
Common mode transient Immunity at low output level	CML	υ	V <sub>CM</sub> = 1500 V <sub>p−p,</sub> R <sub>L</sub> = 20 kΩ, Ta=25	I <sub>F</sub> = 10 mA °C	-15	_	_	kV/μs

\*All typical values are at Ta=25°C

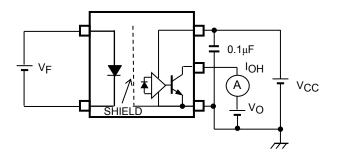
Note :A ceramic capacitor (0.1 μF) should be connected from pin 6 (V<sub>CC</sub>) to pin 4 (GND) to stabilize the operation of<br/>the high gain linear amplifier. Failure to provide the bypass may impair the switching property.<br/>The total lead length between capacitor and coupler should not exceed 1 cm.

- Note 1: f = 10kHz, duty=10%, input current tr = tf = 5ns
- Note 2: Propagation delay skew is defined as the propagation delay time of the largest or smallest t<sub>pLH</sub> minus the largest or smallest t<sub>pHL</sub> of multiple samples. Evaluations of these samples are conducted under identical test conditions (supply voltage, input current, temperature, etc.).

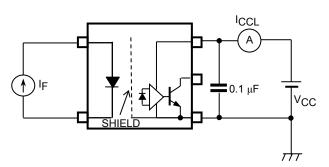
^) I<sub>F</sub>

### **TEST CIRCUIT 1: IOH**

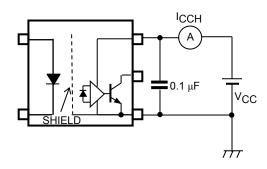
### TEST CIRCUIT 2: VOL



**TEST CIRCUIT 3: ICCL** 



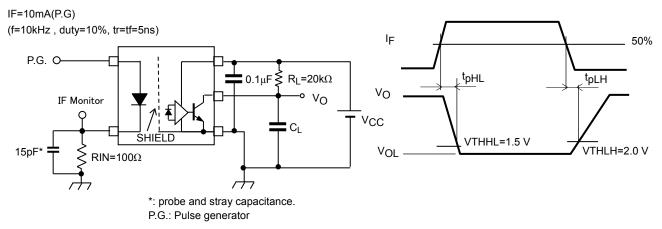


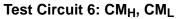


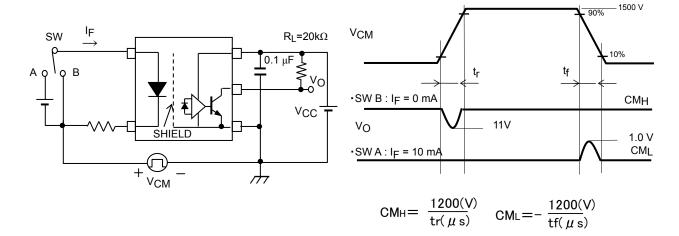
0.1µF

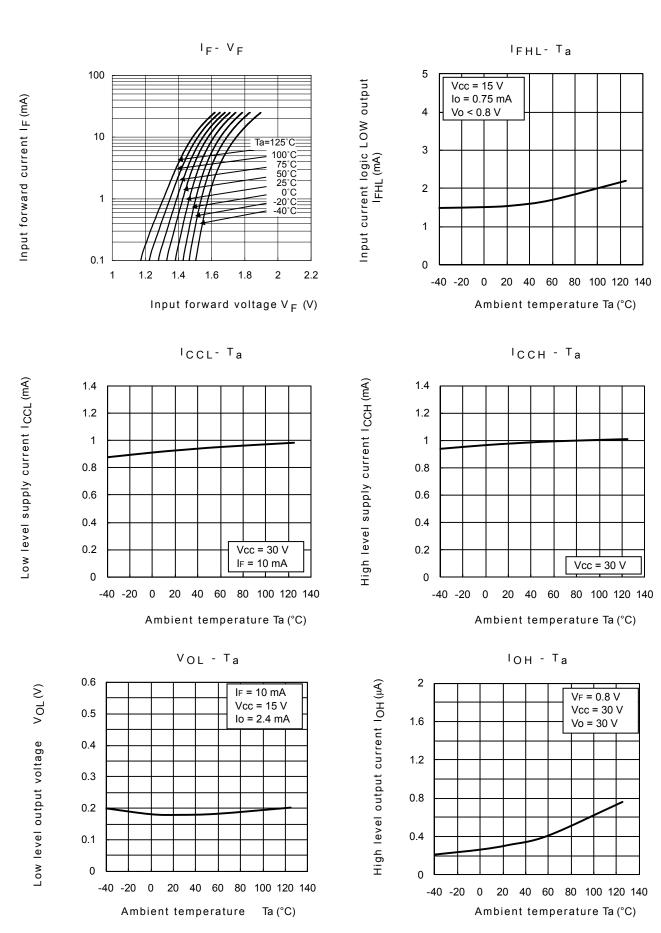
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### Test Circuit 5: t<sub>pHL</sub>, t<sub>pLH</sub>, |t<sub>pHL</sub>-t<sub>pLH</sub>|





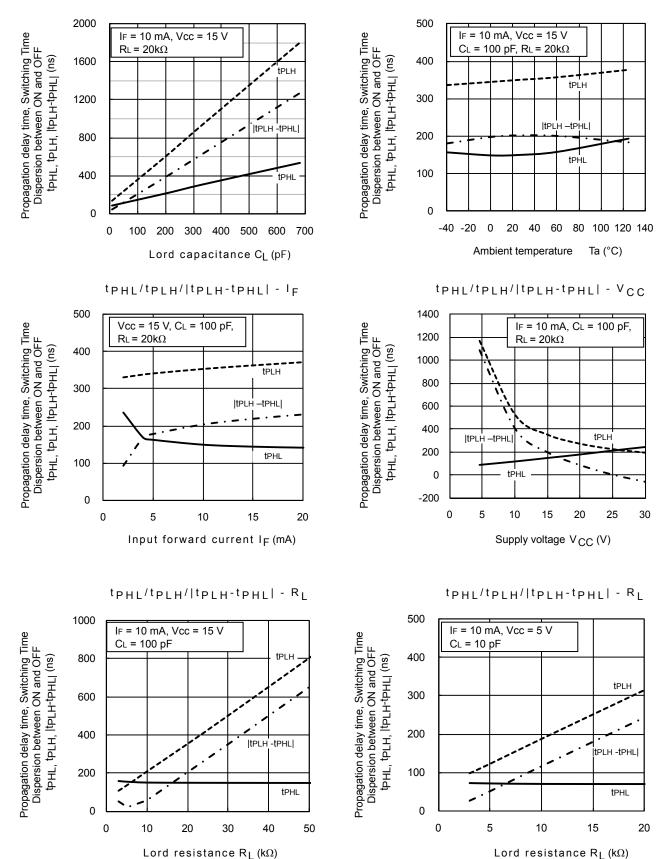




<sup>\*:</sup> The above graphs show typical characteristics.

tPHL/tPLH/|tPLH-tPHL| - CL

#### tрнц/tрцн/|tрцн-tрнц| - Та



<sup>\*:</sup> The above graphs show typical characteristics.

# PRECAUTIONS OF SURFACE MOUNTING TYPE PHOTOCOUPLER SOLDERING & GENERAL STORAGE

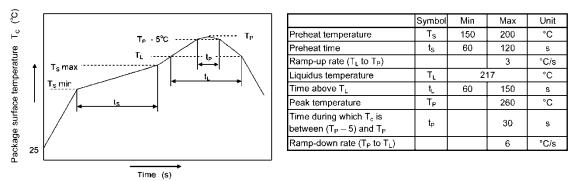
### • Precautions for Soldering

The soldering temperature should be controlled as closely as possible to the conditions shown below, irrespective of whether a soldering iron or a reflow soldering method is used.

1) When Using Soldering Reflow

The soldering temperature profile is based on the package surface temperature. (See the figure shown below.)

An example of a temperature profile when lead (Pb)-free solder is used



- Reflow soldering must be performed once or twice.
- The mounting should be completed with the interval from the first to the last mountings being 2 weeks.

2) When using soldering Flow

- Apply preheating of 150 °C for 60 to 120 seconds.
- Mounting condition of 260 °C or less within 10 seconds is recommended.
- Flow soldering must be performed once

3) When using soldering Iron

- Complete soldering within 10 seconds for lead temperature not exceeding 260 °C or within 3 seconds not exceeding 350 °C.
- Heating by soldering iron must be only once per 1 lead

### (2) Precautions for General Storage

- 1) Do not store devices at any place where they will be exposed to moisture or direct sunlight.
- 2) When transportation or storage of devices, follow the cautions indicated on the carton box.
- The storage area temperature should be kept within a temperature range of 5 °C to 35 °C, and relative humidity should be maintained at between 45% and 75%.
- 4) Do not store devices in the presence of harmful (especially corrosive) gases, or in dusty conditions.
- 5) Use storage areas where there is minimal temperature fluctuation. Because rapid temperature changes can cause condensation to occur on stored devices, resulting in lead oxidation or corrosion, as a result, the solderability of the leads will be degraded.
- 6) When repacking devices, use anti-static containers.
- 7) Do not apply any external force or load directly to devices while they are in storage.
- 8) If devices have been stored for more than two years, even though the above conditions have been followed, it is recommended that solderability of them should be tested before they are used.

### Specification for Embossed–Tape Packing (TPL)(TPR) for SO6 Coupler

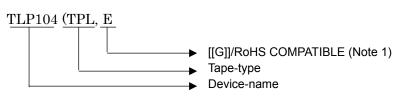
### 1. Applicable Package

Package	Product Type
SO6	Mini-flat coupler

### 2. Product Naming System

Type of package used for shipment is denoted by a symbol suffix after a product number. The method of classification is as below.

(Example)



Note 1: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility. RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

### 3. Tape Dimensions

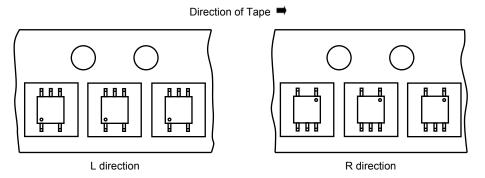
### 3.1 Specification Classification Are as Shown in Table 1

Table 1 Tape Type Classification

Tape type	Classification	Quantity (pcs / reel)
TPL	L direction	3000
TPR	R direction	3000

### **3.2** Orientation of Device in Relation to Direction of Tape Movement

Device orientation in the recesses is as shown in Figure 1.





3.3 Empty Device Recesses Are as Shown in Table 2.

 Table 2
 Empty Device Recesses

	Standard	Remarks
Occurrences of 2 or more successive empty device recesses	0	Within any given 40-mm section of tape, not including leader and trailer
Single empty device recesses	6 devices (max) per reel	Not including leader and trailer

#### 3.4 Start and End of Tape

The start of the tape has 50 or more empty holes. The end of tape has 50 or more empty holes and two empty turns only for a cover tape.

#### 3.5 Tape Specification

- (1) Tape material: Plastic (protection against electrostatics)
- (2) Dimensions: The tape dimensions are as shown in Figure 2 and Table 3.

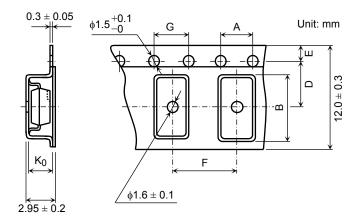


Figure 2 Tape Forms

		Unit: mm Unless otherwise specified: ±0.1
Symbol	Dimension	Remark
A	4.0	—
В	7.6	—
D	5.5	Center line of indented square hole and sprocket hole
E	1.75	Distance between tape edge and hole center
F	8.0	Cumulative error $^{+0.1}_{-0.3}$ (max) per 10 feed holes
G	4.0	Cumulative error $^{+0.1}_{-0.3}$ (max) per 10 feed holes
K <sub>0</sub>	2.6	Internal space

### <u>TOSHIBA</u>

### 3.6 Reel

- (1) Material: Plastic
- (2) Dimensions: The reel dimensions are as shown in Figure 3 and Table 4.

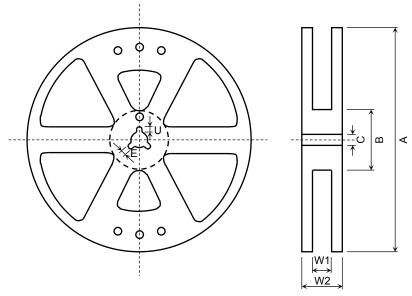


Figure 3 Reel Form

Table 4 Reel Dimensions

	Unit: mm
Symbol	Dimension
A	Φ380 ±2
В	Φ80 ±1
С	Φ13 ±0.5
E	2.0 ±0.5
U	4.0 ±0.5
W1	13.5 ±0.5
W2	17.5 ±1.0

### 4. Packing

Either one reel or five reels of photocoupler are packed in a shipping carton.

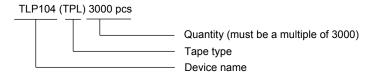
### 5. Label Indication

The carton bears a label indicating the product number, the symbol representing classification of standard, the quantity, the lot number and the Toshiba company name.

### 6. Ordering Method

When placing an order, please specify the product number, the tape type and the quantity as shown in the following example.

(Example)



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