Unit: mm

TOSHIBA Photocoupler GaAłAs Ired & Photo-IC

TLP109

Programmable Controllers Industrial Inverters Switching Power Supplies

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The Toshiba TLP109 mini-flat coupler is a small-outline coupler suitable for surface-mount assembly.

The TLP109 consists of a high-output-power GaAℓAs light emitting diode optically coupled to a high-speed photodiode-transistor chip.

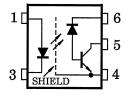
The TLP109 is housed in the SO6 package and guarantees a creepage distance of ≥ 5.0 mm, a clearance of ≥ 5.0 mm and an insulation thickness of ≥ 0.4 mm. Therefore, the TLP109 meets the reinforced insulation class requirements of international safety standards.

- Isolation voltage: 3750 Vrms (min)
- Switching speed: $t_{pHL} = 0.8 \ \mu s$, $t_{pLH} = 0.8 \ \mu s$ (max)
 - $@R_L = 1.9 \text{ k}\Omega$
- TTL-compatible
- UL approved : UL1577, File No.E67349
- c-UL approved CSA Component Acceptance Service No. 5A, File No.E67349
- Option (V4)

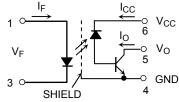
VDE approved : DIN EN60747-5-2

Maximum Operating Insulation Voltage : 707V_{PK} Highest Permissible Over Voltage : 6000V_{PK} (Note) : When a EN60747-5-2 approved type is needed, Please designate "Option(V4)"

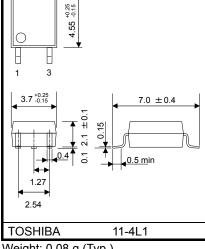
Pin Configuration (Top View)



1: ANODE 3: CATHODE 4: EMITTER (GND) 5: COLLECTOR (OUTPUT) 6: V_{CC} Schematic







Weight: 0.08 g (Typ.)

5 4

Construction Mechanical Ratings

Creepage distance:	5.0 mm (min)
Clearance:	5.0 mm (min)
Insulation thickness:	0.4 mm (min)

Absolute Maximum Ratings (Ta = 25°C)

	Characteristic		Symbol	Rating	Unit
	Forward current	(Note 1)	lF	20	mA
	Pulse forward current	(Note 2)	I _{FP}	40	mA
LED	Peak transient forward current	(Note 3)	I _{FPT}	1	А
	Reverse voltage		V _R	5	V
	Power dissipation	(Note 4)	PD	40	mW
	Output current		Ι _Ο	8	mA
ď	Peak output current		I _{OP}	16	mA
Detector	Supply voltage		V _{CC}	-0.5 to 30	V
ă	Output voltage		Vo	-0.5 to 20	V
	Output power dissipation	(Note 5)	PO	100	mW
Ope	erating temperature range		T _{opr}	-55 to 125	°C
Sto	Storage temperature range		T _{stg}	-55 to 125	°C
Lea	Lead solder temperature (10 s)		T _{sol}	260	°C
Isol	Isolation Voltage		D\/a	3750	Vrms
(AC	(AC,1 min., R.H.≤ 60°%) (N		BVS	3730	VIIIIS

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- (Note 1) Derate 0.36 mA / °C above 95°C.
- (Note 2) 50% duty cycle, 1 ms pulse width. Derate 0.72 mA / °C above 95°C.
- (Note 3) Pulse width \leq 1 µs, 300 pps.
- (Note 4) Derate 0.72 mA / °C above 95°C.
- (Note 5) Derate 1.8 mW / °C above 95°C.
- (Note 6) Device considered a two-terminal device: Pins 1 and 3 shorted together, and pins 4, 5 and 6 shorted together.

Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	V _F	I _F = 16 mA	1.50	1.64	1.85	V
	Forward voltage temperature coefficient	ΔV _F / ΔTa	I _F = 16 mA	_	-1.6		mV /°C
LED	Reverse current	I _R	V _R = 3 V	—	_	10	μA
	Capacitance between terminals	CT	V _F = 0 V, f = 1 MHz	_	60		pF
		I _{OH (1)}	$I_{\rm F}$ = 0 mA, $V_{\rm CC}$ = $V_{\rm O}$ = 5.5 V	_	3	500	nA
or	High level output	I _{OH (2)}	I _F = 0 mA, V _{CC} = 30 V V _O = 20 V	_	_	5	
Detector		ЮН	$I_F = 0 \text{ mA}, V_{CC} = 30 \text{ V}$ $V_O = 20\text{V}, \text{ Ta} = 100^{\circ}\text{C}$	_	_	50	μA
	High level supply current	Іссн	I _F = 0 mA, V _{CC} = 30 V	_	0.01	1	μA
Cur	rent transfer ratio	I _O / I _F	$I_F = 16 \text{ mA}, V_{CC} = 4.5 \text{ V}$ $V_O = 0.4 \text{ V}$	20	_		%
Lov	v level output voltage	V _{OL}	I _F = 16 mA, V _{CC} = 4.5 V I _O = 2.4 mA	_	_	0.4	V

Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Conditions	Min	Тур.	Max	Unit
Capacitance input to output	CS	V = 0 V, f = 1 MHz (Note 6)	_	0.8	_	pF
Isolation resistance	R _S	R.H. ≤ 60%, V _S = 500 V (Note 6)	1×10 ¹²	10 ¹⁴	_	Ω
		AC,1 minute	3750	_		V
Isolation voltage	BVS	AC,1 second, in oil	_	10000		V _{rms}
		DC,1 minute, in oil	_	10000	_	Vdc

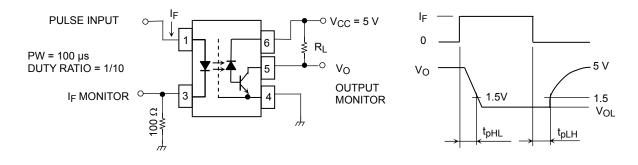
Switching Characteristics (Ta = 25°C, V_{CC} = 5 V)

Characteristic	Symbol	Test Cir– cuit	Test Condition	Min	Тур.	Max	Unit
Propagation delay time ($H \rightarrow L$)	t _{pHL}	1	I _F = 0→ 16 mA R _L = 1.9 kΩ	_	_	0.8	μs
Propagation delay time (L \rightarrow H)	^t pLH	1	I _F = 16→ 0 mA R _L = 1.9 kΩ	_	_	0.8	μs
Common mode transient immunity at high output level (Note 7)	CM _H	2	I _F = 0 mA, V _{CM} = 400 V _{p-p} R _L = 4.1 kΩ	5000	10000	_	V / µs
Common mode transient Immunity at low output level (Note 7)	CML	2	I _F = 16 mA, V _{CM} = 400 V _{p-p} R _L = 4.1 kΩ	-5000	-10000	_	V / µs

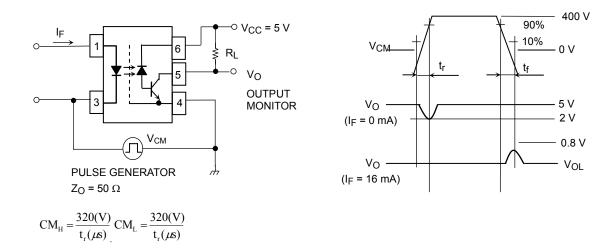
(Note) Maximum electrostatic discharge voltage for any pins: 100 V (C = 200 pF, R=0)

(Note 7) CM_L is the maximum rate of fall of the common mode voltage that can be sustained with the output voltage in the logic low state (V_O < 0.8 V). CM_H is the maximum rate of rise of the common mode voltage that can be sustained with the output voltage in the logic high state (V_O > 2.0 V)

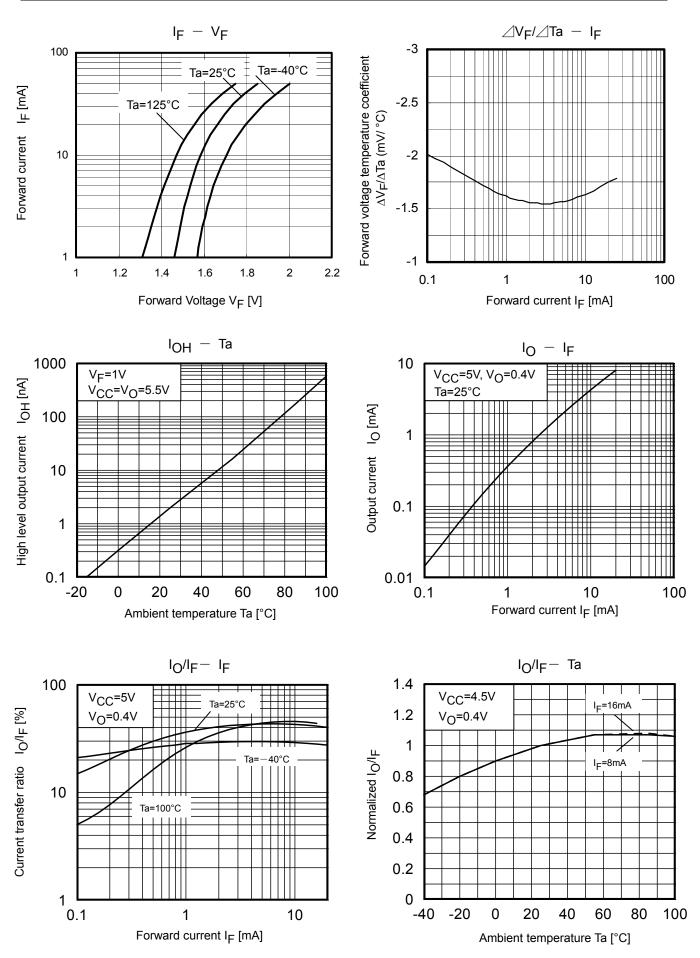
Test Circuit 1: Switching Time Test Circuit



Test Circuit 2: Common Mode Transient Immunity Test Circuit

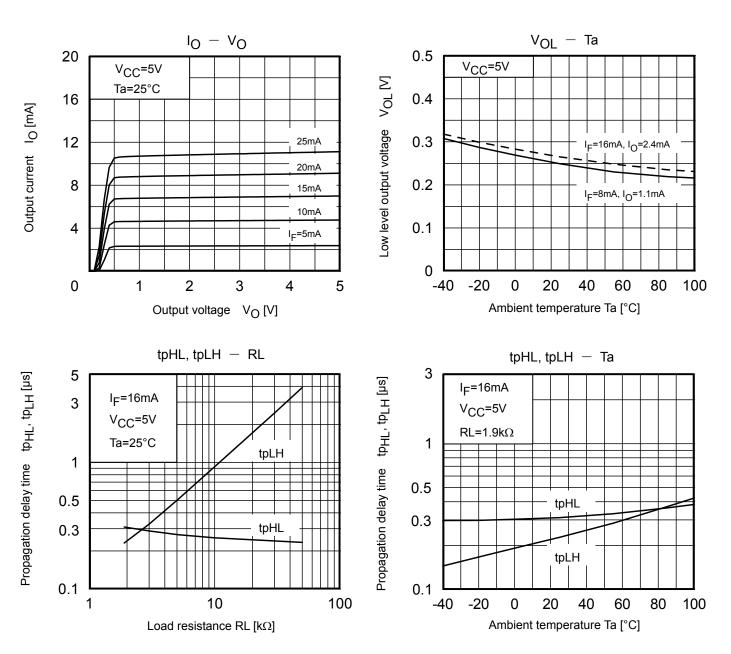


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* The above graphs show typical characteristics.

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Specification for Embossed–Tape Packing (TPL)(TPR) for SO6 Coupler

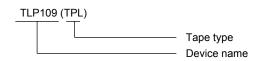
1. Applicable Package

Package	Product Type
SO6	Mini-flat coupler

2. Product Naming System

Type of package used for shipment is denoted by a symbol suffix after a product number. The method of classification is as below.

(Example)



3. Tape Dimensions

3.1 Specification Classification Are as Shown in Table 1

Table 1 Tape Type Classification

Tape type	Classification	Quantity (pcs / reel)
TPL	L direction	3000
TPR	R direction	3000

3.2 Orientation of Device in Relation to Direction of Tape Movement

Device orientation in the recesses is as shown in Figure 1.

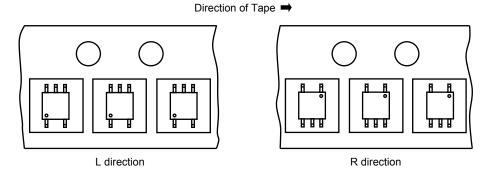


Figure 1 Device Orientation

3.3 Empty Device Recesses Are as Shown in Table 2.

Table 2 Empty Device Recesses

	Standard	Remarks
Occurrences of 2 or more successive empty device recesses	0	Within any given 40-mm section of tape, not including leader and trailer
Single empty device recesses	6 devices (max) per reel	Not including leader and trailer

3.4 Start and End of Tape

The start of the tape has 50 or more empty holes. The end of tape has 50 or more empty holes and two empty turns only for a cover tape.

3.5 Tape Specification

- (1) Tape material: Plastic (protection against electrostatics)
- (2) Dimensions: The tape dimensions are as shown in Figure 2 and Table 3.

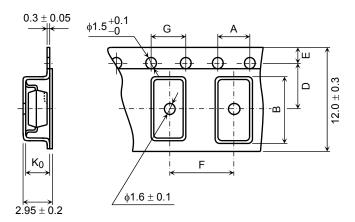


Figure 2 Tape Forms

Table 3 Tape Dimensions

		Unit: mm Unless otherwise specified: ±0.1
Symbol	Dimension	Remark
А	4.0	—
В	7.6	—
D	5.5	Center line of indented square hole and sprocket hole
E	1.75	Distance between tape edge and hole center
F	8.0	Cumulative error $^{+0.1}_{-0.3}$ (max) per 10 feed holes
G	4.0	Cumulative error $^{+0.1}_{-0.3}$ (max) per 10 feed holes
K ₀	2.6	Internal space

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3.6 Reel

- (1) Material: Plastic
- (2) Dimensions: The reel dimensions are as shown in Figure 3 and Table 4.

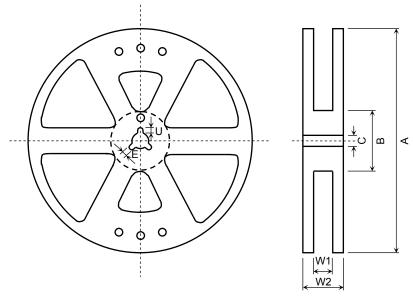


Figure 3 Reel Form

Table 4 Reel Dimensions

	Unit: mm
Symbol	Dimension
A	Ф380 ±2
В	Φ80 ±1
С	Φ13 ±0.5
E	2.0 ±0.5
U	4.0 ±0.5
W1	13.5 ±0.5
W2	17.5 ±1.0

4. Packing

Either one reel or five reels of photocoupler are packed in a shipping carton.

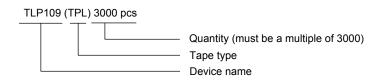
5. Label Indication

The carton bears a label indicating the product number, the symbol representing classification of standard, the quantity, the lot number and the Toshiba company name.

6. Ordering Method

When placing an order, please specify the product number, the tape type and the quantity as shown in the following example.

(Example)



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