TOSHIBA Photocoupler IRED & Photo-Transistor

# **TLP127**

Programmable Controllers DC-Output Module Telecommunication

The TOSHIBA mini-flat coupler TLP127 is a small outline coupler, suitable for a surface mount assembly.

TLP127 consists of an infrared emitting diode, optically coupled to a Darlington photo transistor with an integral base-emitter resistor.

Collector-emitter voltage : 300 V (min)

• Current transfer ratio: 1000 % (min)

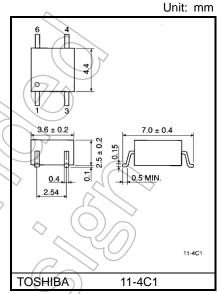
• Isolation voltage: 2500 Vrms (min)

UL-recognized: UL 1577, File No. E67349

cUL-recognized : CSA Component Acceptance Service No.5A

File No.E67349

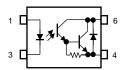
VDE-approved : EN 60747-5-5 (Note 1)



Weight: 0.09 g (typ.)

Note 1: When a VDE approved type is needed, please designate the Option (V4).

### Pin Configurations (top view)



- 1: ANODE
- 3: CATHODE
- 4: EMITTER
- 6: COLLECTOR

Start of commercial production 1988-04



### **Absolute Maximum Ratings (Ta = 25°C)**

	Characteristic	Symbol	Rating	Unit
	Forward current	lF	50	mA
ED	Forward current derating (Ta ≥ 53°C)	ΔI <sub>F</sub> /°C	-0.7	mA/°C
	Pulse forward current (100 μs pulse, 100 pps)	IFP	1	A
"	Reverse voltage	VR	5	N/
	Diode power dissipation	$P_{D}$	100	mW
	Diode power dissipation derating (Ta ≥ 53°C)	ΔP <sub>D</sub> /°C	-1.39	mW/°C
	Junction temperature	Tj	125	√°C
	Collector-emitter voltage	VCEO	300	$\bigcirc$ $\vee$
	Emitter-collector voltage	VECO	0.3	
ţ	Collector current	Ic	150	> mA
Detector	Collector power dissipation	Pc	150	mW ()
ď	Collector power dissipation derating (Ta ≥ 25°C)	ΔP <sub>C</sub> /°C	-1.5	mW/°C
	Junction temperature	Tj	125	°C
Storage temperature range		T <sub>stg</sub>	-55 to 125	<b>℃</b>
Operating temperature range		Topr	-55 to 100	(GC)
Lead soldering temperature (10 s)		T <sub>sol</sub>	260	~c
Total package power dissipation		Рт	200	// mW
Total package power dissipation derating (Ta ≥ 25°C)		ΔΡτ/°C	-2.0	mW/°C
	ation voltage , 60 s, R.H.≤ 60 %) (Note 1)	BVs	2500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 1: Device considered a two terminal device: Pins 1, 3 shorted together and pins 4, 6 shorted together.

## **Recommended Operating Conditions**

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	V <sub>CC</sub>	_	_	200	V
Forward current	lF	_	16	25	mA
Collector current	Ic	_	_	120	mA
Operating temperature	T <sub>opr</sub>	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.



### **Electrical Characteristics (Ta = 25°C)**

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	I <sub>F</sub> = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I <sub>R</sub>	V <sub>R</sub> = 5 V	_	_	10	μΑ
	Capacitance	CT	V = 0 V, f = 1 MHz	<u>                                     </u>	30	_	pF
Detector	Collector-emitter breakdown voltage	V(BR)CEO	IC = 0.1 mA	300		-	V
	Emitter-collector breakdown voltage	V(BR)ECO	IE = 0.1 mA	0.3	)~	-	V
	Collector dark current ICEO	lana	V <sub>CE</sub> = 200 V	) \ )	10	200	nA
		V <sub>CE</sub> = 200 V, Ta = 85 °C	)}	_	20	μΑ	
	Capacitance collector to emitter	CCE	V = 0 V, f = 1 MHz	_	12	-	pF

## **Coupled Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	MIn	Тур.	Max	Unit
Current transfer ratio	I <sub>C</sub> /I <sub>F</sub>	I <sub>F</sub> = 1 mA, V <sub>OE</sub> = 1 V	1000	4000	_	%
Saturated CTR	IC/IF(sat)	IF = 10 mA, VCE = 1 V	500		_	%
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	Ic = 10 mA, IF = 1 mA		7 —	1.0	V
Off-state collector current	I <sub>C(off)</sub>	Ic = 100 mA, IF = 10 mA VF = 0.7 V, VCE = 200 V	0.3	_	20	μА

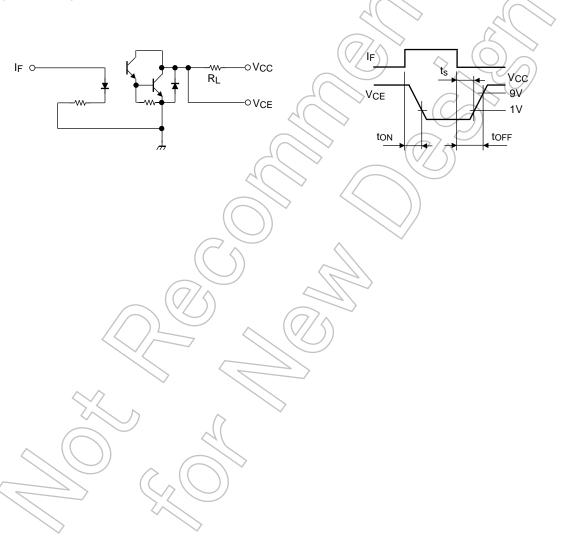
## Isolation Characteristics (Ta = 25°C)

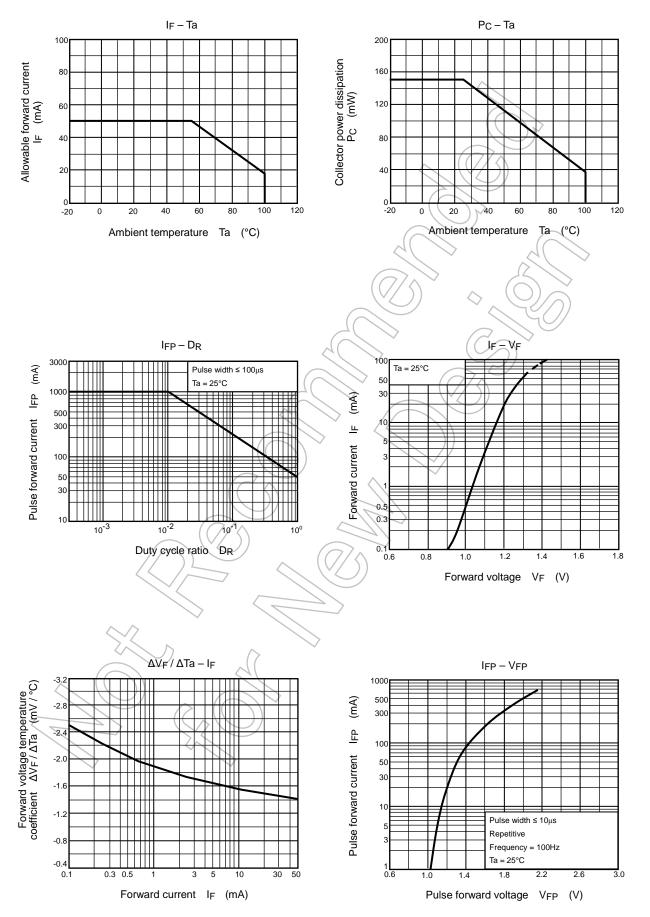
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance (input to output)	(¢s	V <sub>S</sub> = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	Rs	V <sub>S</sub> = 500 V, R.H.≤ 60 %	5×10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
Isolation voltage	↑ BV <sub>S</sub>	AC, 60 s	2500	_	_	V <sub>rms</sub>

## **Switching Characteristics (Ta = 25°C)**

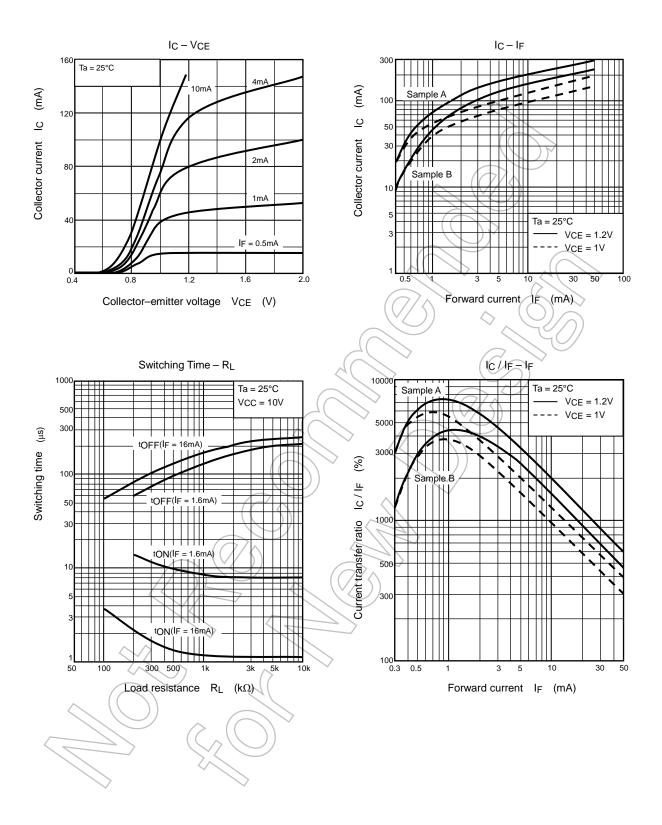
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	t <sub>r</sub>	$V_{CC} = 10 \text{ V, I}_{C} = 10 \text{ mA}$ $R_{L} = 100 \Omega$	_	40	_	
Fall time	t <sub>f</sub>		_	15	_	_
Turn-on time	ton		_	50	_	μS
Turn-off time	t <sub>off</sub>			15	_	
Turn-on time	ton			75	_	
Storage time	t <sub>S</sub>	$R_L = 180 \Omega$ (Fig.1) $V_{CC} = 10 \text{ V, I}_F = 16 \text{ mA}$	<u> </u>	40	_	μS
Turn-off time	toff	VCC = 10 V, IF = 16 IIIA	<i>)}</i>	80	_	

Fig.1: Switching time test circuit

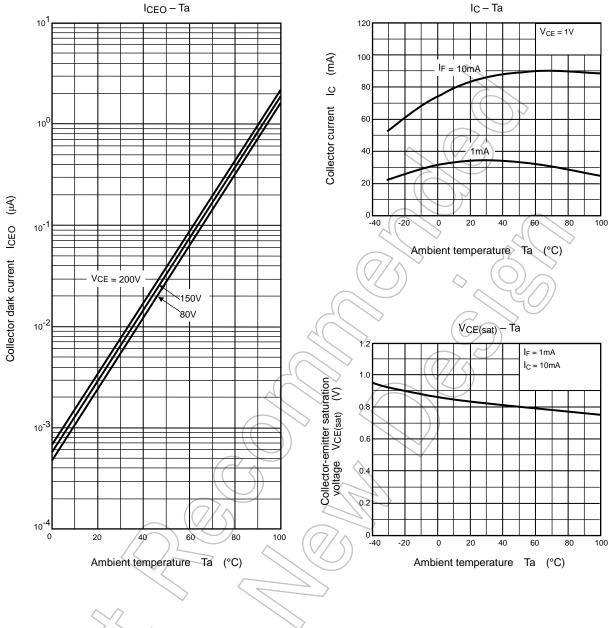




NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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