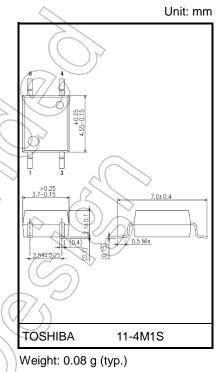
TOSHIBA Photocoupler **IRED & Photo-Transistor**

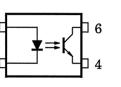
TLP185

Office Machine Programmable Controllers AC Adapter I/O Interface Board The TOSHIBA mini flat coupler TLP185 is a small outline coupler, suitable for surface mount assembly. TLP185 consists of a photo transistor optically coupled to an infrared emitting diode. Since TLP185 is smaller than DIP package, it's suitable for highdensity surface mounting applications such as programmable controllers, Collector-emitter voltage: 80 V (min) • Current transfer ratio: 50 % (min) Rank GB: 100% (min) Isolation voltage: 3750 Vrms (min) Operation Temperature:-55 to 110 °C UL-recognized: UL 1577, File No.E67349 cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349 TOSHIBA CQC-approved: GB4943.1,GB8898 Japan and Thailand Factory Weight: 0.08 g (typ.) CCC 仅适用干海拔 2000m 以下地区安全使用 VDE-approved: EN 60747-5-5 , EN 62368-1 (Note 1) **Pin Configuration (top view)** Note 1: When a VDE approved type is needed, please designate the Option(V4). 1[Construction mechanical rating Creepage distance : 5.0 mm (min) : 5.0 mm (min) Clearance Insulation thickness : 0.4 mm (min) 1: Anode 3: Cathode 4: Emitter 6: Collector

2011-12

Start of commercial production





Current Transfer Ratio

		Current Transfer	Ratio (%) (I _C / I _F)			
Туре	Classification (Note1)	I _F = 5 mA, V _{CE} = 5 V, Ta = 25°C		Marking Of Classification		
		Min	Max	\sim		
	Blank	50	400	Blank, YE, GR, GB, Y+, G, G+, B		
	Rank Y	50	150	YE , Y+		
	Rank GR	100	300	GR , G ,G+		
TLP185	Rank GB	100	400	GB, GR, G, G+, BL, B,		
ILP 100	Rank YH	75	150	Y+		
	Rank GRL	100	200	G		
	Rank GRH	150	300	G+		
	Rank BLL	200	400	$B \triangleleft (\) \qquad \qquad$		

Note1: Ex Rank GB: TLP185 (GB,E

Note: Application, type name for certification test, please use standard product type name, i, e. TLP185(GB,E: TLP185

Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit
	Forward current	lF	50	mA
	Forward current derating (Ta ≥ 90°C)	ΔI _F /°C	-1.5	mA/°C
	Pulse forward current (Note 1)	IFP	1	A
ĒD	Reverse voltage	VR	5	X
_	Diode power dissipation	PD	100	mW
	Diode power dissipation derating (Ta >90°C)	∆P _D /°C	-2.9	mW/°C
	Junction temperature	Тј	125) °C
	Collector-emitter voltage	V _{CEO}	80	V
	Emitter-collector voltage	V _{ECO}		V
ctor	Collector current	Ic	50	mA
Detector	Collector power dissipation	Pc	150	mW
	Collector power dissipation derating $(Ta \ge 25^{\circ}C)$	ΔPc/°C	-1.5	mW/°C
	Junction temperature	Tj) 125 🛇	°C
Оре	erating temperature range	Topr	-55 to 110	°C
Stor	rage temperature range	Tstg	-55 to 125	⊃°C
Lea	d soldering temperature (10 s)	T _{sol}	260	°C
Tota	al package power dissipation	PŢ	200	mW
Tota	al package power dissipation derating (Ta \ge 25°C)	ΔPT/°C	-2.0	mW/°C
Isola	ation voltage (AC, 60 s, R.H. ≤ 60 %) (Note 2)	BVs	3750	Vrms

Note: Using continuously under heavy loads (e.g., the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pulse width \leq 100 µs, f = 100 Hz

Note 2: Device considered a two terminal device: Pins 1 and 3 shorted together and 4 and 6 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	V _{CC}	—	5	48	V
Forward current	lF	—	16	20	mA
Collector current	lc	_	1	10	mA

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	I _F = 10 mA	1.1	1.25	1.4	V
LED	Reverse current	I _R	V _R = 5 V	_	_	5	μA
	Capacitance	CT	V = 0 V, f = 1 MHz	γ	30		pF
	Collector-emitter breakdown voltage	V(BR)CEO	IC = 0.5 mA	80			V
r	Emitter-collector breakdown voltage	V(BR)ECO	IE = 0.1 mA)~		V
Detector	Collector dark current ICEO	1050	V _{CE} = 48 V) <	0.01	0.08	μA
D		ICEO	V _{CE} = 48 V, Ta = 85 °C	\mathcal{F}	2	50	μA
	Capacitance (collector to emitter)	CCE	V = 0 V, f = 1 MHz		10	_	pF

Coupled Electrical Characteristics (Ta = 25°C)

			\mathcal{A}		
Symbol	Test Condition	Min	Тур.	Max	Unit
10/17	IF = 5 mA, VCE = 5 V	50	1A	400	%
IC/IF	Rank GB	100	70/	400	70
10/15(+)	IF = 1 mA, VCE = 0.4 V		60	—	%
IC/IF(sat)	Rank GB	-30	—	—	70
	IC = 2.4 mA, IF = 8 mA	- (—	0.3	
VCE(sat)	$I_{\rm C} = 0.2 \rm{mA}, I_{\rm F} = 1 \rm{mA}$		0.2	—	V
	Rank GB	—	—	0.3	
IC(off)	VF = 0.7 V, VCE = 48 V	_	1	10	μΑ
	IC/IF IC/IF(sat) VCE(sat)	IC/IF IC/IF IF = 5 mA, VCE = 5 V Rank GB IC/IF(sat) IF = 1 mA, VCE = 0.4 V Rank GB IC = 2.4 mA, IF = 8 mA VCE(sat) IC = 0.2 mA, IF = 1 mA Rank GB	IC/IF IF = 5 mA, VCE = 5 V 50 IC/IF(sat) IF = 1 mA, VCE = 0.4 V - IC/IF(sat) IF = 1 mA, VCE = 0.4 V - VCE(sat) IC = 0.2 mA, IF = 8 mA - VCE(sat) IC = 0.2 mA, IF = 1 mA - Rank GB - - Rank GB - -	IC/IF IF = 5 mA, VCE = 5 V 50 IC/IF(sat) IF = 1 mA, VCE = 0.4 V 60 IC/IF(sat) IF = 1 mA, VCE = 0.4 V 60 IC/IF(sat) IF = 1 mA, VCE = 0.4 V 60 IC/IF(sat) IF = 1 mA, VCE = 0.4 V 60 VCE(sat) IC = 0.2 mA, IF = 8 mA - - VCE(sat) IC = 0.2 mA, IF = 1 mA - 0.2 Rank GB - - -	IC/IF IF = 5 mA, VCE = 5 V 50 400 IC/IF (sat) IF = 1 mA, VCE = 0.4 V 60 - IC/IF(sat) IF = 1 mA, VCE = 0.4 V 60 - IC/IF(sat) IF = 1 mA, VCE = 0.4 V 60 - VCE(sat) IC = 0.2 mA, IF = 8 mA - - 0.3 VCE(sat) IC = 0.2 mA, IF = 1 mA - 0.3

Isolation Characteristics (Ta = 25° C)

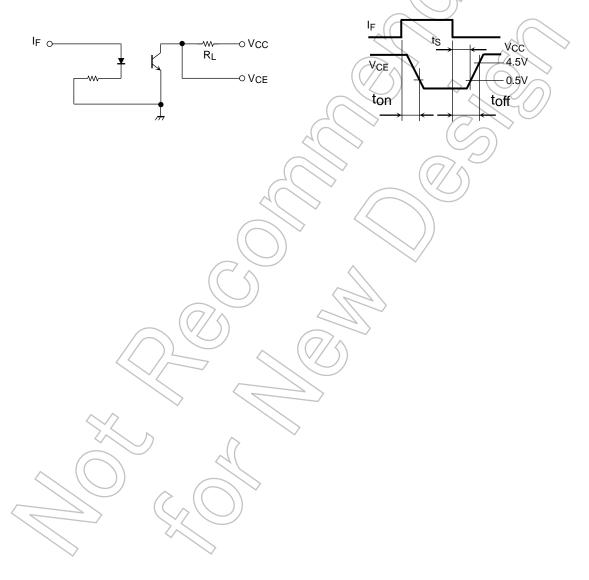
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance (input to output)	Cs	Vs = 0 V, f = 1 MHz	—	0.8	_	pF
Isolation resistance	Rs	V _S = 500 V, R.H. ≤ 60 %	1×10 ¹²	10 ¹⁴	—	Ω
Isolation voltage	BVs	AC, 60 s	3750		-	Vrms

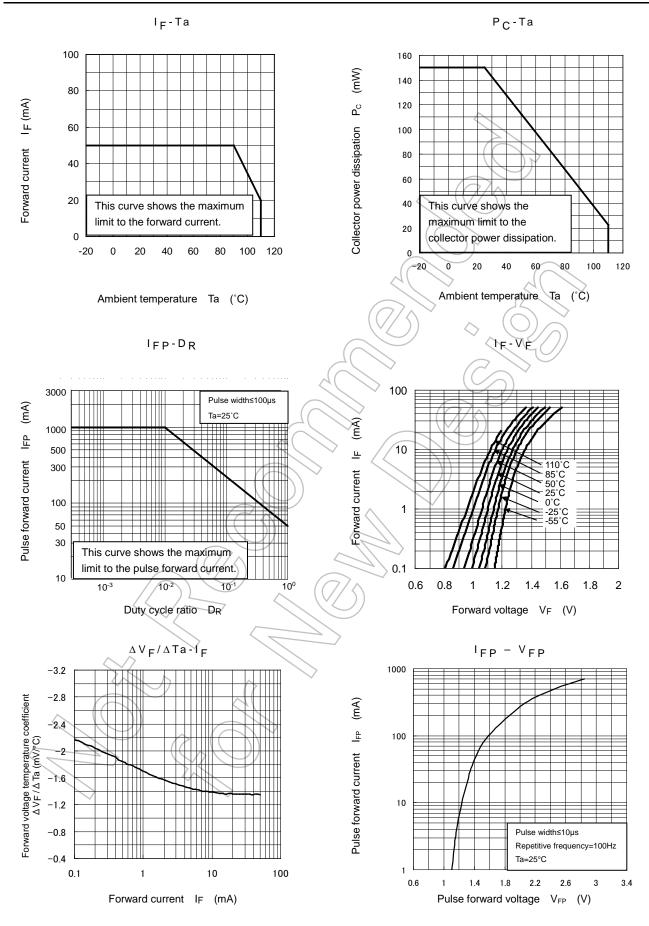


Switching Characteristics (Ta = 25°C)

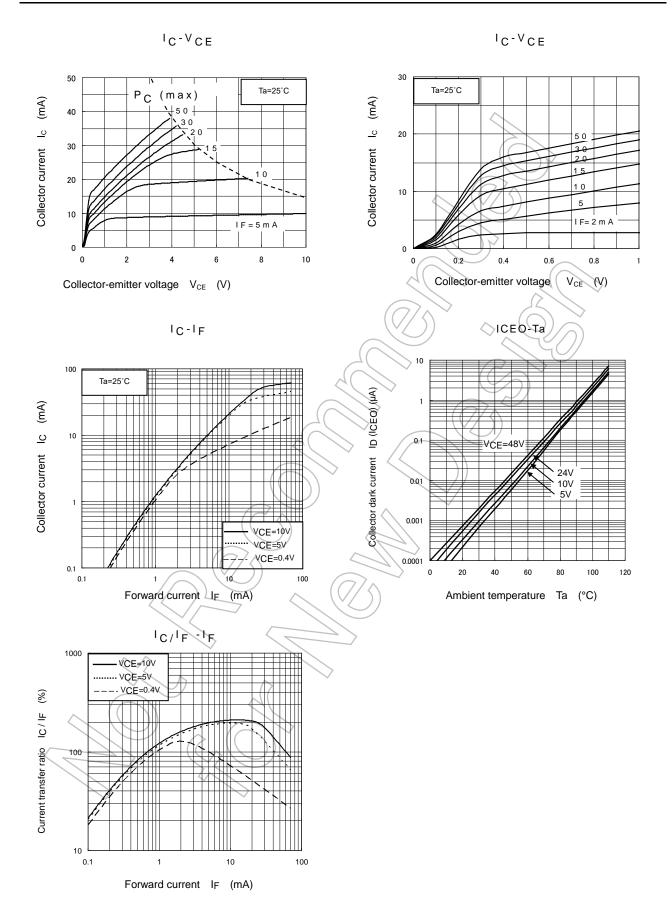
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	tr		_	5	_	μs
Fall time	tf	$V_{CC} = 10 V, I_C = 2 mA$ R _L = 100 Ω	_	9	_	
Turn-on time	t _{on}		/	9	_	
Turn-off time	toff		$\langle \rangle$	9	_	
Turn-on time	ton		(\mathcal{L})	2	_	
Storage time	ts	$R_L = 1.9 k\Omega$ (Fig.1) VCC = 5 V, IF = 16 mA	\sum	30	_	μs
Turn-off time	toff		\mathcal{D}	70	—	

Fig. 1 Switching time test circuit

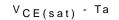




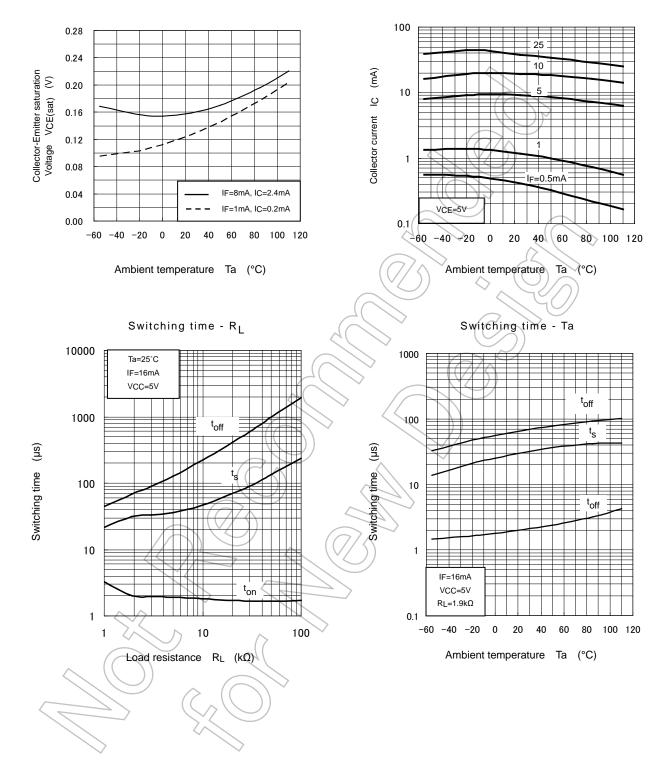
NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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I_С - Та



NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Soldering and Storage

1. Soldering

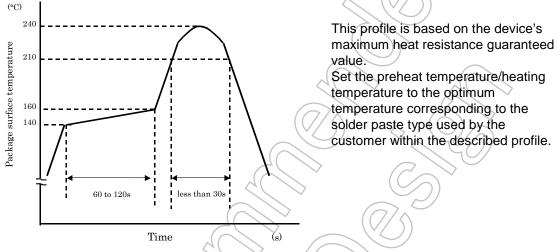
1.1 Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as

much as possible by observing the following conditions.

1) Using solder reflow

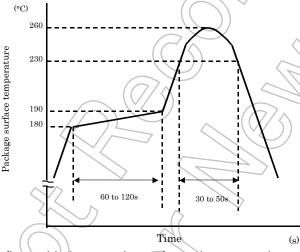




Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the

customer within the described profile.

·Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.

2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)

Please preheat it at 150°C between 60 and 120 seconds.

Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.

3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

2. Storage

- 1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- 2) Follow the precautions printed on the packing label of the device for transportation and storage.

3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.

- 4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- 5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- 6) When restoring devices after removal from their packing, use anti-static containers.
- 7) Do not allow loads to be applied directly to devices while they are in storage.
- 8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

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