**TOSHIBA Photocoupler Photorelay** 

# **TLP224G, TLP224G-2**

Modems PBX

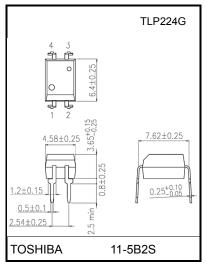
**Telecommunications** 

The TOSHIBA TLP224G series consists of an infrared emitting diode optically coupled to a photo-MOSFET in a 4 pin DIP (DIP4), which is suitable for equipment for high tech communications, including modems. The TLP224G series complies with FCC part 68 rules with current

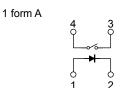
limiting function.

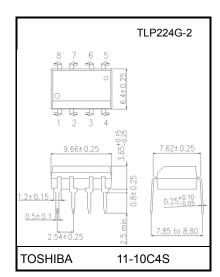
- TLP224G: 4 pin DIP, 1 channel type (1 form A)
- TLP224G-2: 8 pin DIP, 2 channel type (2 form A)
- Peak off-state voltage: 350 V (min)
- Trigger LED current: 3 mA (max)
- On-state current: 120 mA (max)
- Load current limiting: 150 mA to 300 mA (t = 5 ms)
- On-state resistance:  $35 \Omega$  (max)
- Isolation voltage: 2500 Vrms (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349

Unit: mm

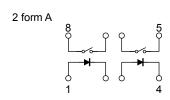


Weight: 0.26 g (typ.)



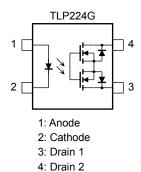


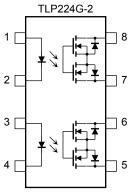
Weight: 0.54 g (typ.)



Start of commercial production 1999-09

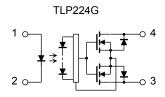
#### Pin Configuration (top view)





- 1, 3: Anode
- 2, 4: Cathode
- 5: Drain 1
- 6: Drain 2
- 7: Drain 3
- 8: Drain 4

#### **Internal Circuit**



#### **Absolute Maximum Ratings (Ta = 25°C)**

	Characteristics	Symbol	Rating	Unit
	Forward current	lF	50	mA
	Forward current derating (Ta ≥ 25°C)	ΔIF/°C	-0.5	mA/°C
	Peak forward current (100 μs pulse, 100 pps)	lFP	1	А
LED	Reverse voltage	$V_{R}$	6	٧
	Diode power dissipation	PD	50	mW
	Diode power dissipation derating (Ta ≥ 25°C)	ΔPD /°C	-0.5	mW/°C
	Junction temperature	Tj	125	°C
	Off-state output terminal voltage	Voff	350	V
	On-state current (Note 1)	Ion	120	mA
Detector		Δl <sub>ON</sub> /°C	-1.2	mA/°C
Detector	Output power dissipation	Ро	504	mW
	Output power dissipation derating (Ta ≥ 25°C)	ΔP <sub>O</sub> / °C	-5.04	mW / °C
	Junction temperature	Tj	125	°C
Storage temperature range		T <sub>stg</sub>	−55 to 125	°C
Operating temperature range		Topr	−40 to 85	°C
Lead solde	ering temperature (10 s)	T <sub>sol</sub>	260	°C
Isolation vo	oltage (AC, 60 s, R.H. ≤ 60 %) (Note 2)	BVs	2500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Two channels operating simultaneously.

Note 2: Device considered a two-terminal device: LED side pins shorted together, and detector side pins shored together.

#### **Recommended Operating Conditions**

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage	$V_{DD}$	_	_	280	V
Forward current	lF	5	7.5	25	mA
On-state current	Ion	_	_	100	mA
Operating temperature	T <sub>opr</sub>	-20	_	65	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

#### **Individual Electrical Characteristics (Ta = 25°C)**

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	I <sub>F</sub> = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	IR	V <sub>R</sub> = 6 V	_	_	10	μА
	Capacitance	Ст	VF = 0 V, f = 1 MHz	_	30	_	pF
Detector	Off-state current	loff	V <sub>OFF</sub> = 350 V	_	_	1	μА
Detector	Capacitance	Coff	V = 0 V, f = 1 MHz	_	40	_	pF

#### **Coupled Electrical Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	lfT	I <sub>ON</sub> = 120 mA	-	1	3	mA
Return LED current	IFC	I <sub>OFF</sub> = 10 μA	0.1	0.4	_	mA
Load current limiting	I <sub>LIM</sub>	$I_F = 5 \text{ mA}, V_{DD} = 5 \text{ V}, t = 5 \text{ ms}$	150	1	300	mA
On-state resistance	Ron	$I_{ON} = 120 \text{ mA}, I_F = 5 \text{ mA}$	_	22	35	Ω

### **Isolation Characteristics (Ta = 25°C)**

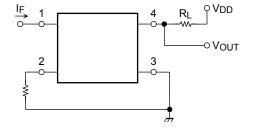
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	Cs	V <sub>S</sub> = 0 V, f = 1 MHz	_	8.0	_	pF
Isolation resistance	Rs	V <sub>S</sub> = 500 V, R.H. ≤ 60 %	$5 \times 10^{10}$	10 <sup>14</sup>	_	Ω
Isolation voltage	BVs	AC, 60 s	2500		_	Vrms

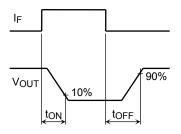
#### **Switching Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Turn-on time	ton	$R_L = 200 \Omega$ (Note) $V_{CC} = 20 \text{ V}, \text{ IF} = 5 \text{ mA}$	_	_	1	ms
Turn-off time	toff	$R_L = 200 \ \Omega$ (Note) $V_{CC} = 20 \ V, \ I_F = 5 \ mA$	_	_	1	ms

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Note: Switching time test circuit





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