

TLP291

Power Supplies
Programmable Controllers
Hybrid ICs

Unit: mm

TLP291 consists of photo transistor, optically coupled to an infrared emitting diode. TLP291 is housed in the SO4 package, very small and thin coupler. Since TLP291 is guaranteed wide operating temperature ($T_a = -55$ to 110 °C) and high isolation voltage (3750Vrms), it's suitable for high-density surface mounting applications such as small switching power supplies and programmable controllers.

- Collector-Emitter Voltage : 80 V (min)
- Current Transfer Ratio : 50% (min)
 Rank GB : 100% (min)
- Isolation Voltage : 3750 Vrms (min)
- Operation temperature : -55 to 110 °C
- UL-recognized : UL 1577, File No.E67349
- cUL-recognized : CSA Component Acceptance Service No.5A
 File No.67349
- VDE-approved : EN 60747-5-5, EN 62368-1 (Note 1)
- CQC-approved : GB4943.1, GB8898 Japan and Thailand Factory

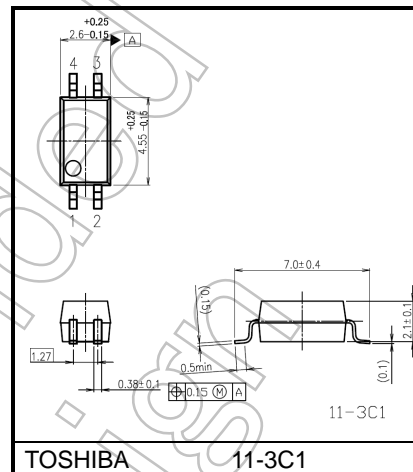


仅适用于海拔 2000m 以下地区安全使用

Note 1: When a VDE approved type is needed, please designate the **Option(V4)**.

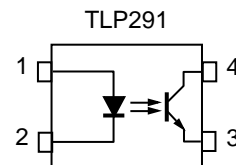
Construction Mechanical Rating

- Creepage distance: 5.0 mm (min)
- Clearance: 5.0 mm (min)
- Insulation thickness: 0.4 mm (min)



Weight: 0.05 g (typ.)

Pin Configuration



- 1: ANODE
- 2: CATHODE
- 3: EMITTER
- 4: COLLECTOR

Start of commercial production
 2012-02

Current Transfer Ratio (CTR) Rank (Unless otherwise specified, Ta = 25°C)

TYPE	Classification (Note1)	Current Transfer Ratio (%) (I _C / I _F)		Marking of Classification
		I _F = 5 mA, V _{CE} = 5 V, Ta = 25°C		
		Min	Max	
TLP291	Blank	50	400	Blank, YE, Y+, GR, GB, G, G+,B
	Rank Y	50	150	YE
	Rank GR	100	300	GR
	Rank GB	100	400	GB
	Rank YH	75	150	Y+
	Rank GRL	100	200	G
	Rank GRH	150	300	G+
	Rank BLL	200	400	B

Note1: Specify both the part number and a rank in this format when ordering

(e.g.) rank GB: TLP291 (GB,E

For safety standard certification, however, specify the part number alone.

(e.g.)TLP291 (GB,E: TLP291

Not Recommended for New Design

Absolute Maximum Ratings (Note)(Unless otherwise specified, Ta = 25°C)

CHARACTERISTIC		SYMBOL	NOTE	RATING	UNIT
LED	Input forward current	I_F		50	mA
	Input forward current derating (Ta≥90°C)	$\Delta I_F / \Delta T_a$		-1.5	mA /°C
	Input forward current (pulsed)	I_{FP}	(Note 2)	1	A
	Input reverse voltage	V_R		5	V
	Input power dissipation	P_D		100	mW
	Input power dissipation derating (Ta ≥ 90°C)	$\Delta P_D / \Delta T_a$		-3.0	mW /°C
	Junction temperature	T_j		125	°C
DETECTOR	Collector-emitter voltage	V_{CEO}		80	V
	Emitter-collector voltage	V_{ECO}		7	V
	Collector current	I_C		50	mA
	Collector power dissipation	P_C		150	mW
	Collector power dissipation derating(Ta≥25°C)	$\Delta P_C / \Delta T_a$		-1.5	mW /°C
	Junction temperature	T_j		125	°C
Operating temperature range		T_{opr}		-55 to 110	°C
Storage temperature range		T_{stg}		-55 to 125	°C
Lead soldering temperature		T_{sol}		260 (10 s)	°C
Total package power dissipation		P_T		200	mW
Total package power dissipation derating(Ta≥25°C)		$\Delta P_T / \Delta T_a$		-2.0	mW /°C
Isolation voltage		BV_S	(Note3)	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note2: Pulse width ≤ 100 μs, frequency 100 Hz

Note3: AC, 60 s, R.H.≤60 %, Device considered a two terminal device: LED side pins shorted together and DETECTOR side pins shorted together.

Electrical Characteristics (Unless otherwise specified, Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
LED	Input forward voltage	V_F	$I_F = 10 \text{ mA}$	1.1	1.25	1.4	V
	Input reverse current	I_R	$V_R = 5 \text{ V}$	-	-	5	μA
	Input capacitance	C_T	$V = 0 \text{ V}, f = 1 \text{ MHz}$	-	30	-	pF
DETECTOR	Collector-emitter breakdown voltage	$V_{(BR) CEO}$	$I_C = 0.5 \text{ mA}$	80	-	-	V
	Emitter-collector breakdown voltage	$V_{(BR) ECO}$	$I_E = 0.1 \text{ mA}$	7	-	-	V
	Dark current	I_{CEO}	$V_{CE} = 48 \text{ V}$	-	0.01	0.08	μA
			$V_{CE} = 48 \text{ V}, T_a = 85 \text{ °C}$	-	2	50	μA
Collector-emitter capacitance	C_{CE}	$V = 0 \text{ V}, f = 1 \text{ MHz}$	-	10	-	pF	

Coupled Electrical Characteristics (Unless otherwise specified, Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Current transfer ratio	I _C / I _F	I _F = 5 mA, V _{CE} = 5 V Rank GB	50	-	400	%
			100	-	400	
Saturated current transfer ratio	I _C / I _F (sat)	I _F = 1 mA, V _{CE} = 0.4 V Rank GB	-	60	-	%
			30	-	-	
Collector-emitter saturation voltage	V _{CE} (sat)	I _C = 2.4 mA, I _F = 8 mA I _C = 0.2 mA, I _F = 1 mA Rank GB	-	-	0.3	V
			-	0.2	-	
			-	-	0.3	
OFF-state collector current	I _C (off)	V _F = 0.7 V, V _{CE} = 48 V	-	-	10	μA

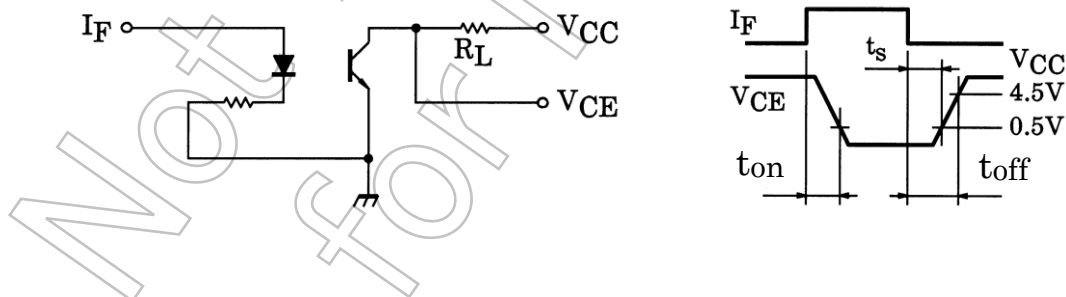
Isolation Characteristics (Unless otherwise specified, Ta = 25°C)

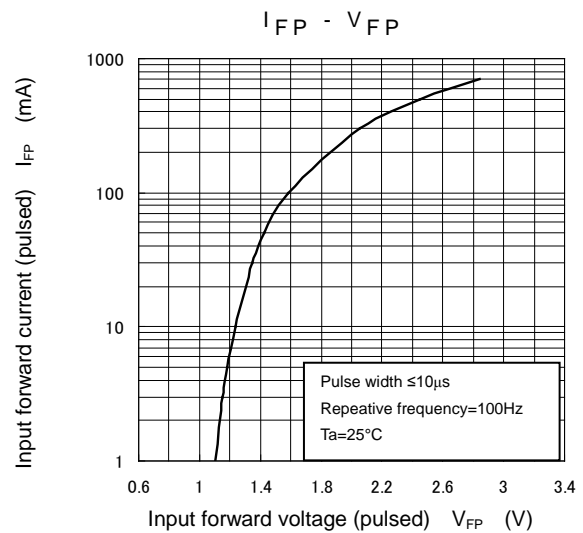
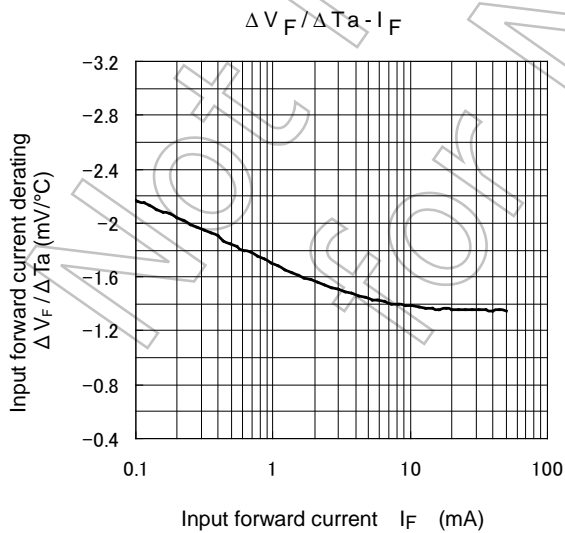
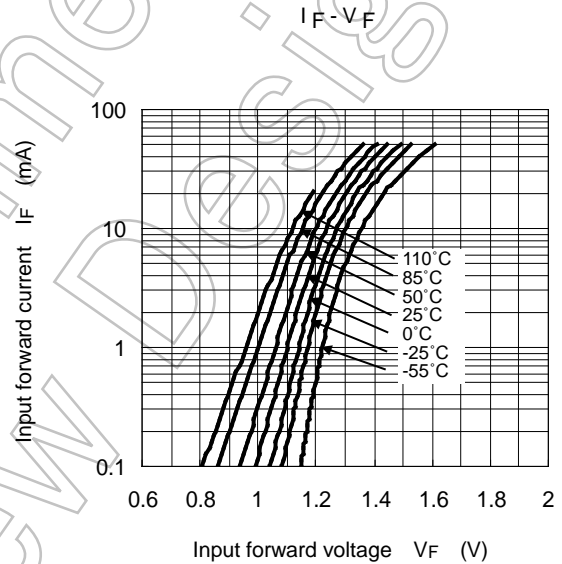
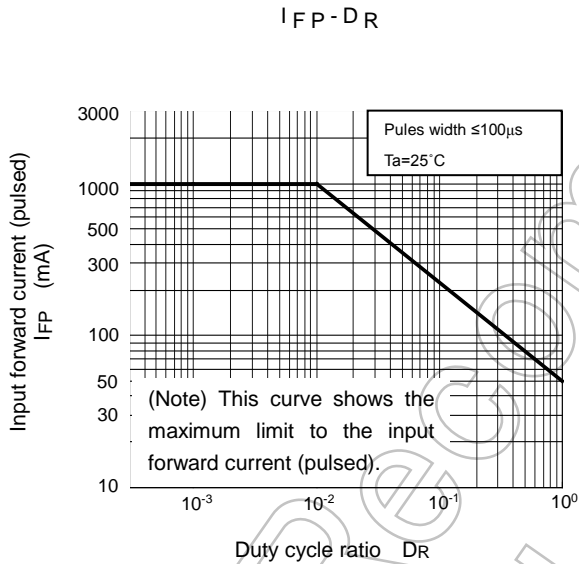
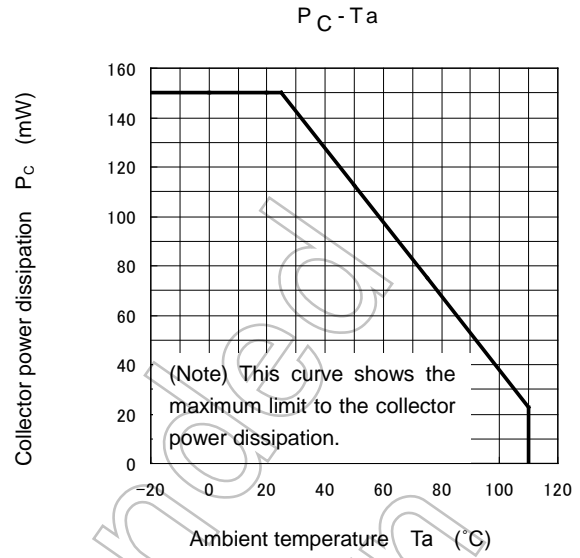
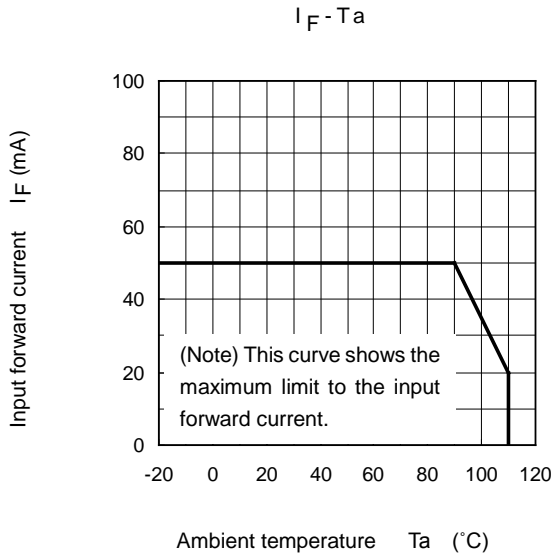
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Total capacitance (input to output)	C _S	V _S = 0 V, f = 1 MHz	-	0.8	-	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≤ 60 %	1×10 ¹²	10 ¹⁴	-	Ω
Isolation voltage	BV _S	AC, 60 s	3750	-	-	V _{rms}

Switching Characteristics (Unless otherwise specified, Ta = 25°C)

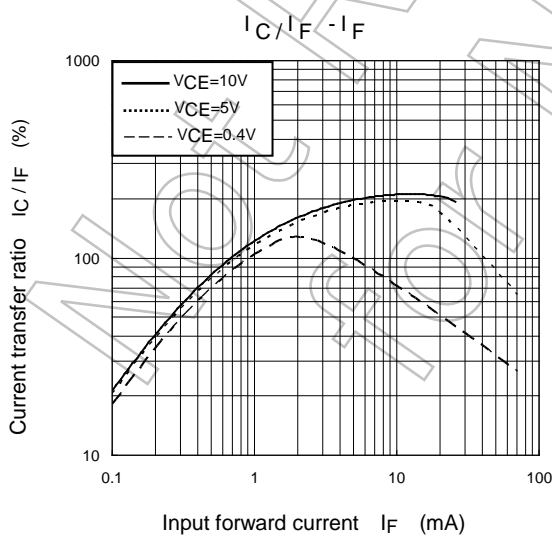
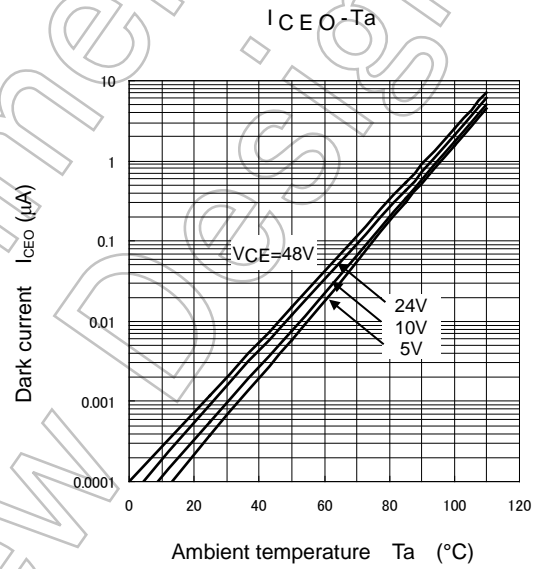
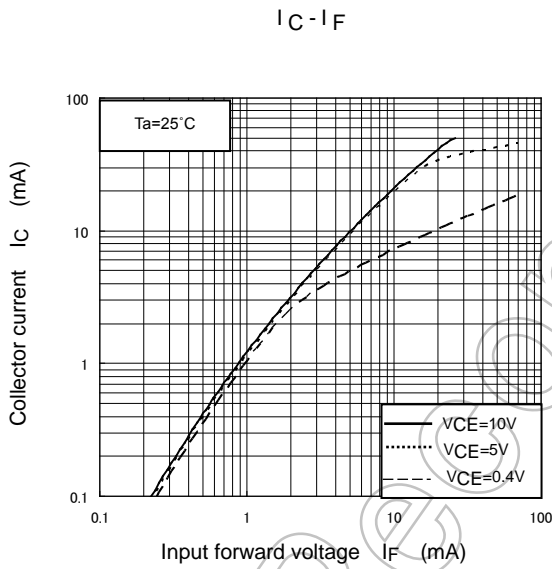
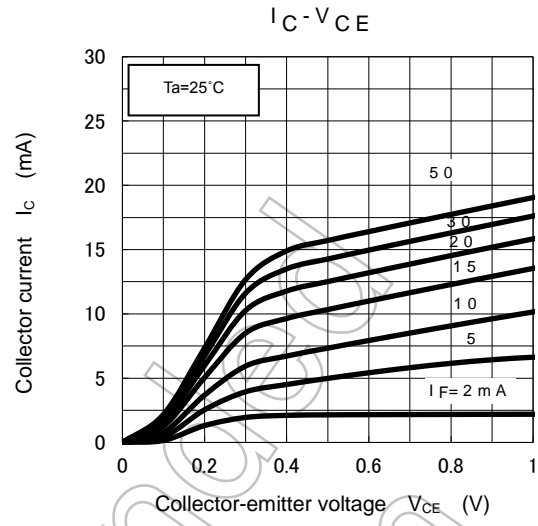
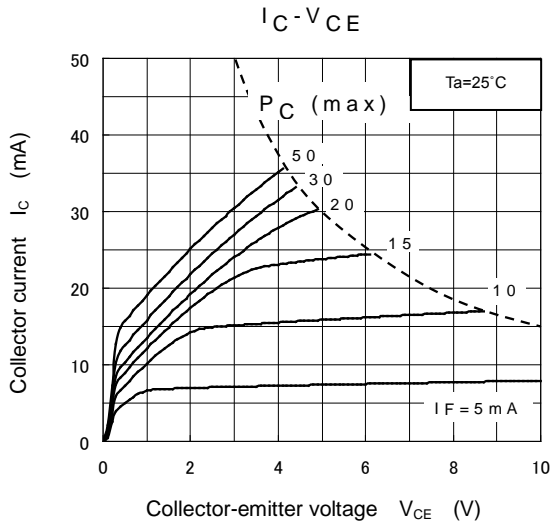
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Rise time	t _r	V _{CC} = 10 V, I _C = 2 mA R _L = 100Ω	-	4	-	μs
Fall time	t _f		-	7	-	
Turn-on time	t _{on}		-	7	-	
Turn-off time	t _{off}		-	7	-	
Turn-on time	t _{on}	R _L = 1.9 kΩ V _{CC} = 5 V, I _F = 16 mA (Fig.1)	-	2	-	μs
Storage time	t _s		-	30	-	
Turn-off time	t _{off}		-	60	-	

(Fig.1) Switching Time Test Circuit



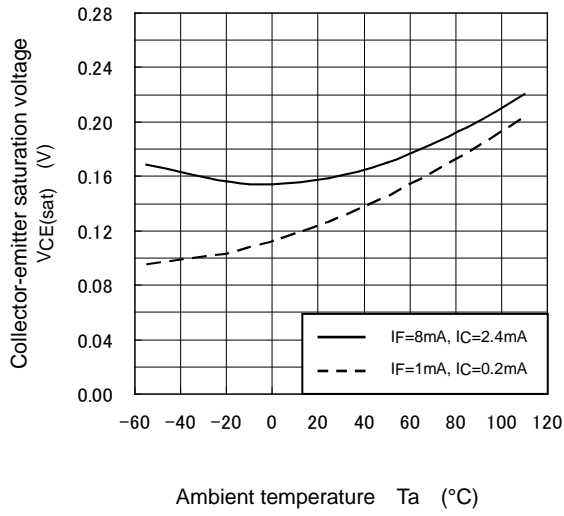


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

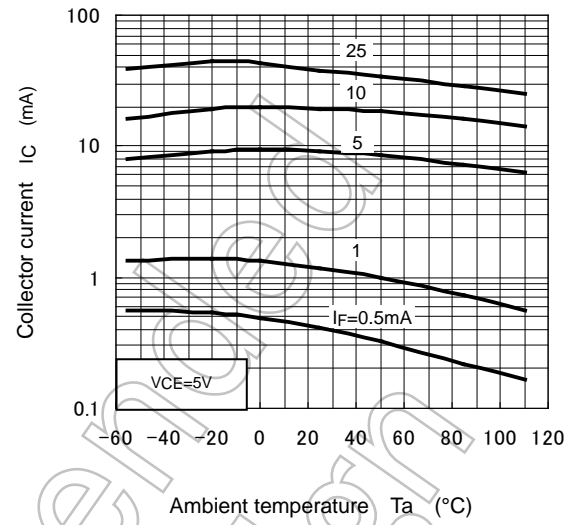


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

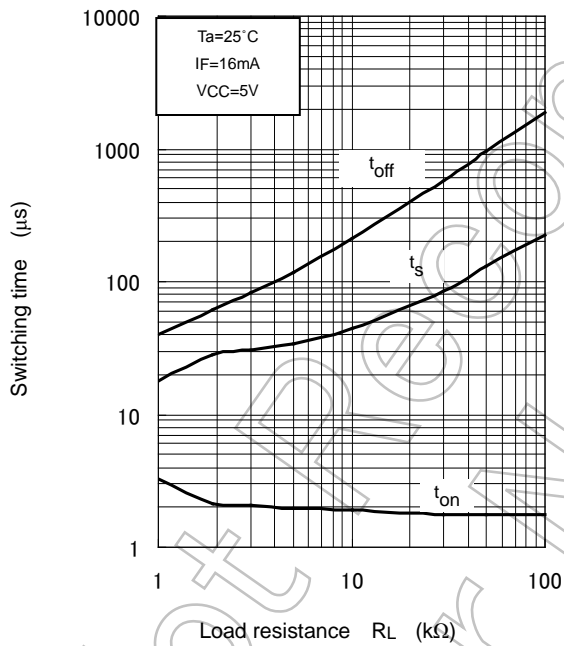
$V_{CE(sat)} - T_a$



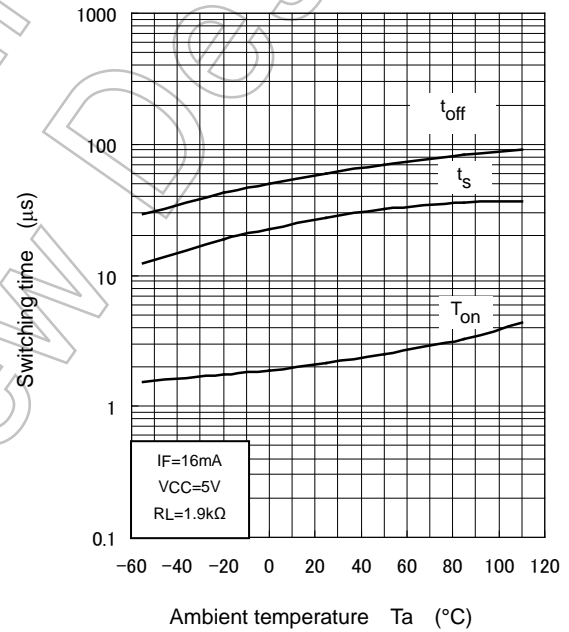
$I_C - T_a$



Switching time - R_L



Switching time - T_a



Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Soldering and Storage

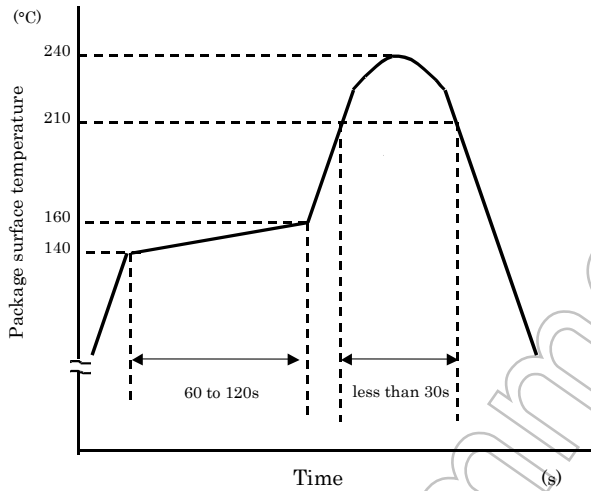
1. Soldering

1.1 Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as much as possible by observing the following conditions.

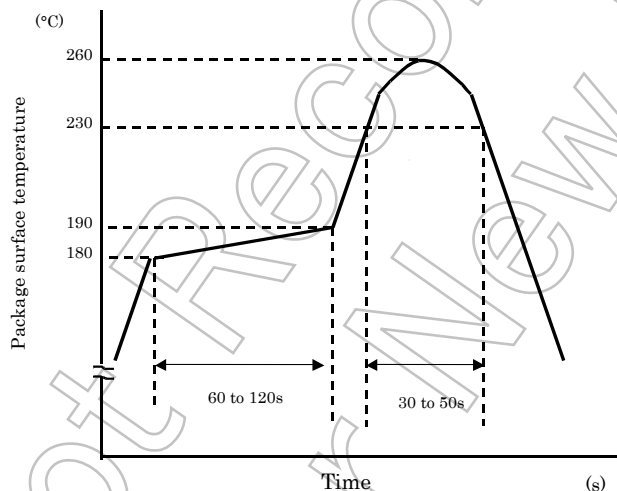
1) Using solder reflow

· Temperature profile example of lead (Pb) solder



This profile is based on the device's maximum heat resistance guaranteed value. Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

· Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value. Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.

2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)

- Please preheat it at 150°C between 60 and 120 seconds.
- Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.

3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

2. Storage

- 1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- 2) Follow the precautions printed on the packing label of the device for transportation and storage.
- 3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.
- 4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- 5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- 6) When restoring devices after removal from their packing, use anti-static containers.
- 7) Do not allow loads to be applied directly to devices while they are in storage.
- 8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

Not Recommended
for New Design

EN 60747-5-5 Option:(V4)

Types : TLP291

 Type designations for “option: (V4)”, which are tested under EN 60747 requirements.

(e.g.): TLP291 (V4GB-TP,E V4 : EN 60747 option
 GB: CTR rank type
 TP : Standard tape & reel type
 E : [[G]]/RoHS COMPATIBLE (Note4)

Note: Use TOSHIBA standard type number for safety standard application.
 (e.g.): TLP291(V4GB-TP,E → TLP291

Note4: Please contact your Toshiba sales representative for details on environmental information such as the product’s RoHS compatibility.
 RoHS is the Directive 2011/65/EU of the European Parliament and of the Council 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronics equipment.

EN 60747 Isolation Characteristics

Description	Symbol	Rating	Unit
Application classification for rated mains voltage $\leq 150\text{Vrms}$ for rated mains voltage $\leq 300\text{Vrms}$		I-IV I-III	-
Climatic classification		55 / 110 / 21	-
Pollution degree		2	-
Maximum operating insulation voltage	VIORM	707	Vpk
Input to output test voltage, Method A Vpr=1.6 × VIORM, type and sample test tp=10s, partial discharge<5pC	Vpr	1132	Vpk
Input to output test voltage, Method B Vpr=1.875 × VIORM, 100% production test tp=1s, partial discharge<5pC	Vpr	1325	Vpk
Highest permissible overvoltage (transient overvoltage, tpr=60s)	VTR	6000	Vpk
Safety limiting values (max. permissible ratings in case of fault, also refer to thermal derating curve) current (input current: IF, P _{Si} =0mW) power (output or total power dissipation) temperature	I _{Si} P _{Si} T _{Si}	250 400 150	mA mW °C
Insulation resistance V _{IO} =500V, T _a =T _{Si}	R _{Si}	$\geq 10^9$	Ω

Insulation Related Specifications

Minimum creepage distance	Cr	5.0mm
Minimum clearance	Cl	5.0mm
Minimum insulation thickness	ti	0.4mm
Comparative tracking index	CTI	175

1. If a printed circuit is incorporated, the creepage distance and clearance may be reduced below this value. (e.g. at a standard distance between soldering eye centers of 3.5mm).
If this is not permissible, the user shall take suitable measures.
2. This photocoupler is suitable for 'safe electrical isolation' only within the safety limit data.
Maintenance of the safety data shall be ensured by means of protective circuit.

VDE test sign: Marking on product
for EN 60747

: Marking on packing
for EN 60747



Marking Example: TLP291

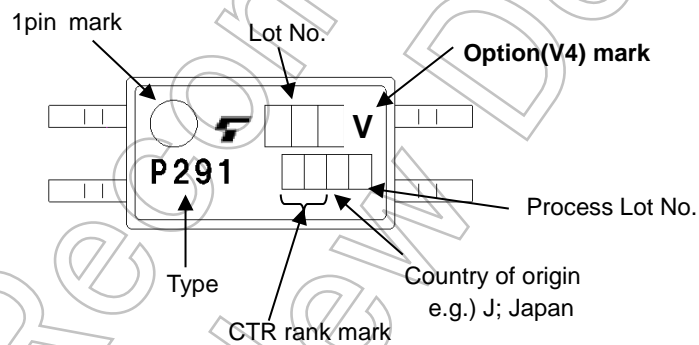


Figure 1 Partial discharge measurement procedure according to EN 60747
Destructive test for qualification and sampling tests.

Method A

(for type and sampling tests,
destructive tests)

- t_1, t_2 = 1 to 10 s
- t_3, t_4 = 1 s
- t_p (Measuring time for partial discharge) = 10 s
- t_b = 12 s
- t_{inj} = 60 s

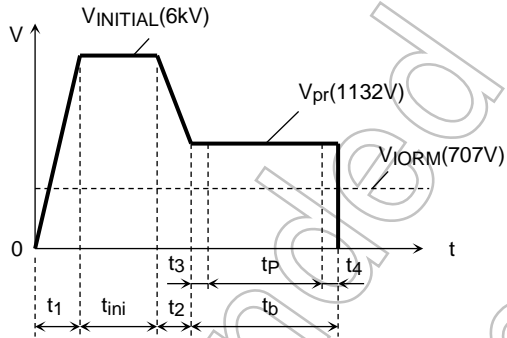


Figure 2 Partial discharge measurement procedure according to EN 60747
Non-destructive test for 100% inspection.

Method B

(for sample test, non-destructive test)

- t_3, t_4 = 0.1 s
- t_p (Measuring time for partial discharge) = 1 s
- t_b = 1.2 s

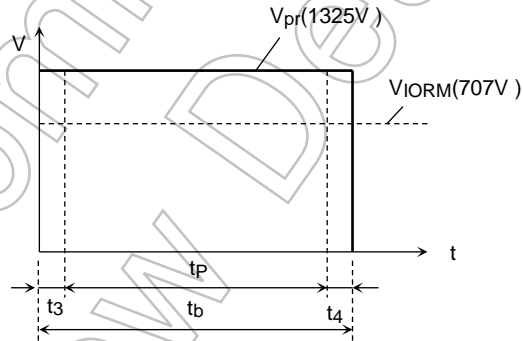
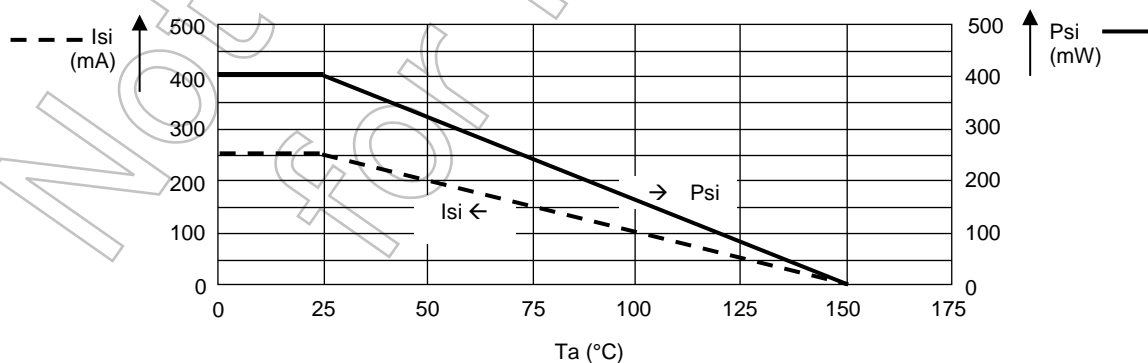


Figure 3 Dependency of maximum safety ratings on ambient temperature



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