TOSHIBA Photocoupler IRED & Photo-Triac

# **TLP3082(S)**

Office Machine Household Use Equipment Triac Driver Solid State Relay

The TOSHIBA TLP3082(S) consists of a zero voltage crossing turn-on photo-triac optically coupled to an infrared emitting diode in a six lead plastic DIP package.

#### **Features**

- Peak off-state voltage: 800 V (min)
- Trigger LED current: 10 mA (max)
- On-state current: 100 mA (max)
- Isolation voltage: 5000 Vrms (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349
- VDE-approved : EN 60747-5-5 , EN 62368-1 (Note 1)

Unit: mm

6

7.12±0.25

7.62±0.25

0.5±0.1

1.2±0.15

2.54±0.25

JEDEC

JEITA

TOSHIBA

11-7A9S

Weight: 0.39 g (Typ.)

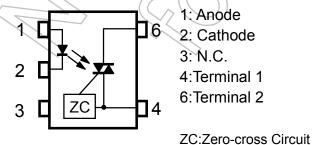
Note1: When a VDE approved type is needed, please designate the **Option(D4)**.

Note: When specifying the application type name for certification testing, be sure to use the standard product type name, e.g. TLP3082

· Construction mechanical rating

	7.62 mm pitch standard type	10.16 mm pitch TLPXXXF type
Creepage distance	7.0 mm (min)	8.0 mm (min)
Clearance	7.0 mm (min)	8.0 mm (min)
Insulation thickness	0.4 mm (min)	0.4 mm (min)

## Pin configuration (top view)



Start of commercial production 2007-01



#### Absolute Maximum Ratings (Ta = 25°C)

	Characteristic		Symbol	Rating	Unit
	Forward current			50	mA
	Forward current derating (Ta≥53°C)			-0.7	mA /°C
	Peak forward current (100μs pulse, 100pps)		IFP	A	Α
LED	Reverse voltage		VR	5	\ \ \ \
	Power Dissipation		PD	100	mW
	Power Dissipation Derating (Ta ≥ 53°C)			1.4	mW/°C
	Junction temperature		Τij	125	°C
	Off-state output terminal voltage	VDRM	800	V	
		Ta=25°C		100	
	On-state RMS current	Ta=70°C	T(RMS)	50	mA
.or	On-state current derating (Ta≥25°C)	ΔITY°C	-1.1	mA/°C	
Detector	Peak on-state current (100µs pulse, 120pps)	/ ITP	2 ((	A	
۵	Peak nonrepetitive surge current (Pw=10ms)	TTSM	1.2	(A)	
	Output Power Dissipation	> Po	300	mW	
	Output Power Dissipation Derating (Ta ≥ 25°C)	ΔP <sub>O</sub> /°C	(3.3)	mW/°C	
	Junction temperature	Tj (	115	°C	
Stor	rage temperature range	T <sub>stg</sub>	-55 to 125	°C	
Ope	erating temperature range	Topr	-40 to 100	°C	
Lea	d soldering temperature (10 s)	T <sub>sol</sub>	260	°C	
Isola	ation voltage (AC,60 s, R.H. ≤60 %)	BVS	5000	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1) Device considered a two terminal device: Pins1, 2 and 3 shorted together and pin4 and pin6 shorted together.

### **Recommended Operating Conditions**

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	VAC	_	_	240	Vac
Forward current	lF	15	20	25	mA
Peak on-state current	ITP	_	_	1	Α
Operating temperature	Topr	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the devices. Each item also has its own independent guideline document. In developing designs using these products, please confirm the specified characteristics shown in these documents.

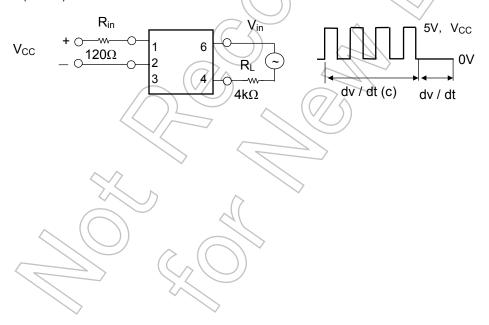
## **Electrical Characteristics (Ta = 25°C)**

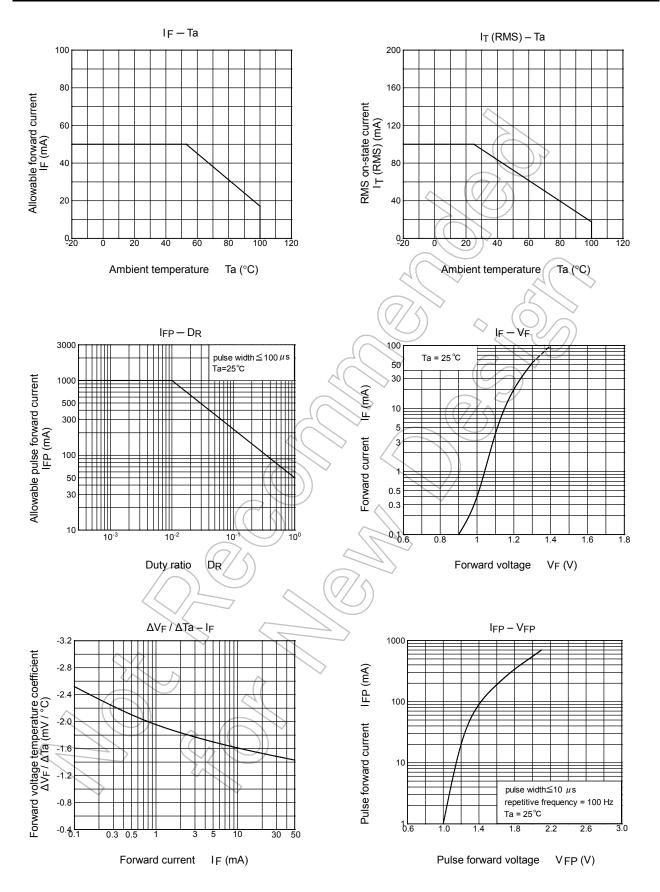
	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	I <sub>F</sub> = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I <sub>R</sub>	V <sub>R</sub> = 5 V	-	_	10	μΑ
	Capacitance	Ст	V = 0 V, f = 1 MHz	_<	30	-	pF
	Peak off-state current	I <sub>DRM</sub>	V <sub>DRM</sub> = 800 V	- /	10	1000	nA
_	Peak on-state voltage	V <sub>TM</sub>	I <sub>TM</sub> = 100 mA		1.7	3.0	V
Detector	Holding current	lΗ	_	(7)	0.6	-	mA
Det	Critical rate of rise of off-state voltage	dv/dt	Vin = 240 Vrms , Ta = 85 °C (Note 2)	200	500	_	V/μs
	Critical rate of rise of commutating voltage	dv/dt(c)	Vin = 60 Vrms , I <sub>T</sub> = 15 mA (Note 2)	<del>\</del>	0.2	-	V/μs

# **Coupled Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Trigger LED current	lfT	V <sub>T</sub> = 3 V	_	5	10//	mA
Inhibit voltage	ViH	IF = Rated IFT	-(~		50	V
Leakage in inhibited state	Іін	I <sub>F</sub> = Rated I <sub>FT</sub> , V <sub>T</sub> = Rated V <sub>DRM</sub>	70	200	600	μΑ
Capacitance (input to output)	Cs	V <sub>S</sub> = 0 V , f = 1 MHz		0.8	1	pF
Isolation resistance	Rs	V <sub>S</sub> = 500 V, R.H.≤60 %	1×10 <sup>12</sup>	10 <sup>14</sup>	_	Ω
Isolation voltage	BVs	AC, 60 s	5000	_	_	Vrms

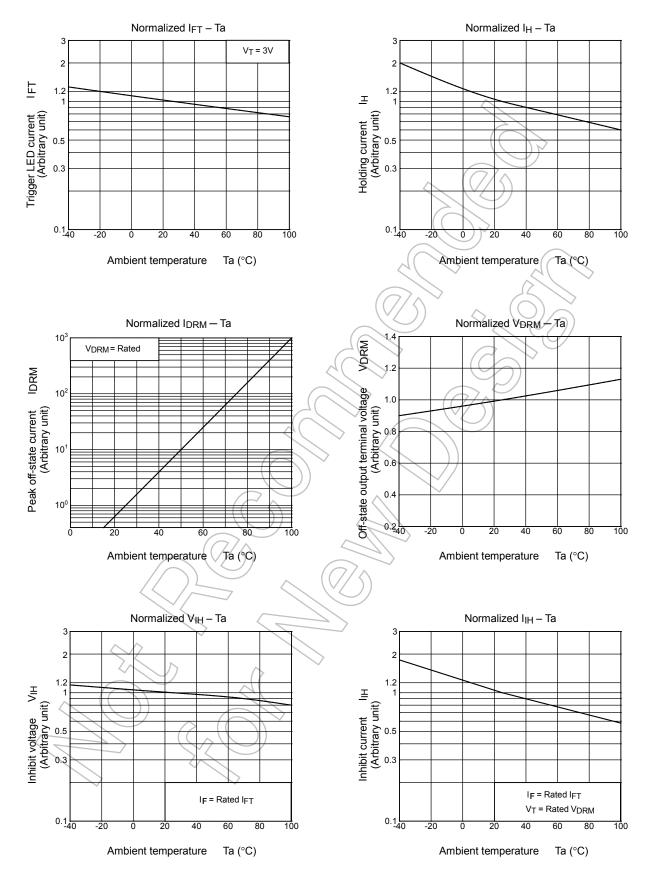
(Note 2) dv / dt test circuit





<sup>\*</sup> The above graphs show typical characteristics.

NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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