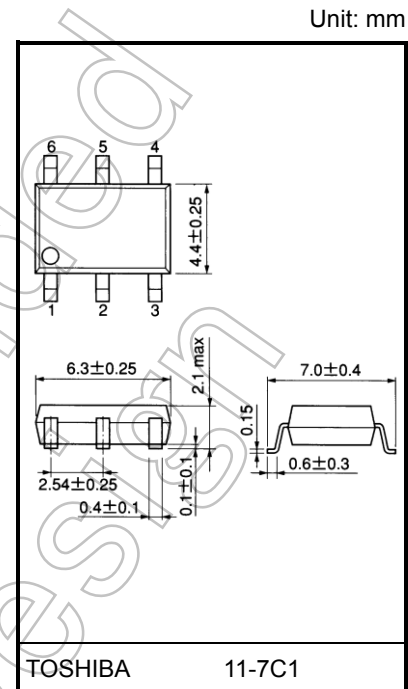


## TLP3120

High-Speed Memory Tester  
 High-Speed Logic Tester  
 High-Frequency Measurement Equipment

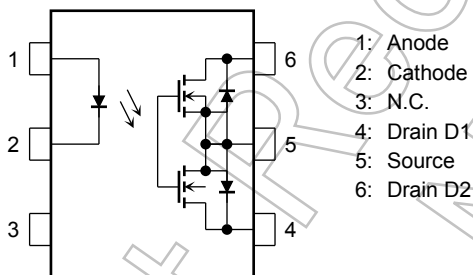
The Toshiba TLP3120 consists of an infrared emitting diode optically coupled to a photo-MOSFET in a SOP, which is suitable for surface mount assembly.

- 6-pin SOP (2.54SOP6): 2.1 mm high, 2.54 mm pitch
- Normally opened (form A) device
- Peak OFF-state voltage: 80 V (min)
- Trigger LED current: 5 mA (max)
- ON-state current: 1.25 A (max)
- ON-state resistance: 0.15 Ω (max)
- Capacitance between output terminals: 1000 pF (max)
- Isolation voltage: 1500 V<sub>rms</sub> (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A  
 File No.E67349



Weight: 0.13 g (typ.)

### Pin Configuration (top view)



Start of commercial production  
 2000-12

## Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Led	Forward current	I <sub>F</sub>	50	mA
	Forward current derating (Ta ≥ 25°C)	ΔI <sub>F</sub> /°C	-0.5	mA/°C
	Reverse voltage	V <sub>R</sub>	5	V
	Diode power dissipation	P <sub>D</sub>	50	mW
	Diode power dissipation derating (Ta ≥ 25°C)	ΔP <sub>D</sub> /°C	-0.5	mW/°C
	Junction temperature	T <sub>j</sub>	125	°C
Detector	OFF-state output terminal voltage	V <sub>OFF</sub>	80	V
	ON-state current	I <sub>ON</sub>	1.25	A
	ON-state current derating (Ta ≥ 25°C)	ΔI <sub>ON</sub> /°C	-12.5	mA/°C
	Output power dissipation	P <sub>O</sub>	234	mW
	Output power dissipation derating (Ta ≥ 25°C)	ΔP <sub>O</sub> /°C	-2.34	mW/°C
	Junction temperature	T <sub>j</sub>	125	°C
Storage temperature range		T <sub>stg</sub>	-40 to 125	°C
Operating temperature range		T <sub>opr</sub>	-20 to 85	°C
Lead soldering temperature (10 s)		T <sub>sol</sub>	260	°C
Isolation voltage (AC, 60 s, R.H. ≤ 60 %) (Note 1)		BVS	1500	V <sub>rms</sub>

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device is considered as a two-terminal device. LED side pins are shorted together and detector side pins are shorted together.

## Recommended Operating Conditions

Characteristics	Symbol	Min	Typ.	Max	Unit
Supply voltage	V <sub>DD</sub>	—	—	64	V
Forward current	I <sub>F</sub>	5	—	30	mA
ON-state current	I <sub>ON</sub>	—	—	1.25	A
Operating temperature	T <sub>opr</sub>	25	—	60	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

## Individual Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
LED	Forward current	V <sub>F</sub>	I <sub>F</sub> = 10 mA	1.0	1.15	1.3	V
	Reverse current	I <sub>R</sub>	V <sub>R</sub> = 5 V	—	—	10	μA
	Capacitance between terminals	C <sub>T</sub>	V <sub>F</sub> = 0 V, f = 1 MHz	—	15	—	pF
Detector	OFF-state current	I <sub>OFF</sub>	V <sub>OFF</sub> = 20 V, Ta = 50 °C	—	1200	1500	pA
	Capacitance between terminals	C <sub>OFF</sub>	V = 0 V, f = 100 MHz	—	460	1000	pF

### Coupled Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Trigger LED current	$I_{FT}$	$I_{ON} = 1.25 \text{ A}$	—	2	5	mA
Return LED current	$I_{FC}$	$I_{OFF} = 10 \mu\text{A}$	0.2	—	—	mA
ON-state resistance	$R_{ON}$	$I_{ON} = 1.25 \text{ A}, I_F = 5 \text{ mA}$	—	0.11	0.15	$\Omega$

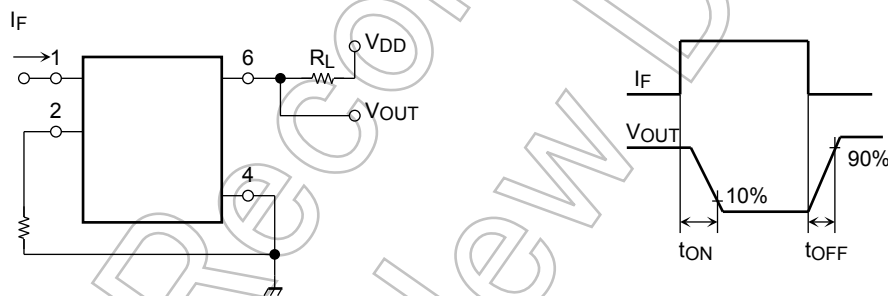
### Isolation Characteristics (Ta = 25°C)

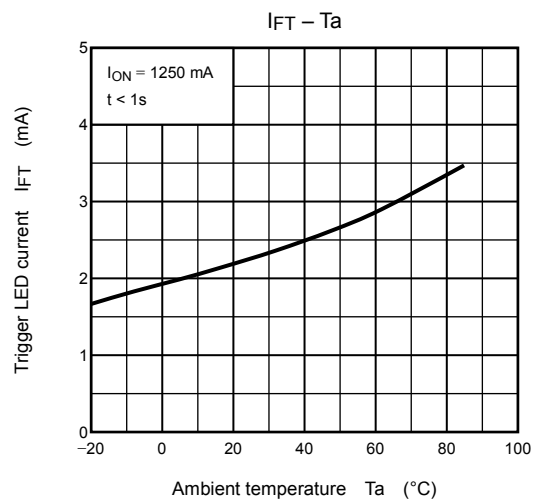
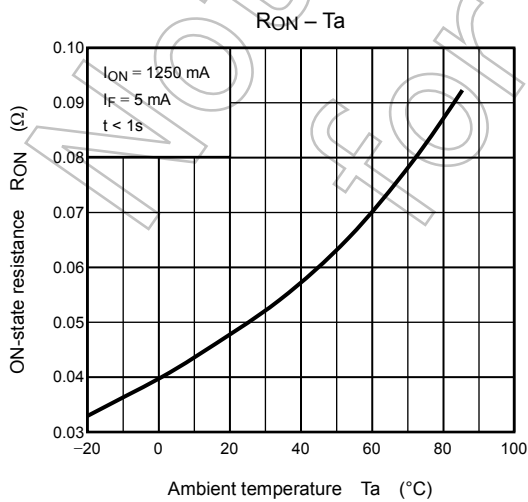
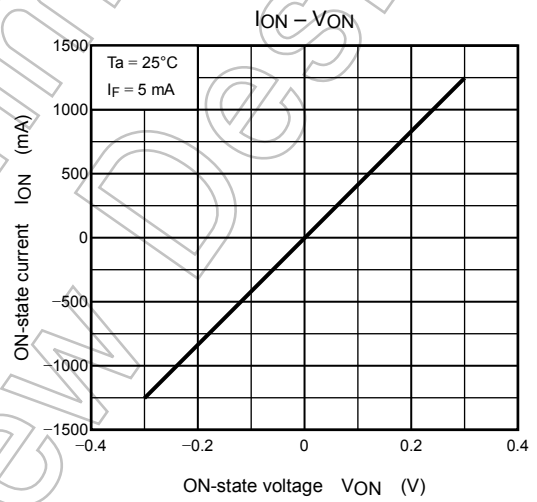
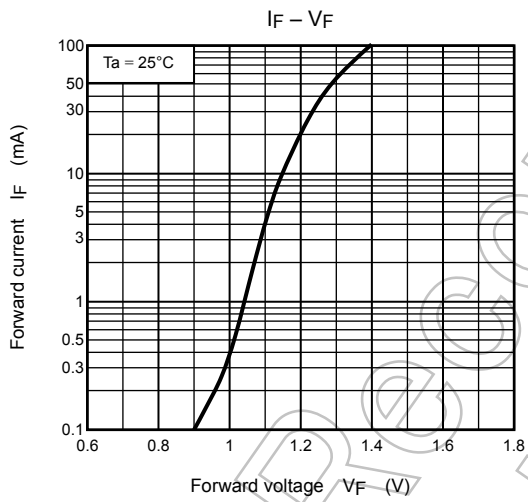
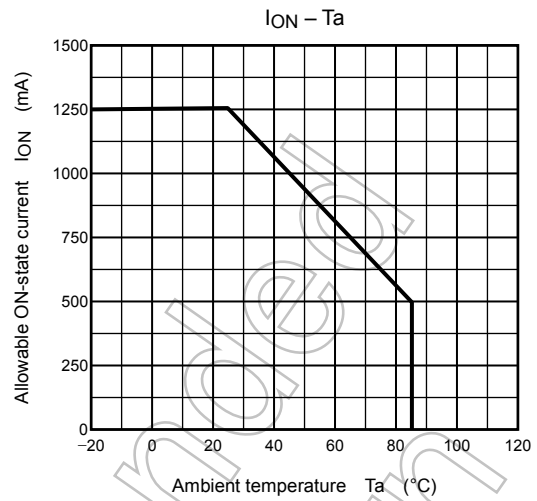
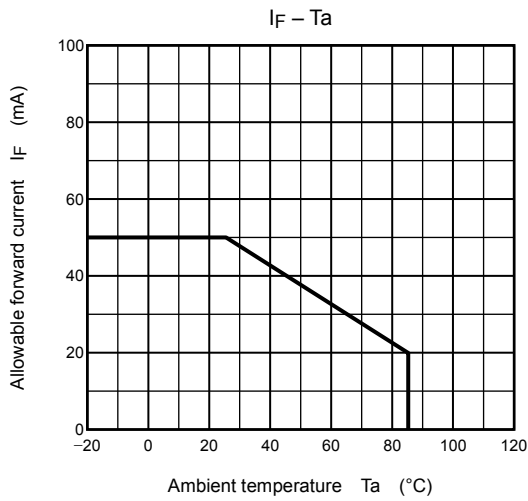
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Capacitance input to output	$C_S$	$V_S = 0 \text{ V}, f = 1 \text{ MHz}$	—	0.8	—	pF
Isolation resistance	$R_S$	$V_S = 500 \text{ V}, \text{R.H.} \leq 60 \%$	$5 \times 10^{10}$	$10^{14}$	—	$\Omega$
Isolation voltage	$BV_S$	AC, 60 s	1500	—	—	Vrms

### Switching Characteristics (Ta = 25°C)

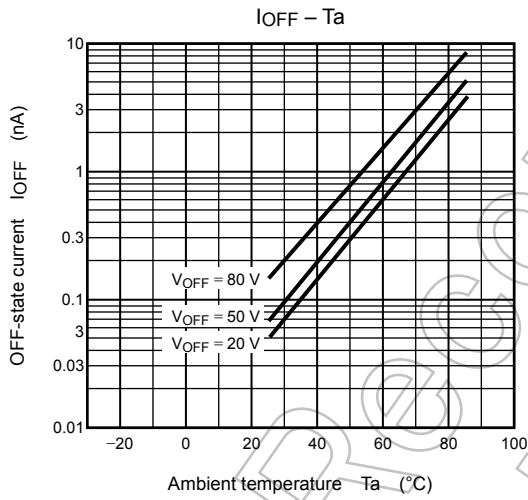
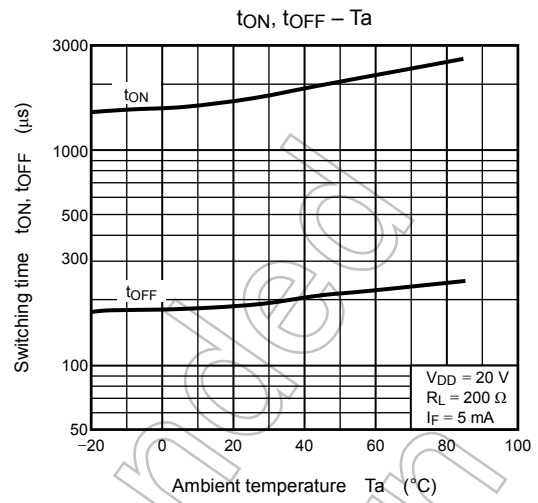
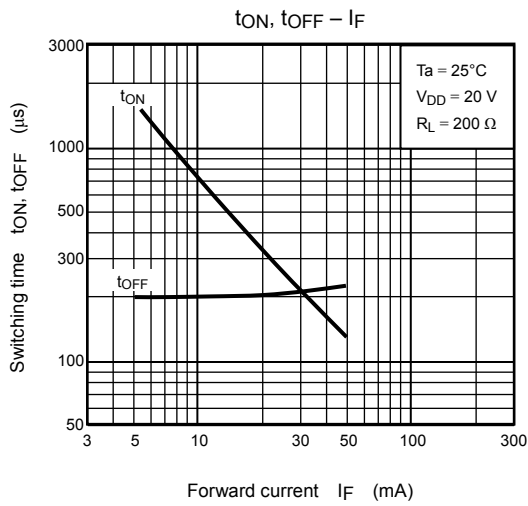
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Turn-ON time	$t_{ON}$	$R_L = 200 \Omega$	—	2.0	3.0	ms
Turn-OFF time	$t_{OFF}$	$V_{DD} = 20 \text{ V}, I_F = 5 \text{ mA}$ (Note 2)	—	0.7	1.0	

Note 2: Switching time test circuit





NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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