TOSHIBA Photocoupler IRED & Photo-Transistor

# TLP620, TLP620-2, TLP620-4

Programmable Controllers
AC / DC-Input Module
Telecommunication

The TOSHIBA TLP620, -2 and -4 consists of a photo–transistor optically coupled to two infrared emitting diode connected in inverse parallel.

The TLP620–2 offers two isolated channels in an eight lead plastic DIP, while the TLP620–4 provides four isolated channels in a sixteen plastic DIP

- Collector-emitter voltage: 55V (min.)
- Current transfer ratio: 50% (min.) Rank GB: 100% (min.)
- Isolation voltage: 5000V<sub>rms</sub> (min.)
- · Safety Standards
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349
- VDE-approved: EN 60747-5-5 (Note 1)

Note 1: When an VDE approved type is needed, please designate the **Option(D4)**.

7.62 mm pitch 10.16 mm pitch standard type TLP×××F type

• Creepage distance

Clearance

: 6.4 mm (min)

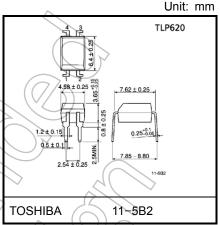
8.0 mm (min)

Insulation thickness

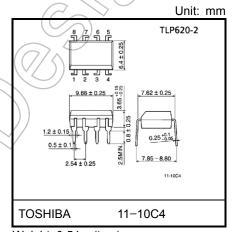
: 0.4 mm (min)

: 6.4 mm (min)

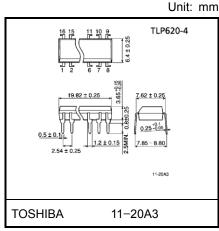
0.4 mm (min)



Weight: 0.26 g (typ.)



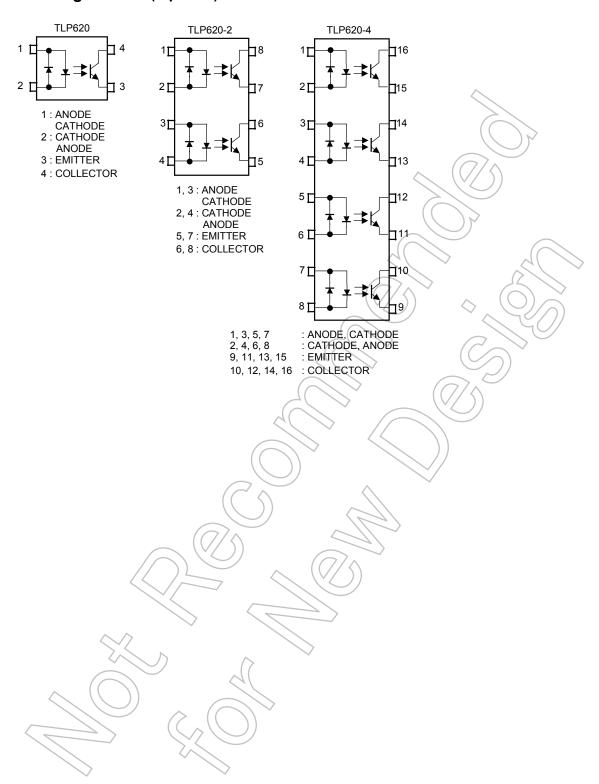
Weight: 0.54 g (typ.)



Weight: 1.1 g (typ.)

Start of commercial production 1984-01

### Pin Configurations (top view)



#### **Absolute Maximum Ratings (Ta = 25°C)**

			Rat		
	Characteristic	Symbol	TLP620	TLP620-2 TLP620-4	Unit
	Forward current	IF (RMS)	60	50	mA
	Forward current derating	ΔIF / °C	–0.7 (Ta ≥ 39°C)	–0.5 (Ta ≥ 25°C)	mA / °C
	Pulse forward current	lfP	±1 (100µs pulse, 100pps)		Α
LED	Power dissipation (1 circuit)	PD	100	70	mW
	Power dissipation derating (1 circuit)	ΔP <sub>D</sub> / °C	–1.2 (Ta ≥ 39°C)	–0.7 (Ta ≥ 25°C)	mW / °C
	Junction temperature	Tj	12	25	°C
	Collector-emitter voltage	VCEO	(5	5	V
	Emitter-collector voltage	VECO	V		V
٦٢	Collector current	Ic	50		mA
Detector	Collector power dissipation (1 circuit)	PC	150	100	mW
	Collector power dissipation derating (1 circuit) (Ta ≥ 25°C)	ΔP <sub>C</sub> / °C	-1.5	-1.0	mW / °C
	Junction temperature	Tj	12	25	°C
Storage temperature range		T <sub>stg</sub>	-55	-125	°C
Operating temperature range		Topr	-55	100	°C
Lead soldering temperature		T <sub>sold</sub>	260 (	(10 s)	°C
Total package power dissipation (1 circuit)		Рт	250	150	mW
Total package power dissipation derating (Ta ≥ 25°C, 1 circuit)		ΔPT/°C	-2.5	-1.5	mW / °C
Isolation voltage (Note1)		BVS	5000 (AC, 60	s., RH ≤ 60 %)	V <sub>rms</sub>

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: Device considered a two terminal device: LED side pins shorted together and detector side pins shorted together.

#### **Recommended Operating Conditions**

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	Vcc	_	5	24	V
Forward current	IF (RMS)	_	16	25	mA
Collector current	Ic	_	1	10	mA
Operating temperature	Topr	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

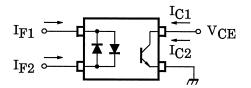
## Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	I <sub>F</sub> = ±10 mA	1,0	1.15	1.3	V
E	Forward current	lF	V <sub>F</sub> = ±0.7 V	7	2.5	20	μΑ
	Capacitance	Ст	V = 0 V, f = 1 MHz		60		pF
	Collector–emitter breakdown voltage	V (BR) CEO	I <sub>C</sub> = 0.5 mA	55	_	1	٧
ctor	Emitter–collector breakdown voltage	V (BR) ECO	IE = 0:1 mA	7	ı	1	٧
Detector	Collector dark current	ICEO	V <sub>CE</sub> = 24 V	-	10	100	nA
	Collector dark current		V <sub>CE</sub> = 24 V, Ta = 85 °C	I	2	50	μA
	Capacitance (collector to emitter)	CCE	V <sub>CE</sub> = 0 V, f = 1 MHz	_	10	_	pF

# Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Current transfer ratio	IC / IF = ±5 mA, VCE = 5 V	IF = ±5 mA, V <sub>CE</sub> = 5 V	50	_	600	%
Current transfer fatto		Rank GB	100	_	600	%
Saturated CTR	IC / IF (sat)	IF = ±1 mA, V <sub>CE</sub> = 0.4 V Rank GB	_	60	_	%
Saturated CTR			30	_	_	70
		IC = 2.4 mA, IF = ±8 mA	_	_	0.4	
Collector–emitter saturation voltage	VCE (sat)	$I_C = 0.2 \text{ mA}, I_F = \pm 1 \text{ mA}$ Rank GB	_	0.2	_	V
			_	_	0.4	
Off-state collector current	IC (off)	$V_F = \pm 0.7 \text{ V}, V_{CE} = 24 \text{ V}$	_	1	10	μΑ
CTR symmetry (Note.)	IC (ratio)	$I_C (I_F = -5 \text{ mA}) / I_C (I_F = +5 \text{ mA})$	0.33	_	3	_

Note :  $I_{C(ratio)} = \frac{I_{C2}(I_F = I_{F2}, V_{CE} = 5V)}{I_{C1}(I_F = I_{F1}, V_{CE} = 5V)}$ 



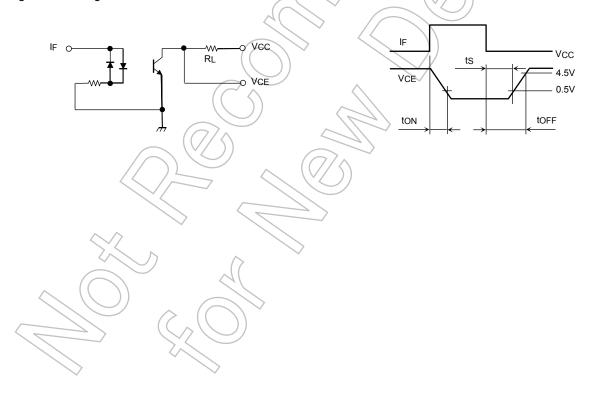
## **Isolation Characteristics (Ta = 25°C)**

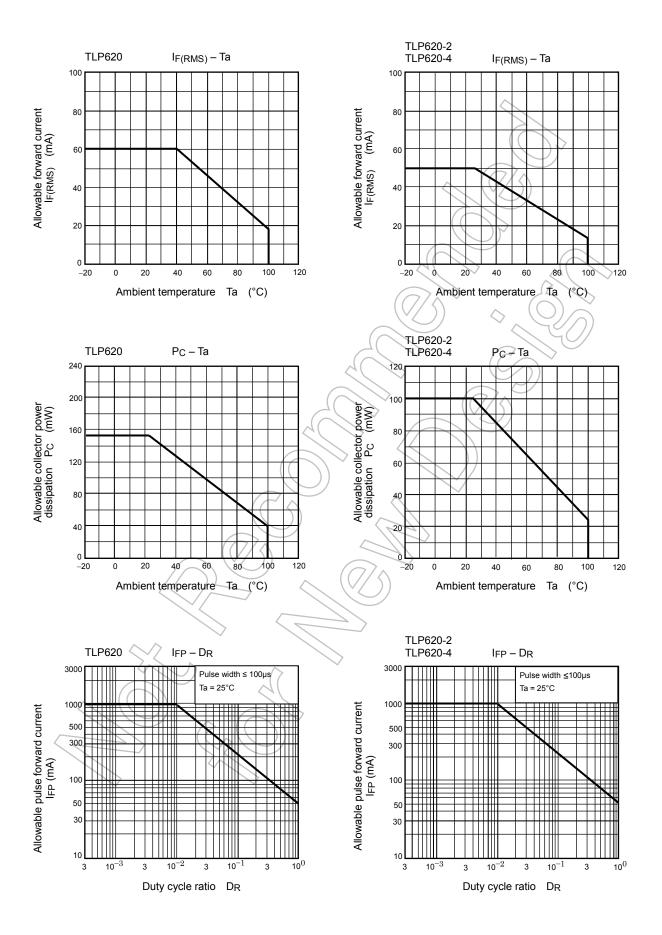
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	Cs	V <sub>S</sub> = 0 V, f = 1 MHz	-	0.8	_	pF
Isolation resistance	Rs	V <sub>S</sub> = 500 V, R.H. ≤ 60 %	1×10 <sup>12</sup>	10 <sup>14</sup>	_	Ω
Isolation voltage	BVS	AC, 60 s	5000	_	_	V <sub>rms</sub>

## **Switching Characteristics (Ta = 25°C)**

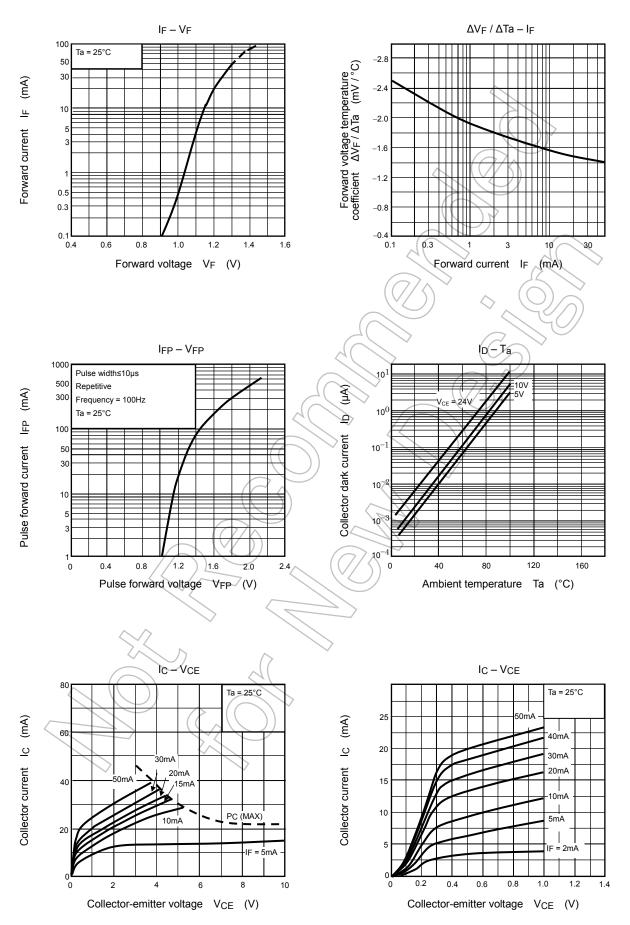
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	tr	(( )>	_	2	_	
Fall time	tf	Vcc = 10 V	_	3	_	
Turn-on time	ton	$I_C = 2 \text{ mA}$ $R_L = 100 \Omega$	_	(3	þ	μs
Turn-off time	toff		-02	3		
Turn-on time	ton		+(	))2	_	
Storage time	ts	$R_L = 1.9 \text{ k}\Omega$ (Fig.1) VCC = 5 V, IF = ±16 mA	7	(15)/	_	μs
Turn-off time	toff		7	25	1	

Fig. 1 Switching time test circuit

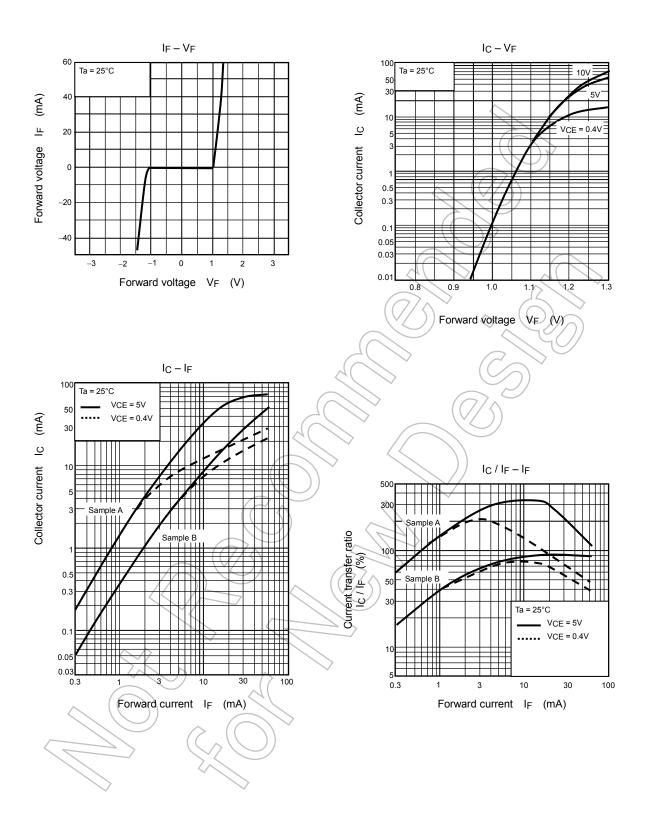




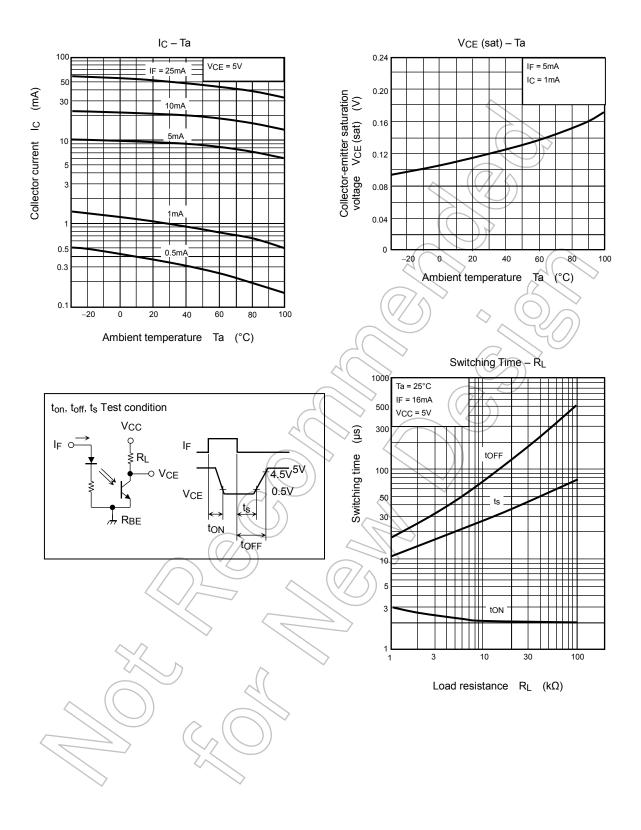
NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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