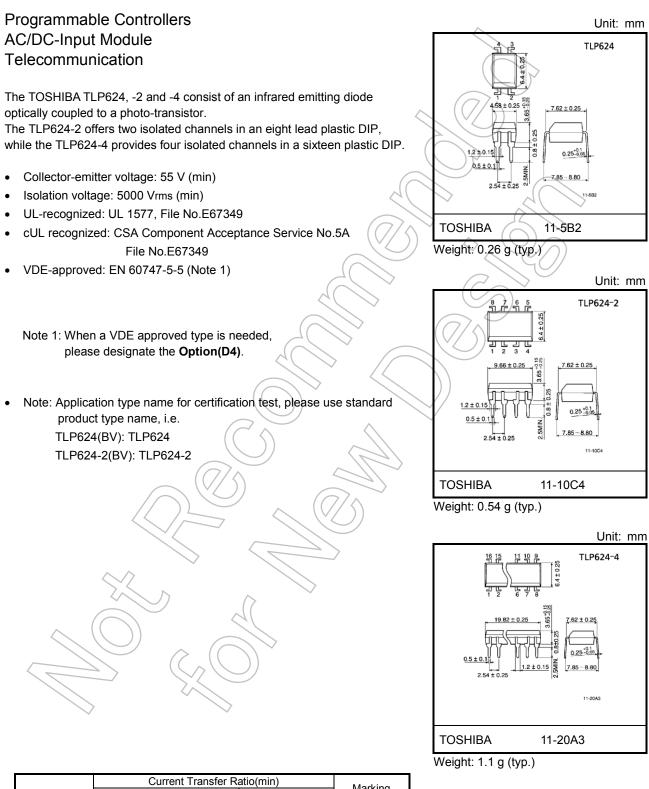
TOSHIBA Photocoupler IRED & Photo-Transistor

# TLP624, TLP624-2, TLP624-4

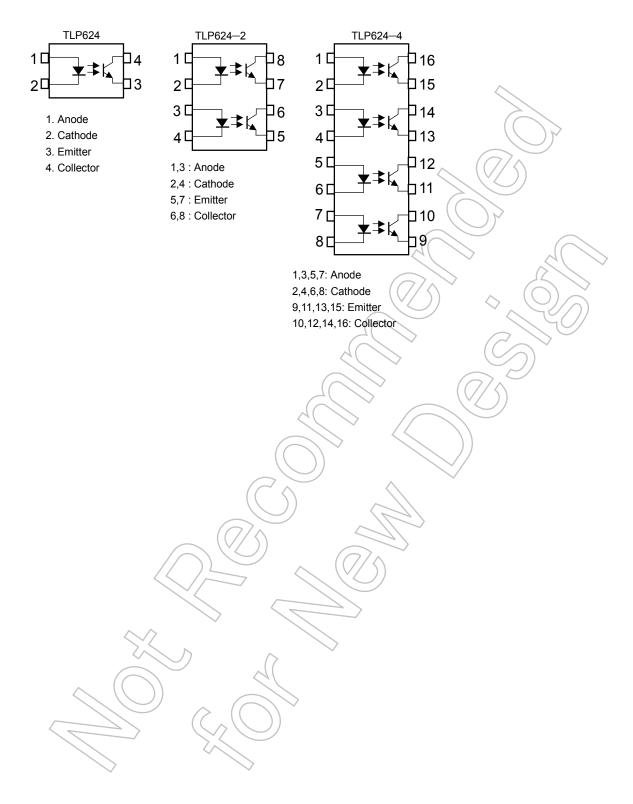


Classification	Cur	Marking		
	Ta = 25°C		Ta=-25 to 75°C	Marking
Classification	IF=1mA VCE=0.5V	IF=0.5mA V <sub>CE</sub> =1.5V	I <sub>F</sub> =1mA V <sub>CE</sub> =0.5V	classification
Rank BV	200%	100%	100%	BV
Standard	100%	50%	50%	BV, blank

Start of commercial production 1986-04

## TOSHIBA

## Pin Configurations (top view)



#### Absolute Maximum Ratings (Ta = 25°C)

			Ra	ting	
	Characteristic	Symbol	TLP624	TLP624-2 TLP624-4	Unit
	Forward current	١ <sub>F</sub>	60	50	mA
	Forward current detating	ΔI <sub>F</sub> / °C	-0.7(Ta ≥ 39°C)	-0.5(Ta ≥ 25°C)	mA / °C
	Pulse forward current	IFP	1 (100μs, pι	llse, 100pps)	А
LED	Diode Power dissipation	PD	100	70	mW
	Diode Power dissipation derating	ΔP <sub>D</sub> / °C	-1.2(Ta ≥ 39°C)	-0.7(Ta ≥ 25°C)	mW / °C
	Reverse voltage	V <sub>R</sub>		5	V
	Junction temperature	Тј		25	°C
	Collector-emitter voltage	V <sub>CEO</sub>	5	5	V
	Emitter-collector voltage	VECO		7	V
Detector	Collector current	.€	5		mA
Dete	Collector power dissipation (1 circuit)	PG	150	100	mW
	Collector power dissipation derating (Ta $\ge$ 25°C, 1 circuit)	∆Pc1°C	-1.5	_10	mW / °C
	Junction temperature	Тј		25	°C
Stor	age temperature range	T <sub>stg</sub>	-55 t	o 125	°C
Ope	rating temperature range	Popr	-55 t	o 100	°C
Lea	d soldering temperature	T <sub>sol</sub>	260	(10 s)	°C
Tota	al package power dissipation (1 circuit)	PT	250	150	mW
Tota	al package power dissipation derating (Ta ≥ $25^{\circ}$ C, 1 circuit)	ΔPT / °C	-2.5	-1.5	mW / °C
Isola	ation voltage (Note 1)	BVs	5000 (AC, 60	s, R.H.≤60 %)	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two terminal device: LED side pins shorted together, and detector side pins shorted together.

## **Recommended Operating Conditions**

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	Vcc	_	5	24	V
Forward current	IF	_	1.6	20	mA
Collector current	lc	_	1	10	mA
Operating temperature	∼ T <sub>opr</sub>	-25		75	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

**Electrical Characteristics (Ta = 25°C)** 

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	I <sub>F</sub> = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I <sub>R</sub>	V <sub>R</sub> = 5 V	_	—	10	μA
	Capacitance	CT	V = 0 V, f = 1 MHz	-<	30	-	pF
	Collector-emitter breakdown voltage	V(BR)CEO	IC = 0.5 mA	55		1	V
ŗ	Emitter-collector breakdown voltage	V(BR)ECO	IE = 0.1 mA	7		1	V
Detector	Collector dark current	1050	V <sub>CE</sub> = 24 V	$\overline{(7)}$	10	100	nA
De		ICEO	V <sub>CE</sub> = 24 V, Ta = 85 °C	Y),	)2	50	μA
	Capacitance collector to emitter	CCE	V = 0 V, f = 1 MHz	1	12	_	pF

## Coupled Electrical Characteristics (Ta = 25°C)

-	,		$\searrow$		1	
Characteristic	Symbol	Test Condition	Min	Тур	Max	Unit
Current transfer ratio	IC / IF	I <sub>F</sub> = 1 mA, V <sub>CE</sub> = 0.5 V Rank BV	100 200	MC	1200 1200	%
Low input CTR	IC / IF (low)	I <sub>F</sub> = 0.5 mA, V <sub>CE</sub> = 1.5 V Rank BV	50 100	$\mathcal{D}$	_	%
		IC = 0.5 mA, IF = 1 mA	(0/s)	—	0.4	
Collector-emitter saturation voltage	VCE (sat)	IC = 1 mA, IF = 1 mA		0.2	—	V
		Rank BV	))-	—	0.4	

## Coupled Electrical Characteristics (Ta = $-25^{\circ}$ C to $75^{\circ}$ C)

Characteristic	s Symbol	Test Condition	Min	Тур.	Max	Unit
Current transfer ratio	I <sub>F</sub> = 1 mA, V <sub>CE</sub> = 0.5 V	50	_		%	
		Rank BV	100	—	_	70
		IF = 0.5 mA, VCE = 1.5 V	_	50		%
Low input CTR	IC / IF (low)	Rank BV	_	100	_	/0

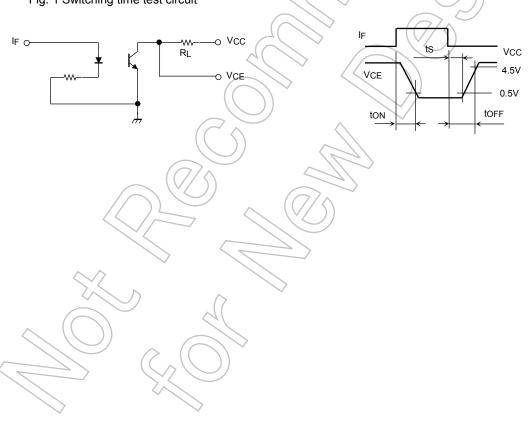
## Isolation Characteristics (Ta = 25°C)

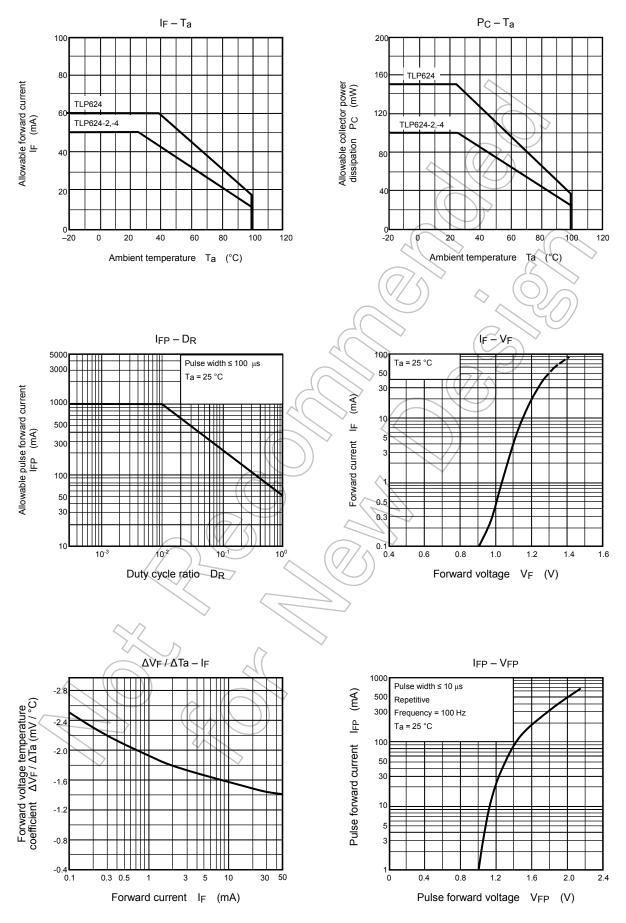
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	CS	V <sub>S</sub> = 0 V, f = 1 MHz	—	0.8	_	pF
Isolation resistance	Rs	V <sub>S</sub> = 500 V, R.H. ≤ 60 %	5×10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
Isolation voltage	BVs	AC, 60 s	5000	/	_	Vrms

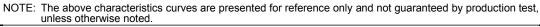
#### Switching Characteristics (Ta = 25°C)

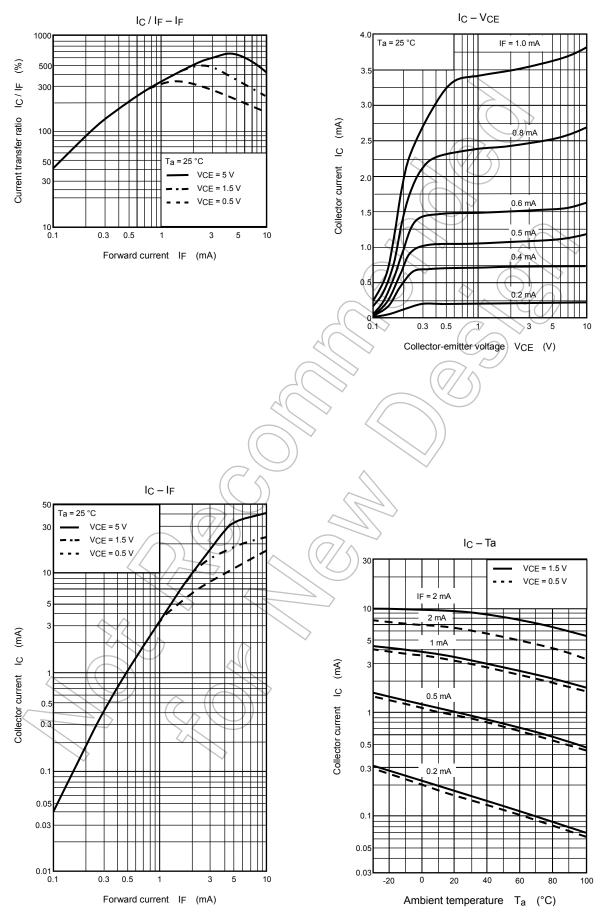
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	tr	V <sub>CC</sub> = 10 V, I <sub>C</sub> = 2 mA R <sub>L</sub> = 100 Ω		8	—	
Fall time	tf		S	8	-	
Turn-on time	t <sub>on</sub>		<u> </u>	10		μs
Turn-off time	toff		~ —	8	<pre></pre>	$\searrow$
Turn-on time	ton	(7/5)	_	10	$\langle - \rangle$	
Storage time	ts	$R_L = 4.7 \text{ k}\Omega \text{ (Fig.1)}$ VCC = 5 V, IF = 1.6 mA		50	$\mathcal{I}_{\mathcal{F}})$	μs
Turn-off time	tOFF			300	1	

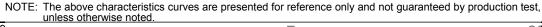
Fig. 1 Switching time test circuit

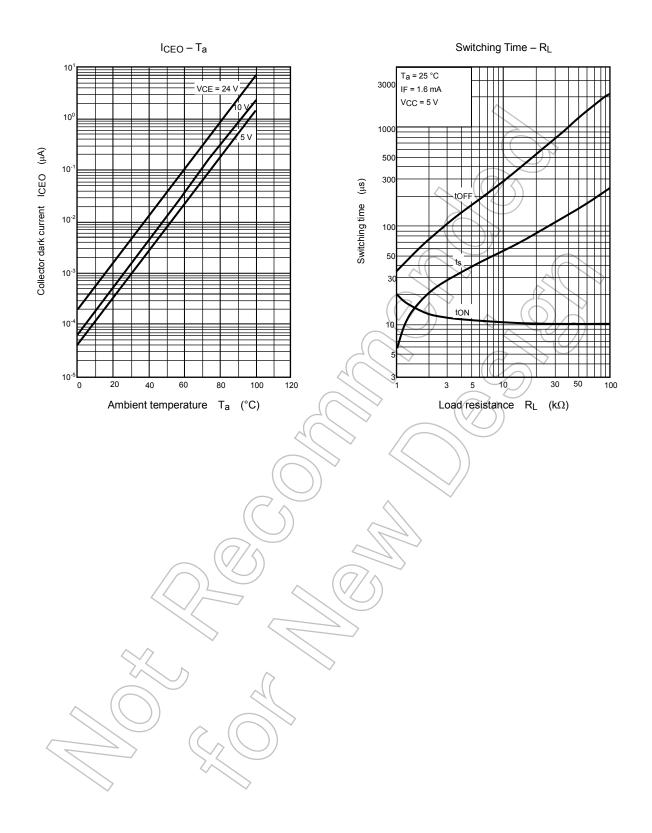












NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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