

# TLP626, TLP626-2, TLP626-4

Programmable Controllers  
AC / DC-Input Module  
Telecommunication

The TOSHIBA TLP626, -2 and -4 consist of two infrared emitting diodes connected in inverse parallel, optically coupled to a photo-transistor. The TLP626-2 offers two isolated channels in an eight lead plastic DIP, while the TLP626-4 provides four isolated channels in a sixteen plastic DIP.

- Collector-emitter voltage: 55 V (min)
- Isolation voltage: 5000 Vrms (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A  
File No.E67349
- VDE-approved: EN 60747-5-5 (Note 1)

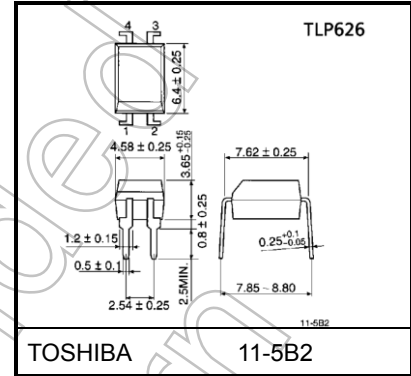
Note 1: When a VDE approved type is needed, please designate the **Option(D4)**.

Current transfer ratio

Classification (Note 1)	Current Transfer Ratio (min)			Marking of Classification
	Ta = 25°C		Ta = -25 to 75°C	
	If = ±1mA VCE = 0.5V	If = ±0.5mA VCE = 1.5V	If = ±1mA VCE = 0.5V	
Rank BV	200%	100%	100%	BV
Standard	100%	50%	50%	BV, blank

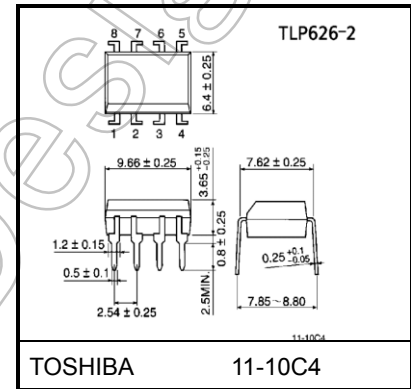
Note 1: Only TLP626 is applied to BV rank items.  
Note: Application type name for certification test, please use standard product type name, i.e. TLP626(BV): TLP626

Unit: mm



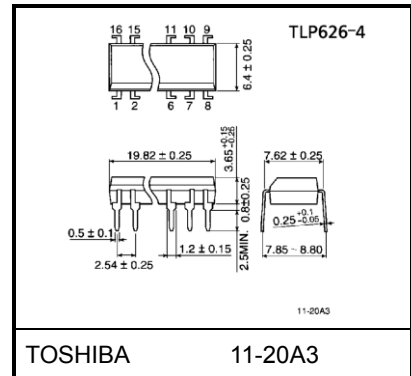
Weight: 0.26 g (typ.)

Unit: mm



Weight: 0.54 g (typ.)

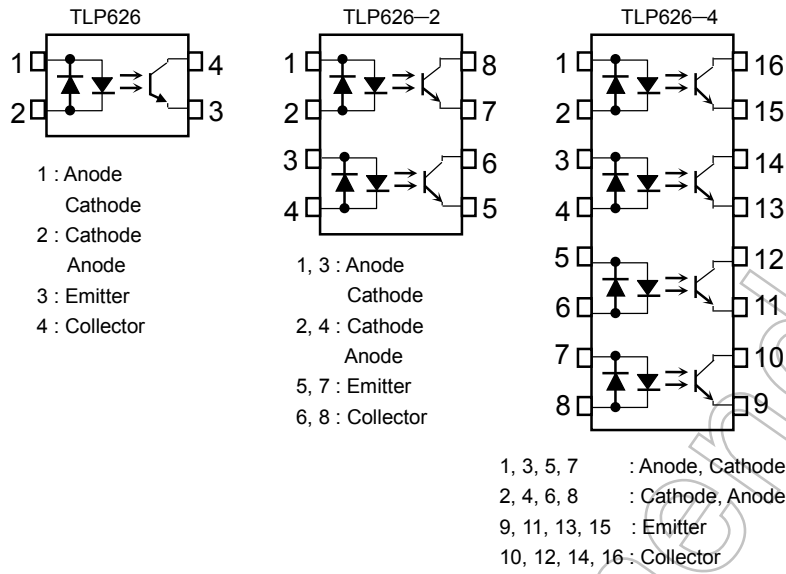
Unit: mm



Weight: 1.1 g (typ.)

Start of commercial production  
1984-04

## Pin Configuration (top view)



Not Recommended for New Design

### Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating		Unit
			TLP626	TLP626-2 TLP626-4	
LED	Forward current	$I_F$	60	50	mA
	Forward current derating	$\Delta I_F / ^\circ\text{C}$	-0.7 (Ta ≥ 39°C)	-0.5 (Ta ≥ 25°C)	mA / °C
	Pulse forward current	$I_{FP}$	1 (100μs pulse, 100pps)		A
	Diode Power dissipation	$P_D$	100	70	mW
	Diode Power dissipation derating	$\Delta P_D / ^\circ\text{C}$	-1.2 (Ta ≥ 39°C)	-0.7 (Ta ≥ 25°C)	mW / °C
	Junction temperature	$T_j$	125		°C
Detector	Collector-emitter voltage	$V_{CEO}$	55		V
	Emitter-collector voltage	$V_{ECO}$	7		V
	Collector current	$I_C$	50		mA
	Collector power dissipation (1 circuit)	$P_C$	150	100	mW
	Collector power dissipation derating (Ta ≥ 25°C, 1 circuit)	$\Delta P_C / ^\circ\text{C}$	-1.5	-1.0	mW / °C
	Junction temperature	$T_j$	125		°C
Storage temperature range		$T_{stg}$	-55 to 125		°C
Operating temperature range		$P_{opr}$	-55 to 100		°C
Lead soldering temperature		$T_{sol}$	260 (10 s)		°C
Total package power dissipation (1 circuit)		$P_T$	250	150	mW
Total package power dissipation derating (Ta ≥ 25°C, 1 circuit)		$\Delta P_T / ^\circ\text{C}$	-2.5	-1.5	mW / °C
Isolation voltage (Note 1)		$BV_S$	5000 (AC, 60 s, R.H. ≤ 60 %)		Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two terminal: LED side pins shorted together, and detector side pins shorted together.

### Recommended Operating Conditions

Characteristic	Symbol	Min	Typ.	Max	Unit
Supply voltage	$V_{CC}$	—	5	24	V
Forward current	$I_{F(RMS)}$	—	1.6	20	mA
Collector current	$I_C$	—	1	10	mA
Operating temperature	$T_{opr}$	-25	—	75	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

### Electrical Characteristics (Ta = 25°C)

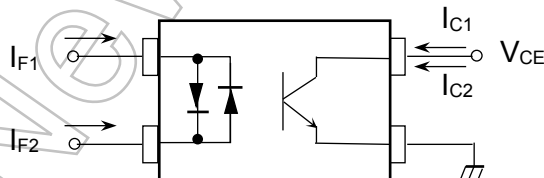
Characteristic		Symbol	Test Condition	Min	Typ.	Max	Unit
LED	Forward voltage	V <sub>F</sub>	I <sub>F</sub> = ±10 mA	1.0	1.15	1.3	V
	Reverse current	I <sub>F</sub>	V <sub>F</sub> = ±0.7 V	—	2.5	20	μA
	Capacitance	C <sub>T</sub>	V = 0 V, f = 1 MHz	—	60	—	pF
Detector	Collector-emitter breakdown voltage	V <sub>(BR)CEO</sub>	I <sub>C</sub> = 0.5 mA	55	—	—	V
	Emitter-collector breakdown voltage	V <sub>(BR)ECO</sub>	I <sub>E</sub> = 0.1 mA	7	—	—	V
	Collector dark current	I <sub>CEO</sub>	V <sub>CE</sub> = 24 V	—	10	100	nA
			V <sub>CE</sub> = 24 V, Ta = 85° C	—	2	50	μA
Capacitance collector to emitter	C <sub>CE</sub>	V = 0 V, f = 1 MHz	—	12	—	pF	

### Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Current transfer ratio	I <sub>C</sub> / I <sub>F</sub>	I <sub>F</sub> = ±1 mA, V <sub>CE</sub> = 0.5 V rank BV(Note 2)	100	—	1200	%
			200	—	1200	
Low input CTR	I <sub>C</sub> / I <sub>F</sub> (low)	I <sub>F</sub> = ±0.5 mA, V <sub>CE</sub> = 1.5 V rank BV(Note 2)	50	—	—	%
			100	—	—	
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = 0.5 mA, I <sub>F</sub> = ±1 mA	—	—	0.4	V
		I <sub>C</sub> = 1 mA, I <sub>F</sub> = ±1 mA	—	0.2	—	
		rank BV(Note 2)	—	—	0.4	
Off-state collector current	I <sub>C(off)</sub>	V <sub>F</sub> = ±0.7 V, V <sub>CE</sub> = 24 V	—	1	10	μA
CTR symmetry (Note 1)	I <sub>C</sub> (ratio)	I <sub>C</sub> (I <sub>F</sub> = -1 mA) / I <sub>C</sub> (I <sub>F</sub> = 1 mA)	0.5	—	2	—

Note 1

$$I_C(\text{ratio}) = \frac{I_{C2}(I_F = I_{F2}, V_{CE} = 5V)}{I_{C1}(I_F = I_{F1}, V_{CE} = 5V)}$$



Note 2: Only TLP626 is applied to BV rank items.

### Coupled Electrical Characteristics (Ta = -25 to 75°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Current transfer ratio	I <sub>C</sub> / I <sub>F</sub>	I <sub>F</sub> = 1 mA, V <sub>CE</sub> = 0.5 V rank BV(Note 1)	50	—	—	%
			100	—	—	
Low input CTR	I <sub>C</sub> / I <sub>F</sub> (low)	I <sub>F</sub> = 0.5 mA, V <sub>CE</sub> = 1.5 V rank BV(Note 1)	—	50	—	%
			—	100	—	

Note 1: Only TLP626 is applied to BV rank items.

## Isolation Characteristics (Ta = 25°C)

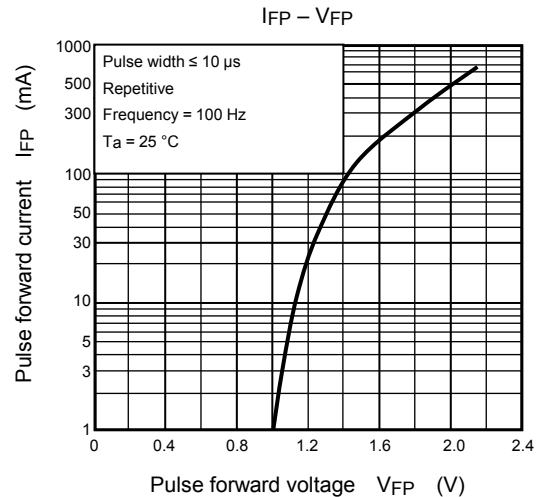
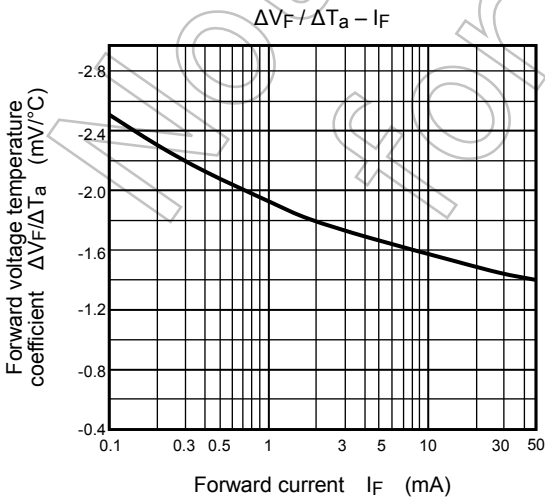
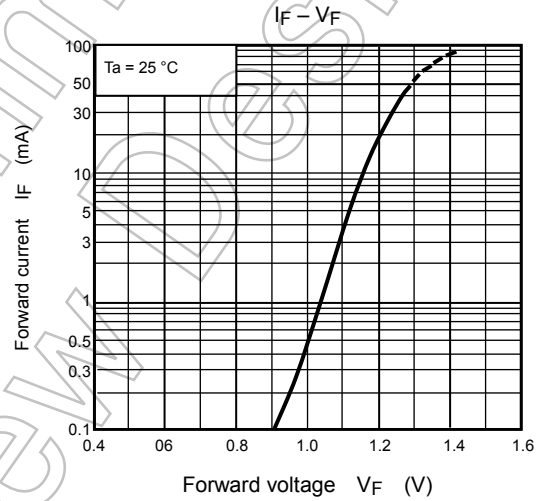
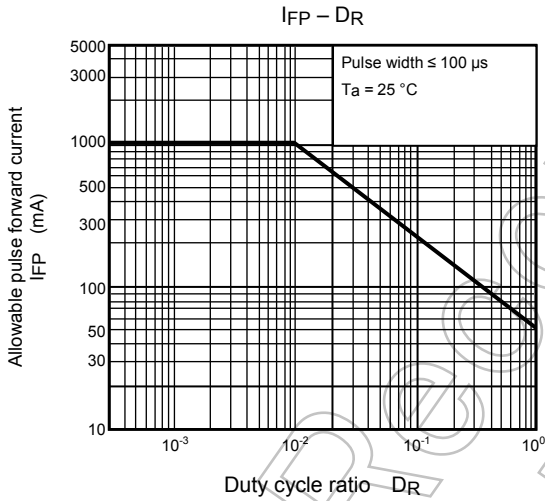
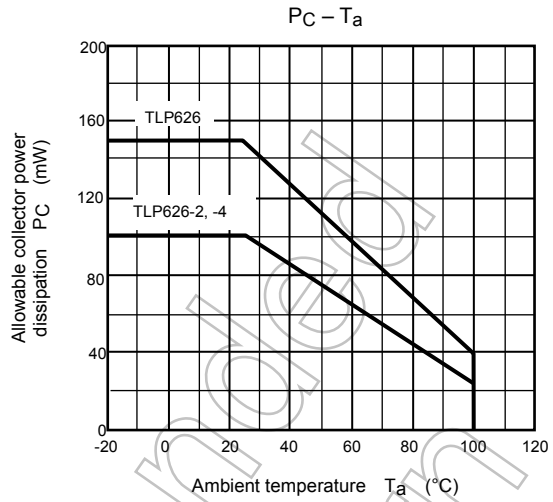
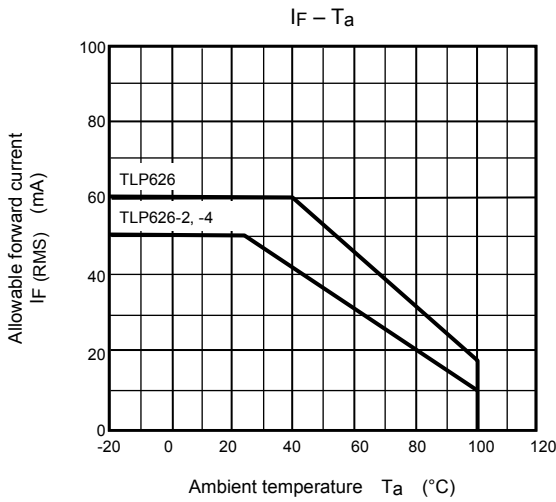
Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Capacitance input to output	C <sub>S</sub>	V <sub>S</sub> = 0 V, f = 1 MHz	—	0.8	—	pF
Isolation resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H. ≤ 60 %	5 × 10 <sup>10</sup>	10 <sup>14</sup>	—	Ω
Isolation voltage	BV <sub>S</sub>	AC, 60 s	5000	—	—	V <sub>rms</sub>

## Switching Characteristics (Ta = 25°C)

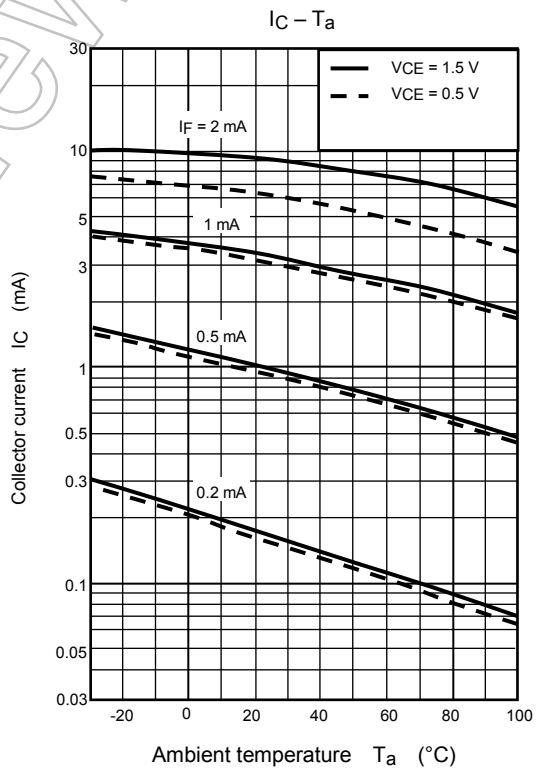
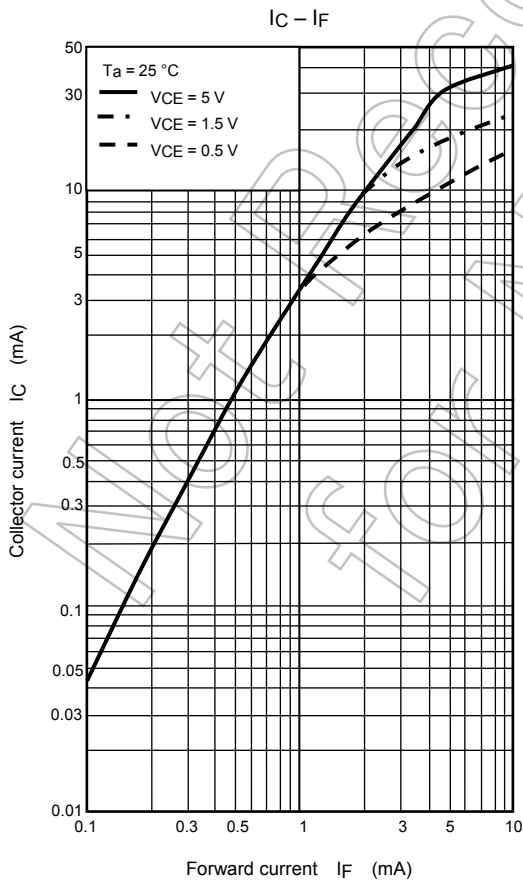
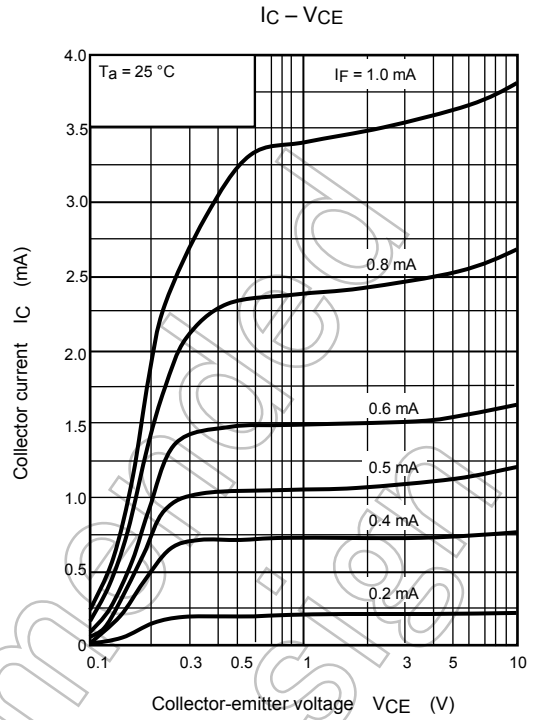
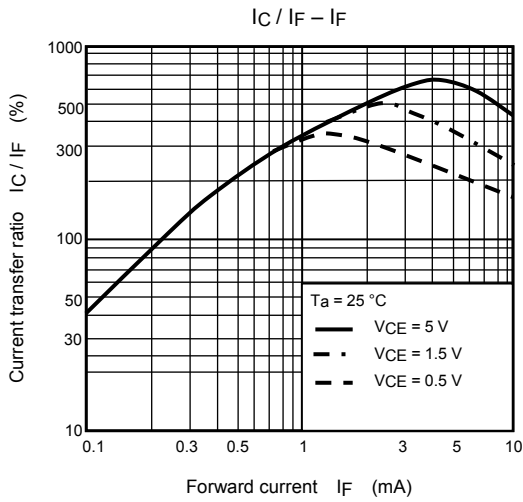
Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Rise time	t <sub>r</sub>	V <sub>CC</sub> = 10 V, I <sub>C</sub> = 2 mA R <sub>L</sub> = 100 Ω	—	8	—	μs
Fall time	t <sub>f</sub>		—	8	—	
Turn-on time	t <sub>on</sub>		—	10	—	
Turn-off time	t <sub>off</sub>		—	8	—	
Turn-on time	t <sub>ON</sub>	R <sub>L</sub> = 4.7 kΩ (Fig.1) V <sub>CC</sub> = 5 V, I <sub>F</sub> = ±1.6 mA	—	10	—	μs
Storage time	t <sub>s</sub>		—	50	—	
Turn-off time	t <sub>OFF</sub>		—	300	—	

Fig. 1: Switching operating conditions

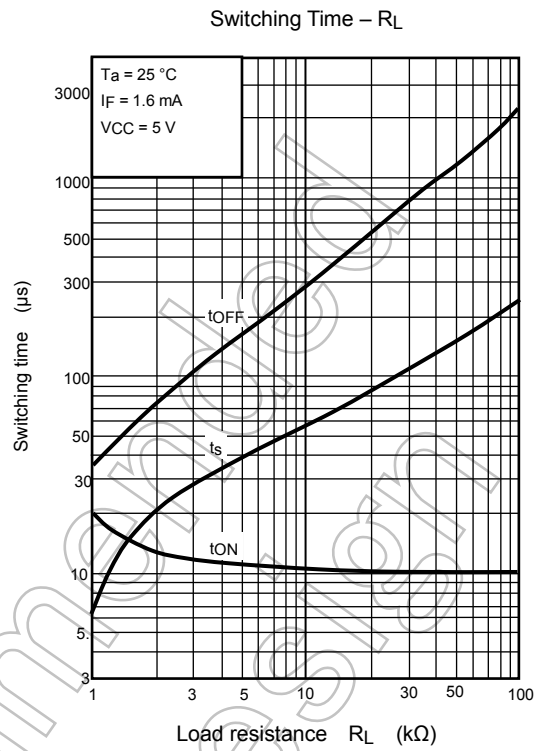
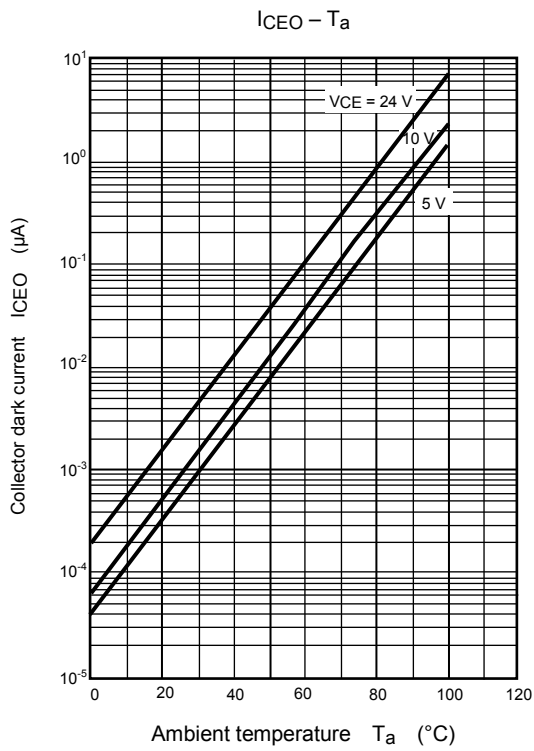




NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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