TOSHIBA Photocoupler IRLED & Photo-Transistor

# **TLX9300**

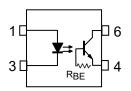
- O Various Controllers
- O Signal transmission between different circuit potential
- HEV (Hybrid Electric Vehicle) and EV (Electric Vehicle) Applications

The TOSHIBA TLX9300 mini-flat photocoupler is suitable for surface-mount assembly. The TLX9300 consists of an infrared LED optically coupled to a photo-transistor.

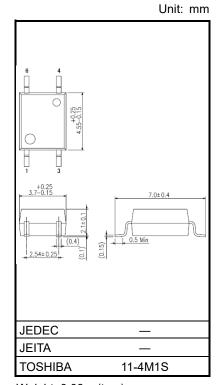
This photocoupler can be used to the extensive applications. It is generic speed transistor output.

- Collector-emitter voltage: 40 V (min)
- Current transfer ratio: 100% (min) to 900%(max)
- Isolation voltage: 3750 Vrms (min)
- AEC-Q101 qualified

#### **Pin Configuration**



- 1: Anode
- 3: Cathode
- 4: Emitter
- 6: Collector



Weight: 0.08 g (typ.)

#### Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25°C)

	Characteristic	Symbol	Rating	Unit
	Forward current	lF	30	mA
	Forward current (Ta=125 °C)	lF	18	mA
	Forward current derating(Ta ≥ 108 °C)	ΔIF/°C	-0.7	mA/°C
LED	Pulse forward current (Note 1)	IFP	1	Α
	Input Power Dissipation	PD	50	mW
	Input Power Dissipation Derating (Ta ≥ 50°C)	ΔPD/°C	-0.5	mW/°C
	Reverse voltage	VR	5	V
	Collector-emitter voltage	V <sub>CEO</sub>	40	V
o	Emitter-collector voltage	V <sub>ECO</sub>	5	V
Detector	Collector current	Ic	50	mA
۵	Collector power dissipation	Pc	150	mW
	Collector power dissipation derating (Ta ≥ 50 °C)	ΔP <sub>C</sub> /°C	-1.5	mW/°C
Оре	Operating temperature range		-40 to 125	°C
Sto	Storage temperature range		-55 to 150	°C
Lead soldering temperature(10 s)		T <sub>sol</sub>	260	°C
Total package power dissipation		PT	200	mW
Tota	Total package power dissipation derating (Ta ≥ 50°C)		-2.0	mW/°C
Isol	ation voltage (AC, 60 s, R.H. ≤ 60 %) (Note 2)	BVS	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pulse width PW  $\leq 100 \mu s$ , f = 100 Hz

Note 2: This device is considered as a two terminal device: Pins 1 and 3 are shorted together, and pins 4 and 6 are shorted together.

# **Recommended Operating Conditions (Note)**

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	Vcc	_	5	24	V
Forward current	lF	_	10	15	mA
Collector current	Ic	_	1	10	mA
Operating temperature	Topr	-40	_	125	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.



# Electrical Characteristics (Unless otherwise specified, Ta = -40 to 125°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
LED	Forward voltage	VF	I <sub>F</sub> = 10 mA, Ta=25 °C	1.1	1.25	1.4	V
			IF = 10 mA	1.0	_	1.55	V
	Reverse current	IR	V <sub>R</sub> = 5 V	_	_	10	μΑ
	Capacitance	Ст	V = 0 V, f = 1 MHz, Ta=25 °C	_	35	_	pF
Detector	Collector-emitter breakdown voltage	V(BR) CEO	IC = 0.5 mA	40	_	_	V
	Emitter-collector breakdown voltage	V(BR) ECO	IE = 0.5 mA	5	_	_	V
	Collector dark current	ICEO	V <sub>CE</sub> = 24 V, Ta=25 °C	_	_	100	nA
			V <sub>CE</sub> = 24 V, Ta=105 °C	_	0.1	5	μΑ
			V <sub>CE</sub> = 24 V, Ta=125 °C	_	1	10	μА
	Capacitance (collector to emitter)	C <sub>CE</sub>	V = 0 V, f = 1 MHz, Ta=25 °C	_	10	_	pF

# Coupled Electrical Characteristics (Unless otherwise specified, Ta = -40 to 125°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Current transfer ratio	Ic / IF	I <sub>F</sub> = 5 mA, V <sub>CE</sub> = 5 V	20	_	900	- %
Current transfer fatto		I <sub>F</sub> = 5 mA, V <sub>CE</sub> = 5 V, Ta=25 °C	100	_	900	
Saturated CTR	IC / IF (sat)	I <sub>F</sub> = 1 mA, V <sub>CE</sub> = 0.4 V, Ta=25 °C	30	_	-	%
Collector-emitter		IC = 2.4 mA, IF = 8 mA, Ta=25 °C		_	0.4	,,
saturation voltage	VCE (sat)	I <sub>C</sub> = 0.2 mA, I <sub>F</sub> = 1 mA, Ta=25 °C		_	0.4	V
Off-state collector current	IC (off)	V <sub>F</sub> = 0.7V, V <sub>CE</sub> = 24 V , Ta=25 °C		_	10	μΑ

# **Isolation Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance (input to output)	Cs	V <sub>S</sub> = 0 V, f = 1 MHz	_	0.5	_	pF
Isolation resistance	Rs	V <sub>S</sub> = 500 V, R.H. ≤ 60 %	5×10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
Isolation voltage	BVs	AC, 60 s	3750	_	_	Vrms

Note: This device is considered as a two terminal device: Pins 1 and 3 are shorted together, and pins 4 and 6 are shorted together.

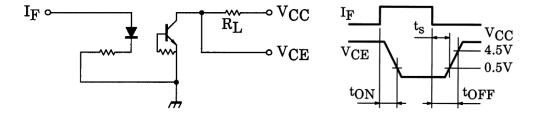


# Switching Characteristics (Note) (Unless otherwise specified, Ta = -40 to 125°C)

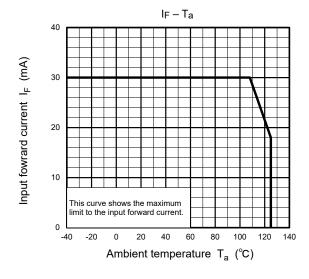
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Turn-on time	ton		1	15	100	
Storage time	ts	$R_L$ = 10 k $\Omega$ (Note 1) $V_{CC}$ = 5 V, $I_F$ = 2 mA	_	20	200	μS
Turn-off time	tOFF	, , , , , , , , , , , , , , , , , , , ,	1	50	300	

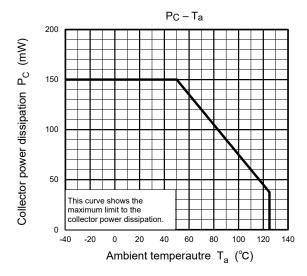
Note: All typical values at  $T_a = 25$  °C.

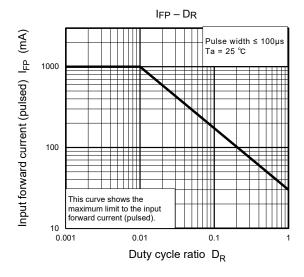
Note 1: Switching time test circuit and waveform.

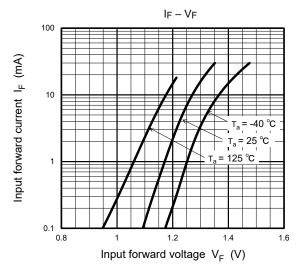


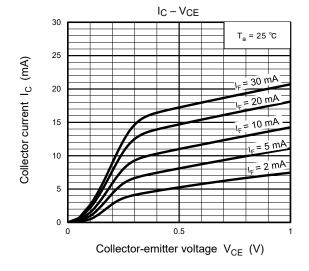
### **Characteristic Curves (Note)**

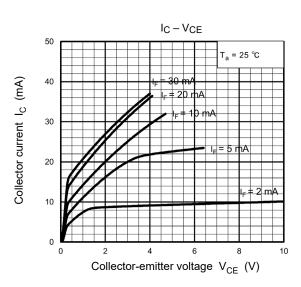


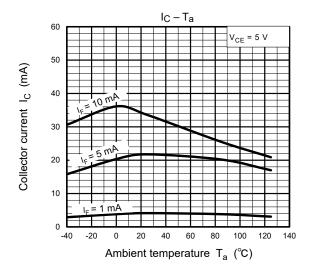


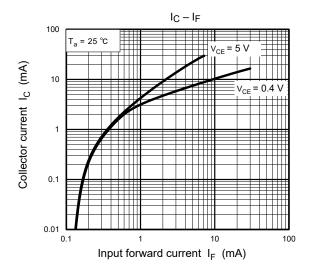


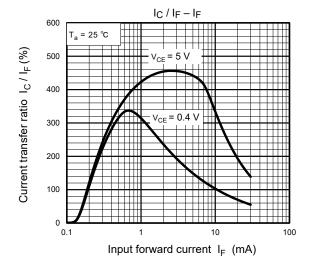


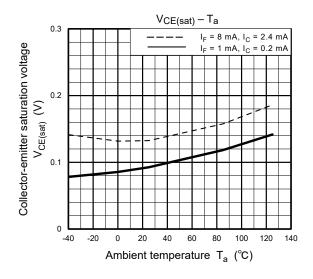


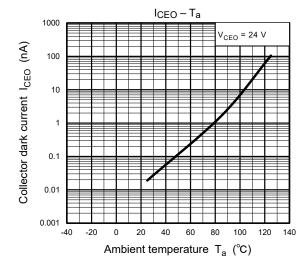


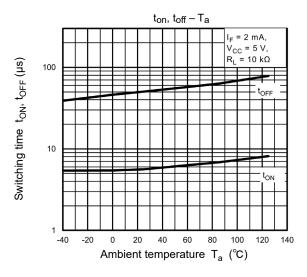


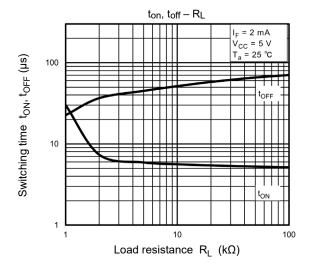


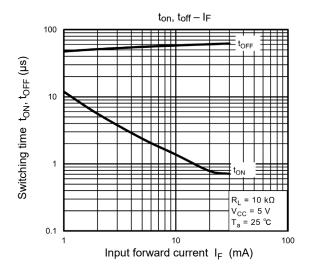












Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise specified.

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