

MOSFETs Silicon N-channel MOS (U-MOSIX-H)

# XPQR3004PB

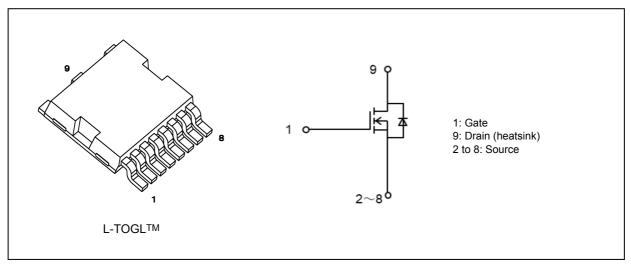
#### 1. Applications

- · Automotive
- · Switching Voltage Regulators
- · Motor Drivers
- · DC-DC Converters

#### 2. Features

- (1) AEC-Q101 qualified
- (2) Low drain-source on-resistance:  $R_{DS(ON)} = 0.23 \text{ m}\Omega$  (typ.) ( $V_{GS} = 10 \text{ V}$ )
- (3) Low leakage current:  $I_{\rm DSS}$  = 10  $\mu A$  (max) (V\_{DS} = 40 V)
- (4) Enhancement mode:  $V_{th}$  = 2.0 to 3.0 V ( $V_{DS}$  = 10 V,  $I_{D}$  = 1.0 mA)

#### 3. Packaging and Internal Circuit



Note: L-TOGL<sup>TM</sup> is a trademark of Toshiba Electronic Devices & Storage Corporation.



### 4. Absolute Maximum Ratings (Note) (Ta = 25 °C unless otherwise specified)

Characteristics				Rating	Unit
Drain-source voltage			V <sub>DSS</sub>	40	V
Gate-source voltage	,		V <sub>GSS</sub>	±20	
Drain current (DC)		(Note 1)	I <sub>D</sub>	400	Α
Drain current (pulsed)		(Note 1)	I <sub>DP</sub>	1200	
Power dissipation	(T <sub>c</sub> = 25 °C)		P <sub>D</sub>	750	W
Single-pulse avalanche energy		(Note 2)	E <sub>AS</sub>	624	mJ
Single-pulse avalanche current			I <sub>AS</sub>	400	Α
Channel temperature		(Note 3)	T <sub>ch</sub>	175	ç
Storage temperature		(Note 3)	T <sub>stg</sub>	-55 to 175	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### 5. Thermal Characteristics

Characteristics		Symbol	Max	Unit
Channel-to-case thermal impedance	(T <sub>c</sub> = 25 °C)	Z <sub>th(ch-c)</sub>	0.2	°C/W

Note 1: Ensure that the channel temperature does not exceed 175 °C.

Note 2:  $V_{DD}$  = 32 V,  $T_{ch}$  = 25 °C (initial), L = 3  $\mu$ H,  $R_G$  = 25  $\Omega$ ,  $I_{AS}$  = 400 A

Note 3: The definitions of the absolute maximum channel and storage temperatures are based on AEC-Q101.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



#### 6. Electrical Characteristics

# 6.1. Static Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V			10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	40			٧
	V <sub>(BR)DSX</sub>	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	20	_		
Gate threshold voltage (Note 4)	$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1.0 mA	2.0	_	3.0	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 6 V, I <sub>D</sub> = 200 A		0.30	0.47	mΩ
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 200 A		0.23	0.30	

# 6.2. Dynamic Characteristics (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	$V_{DS}$ = 10 V, $V_{GS}$ = 0 V, f = 300 kHz	_	20700	26910	pF
Reverse transfer capacitance	C <sub>rss</sub>		I	1500	2550	
Output capacitance	C <sub>oss</sub>			13650		
Gate resistance	r <sub>g</sub>		I	4.0	8.0	Ω
Switching time (rise time)	t <sub>r</sub>	See Fig. 6.2.1	1	65		ns
Switching time (turn-on time)	t <sub>on</sub>		-	120		
Switching time (fall time)	t <sub>f</sub>		_	130	_	
Switching time (turn-off time)	t <sub>off</sub>		_	395	_	

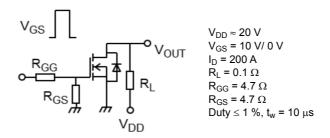


Fig. 6.2.1 Switching Time Test Circuit

# 6.3. Gate Charge Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 32 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 400 \text{ A}$	_	295		nC
Gate-source charge 1	Q <sub>gs1</sub>		_	80		
Gate-drain charge	Q <sub>gd</sub>		_	80	_	

# 6.4. Source-Drain Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 5)	I <sub>DR</sub>	_	_	_	400	Α
Reverse drain current (pulsed)	(Note 5)	I <sub>DRP</sub>	_	_	_	1200	
Diode forward voltage	·	$V_{DSF}$	I <sub>DR</sub> = 400 A, V <sub>GS</sub> = 0 V	_	_	-1.2	V
Reverse recovery time		t <sub>rr</sub>	I <sub>DR</sub> = 400 A, V <sub>GS</sub> = 0 V	_	181	_	ns
Reverse recovery charge		$Q_{rr}$	-dI <sub>DR</sub> /dt = 100 A/μs	-	416	_	nC

Note 5: Ensure that the channel temperature does not exceed 175 °C.

Rev.4.0



# 7. Marking

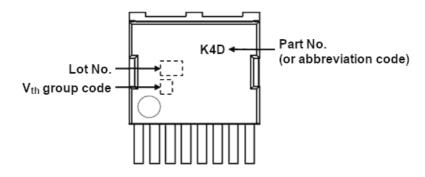


Fig. 7.1 Marking

Note 4: If requested,  $V_{th}$  grouping is possible for each reel. ( $V_{th}$  width is 0.4 V)

However, we do not accept specifications in specific groups.

If there is no request, the group-free reel will be applied. ( $V_{th}$  width is 1.0 V, no  $V_{th}$  group code is printed on marking)



#### 8. Characteristics Curves (Note)

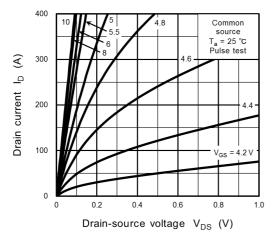


Fig. 8.1 I<sub>D</sub> - V<sub>DS</sub>

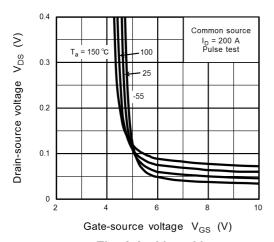


Fig. 8.3 V<sub>DS</sub> - V<sub>GS</sub>

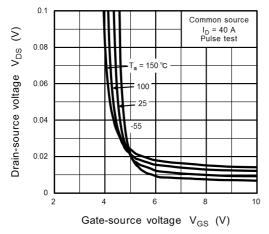


Fig. 8.5 VDS - VGS

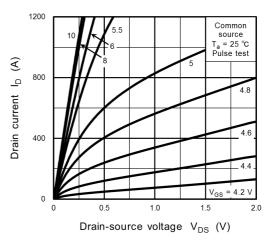


Fig. 8.2 I<sub>D</sub> - V<sub>DS</sub>

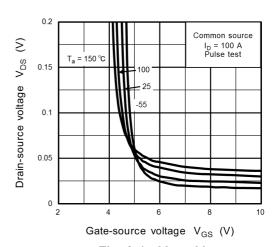


Fig. 8.4 VDS - VGS

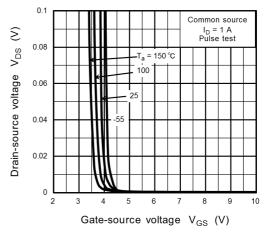


Fig. 8.6 V<sub>DS</sub> - V<sub>GS</sub>



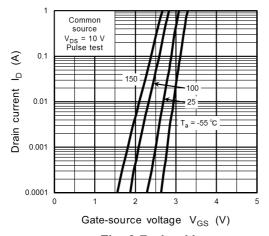


Fig. 8.7  $I_D - V_{GS}$ 

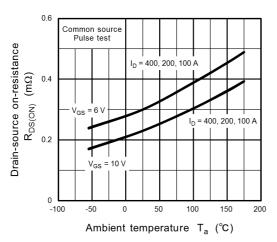


Fig. 8.9 R<sub>DS(ON)</sub> - T<sub>a</sub>

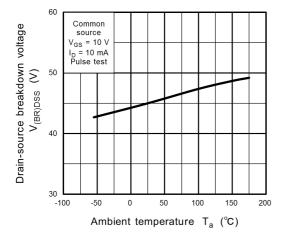


Fig. 8.11 V<sub>(BR)DSS</sub> - T<sub>a</sub>

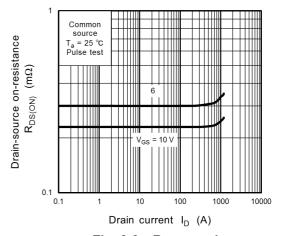


Fig. 8.8 R<sub>DS(ON)</sub> - I<sub>D</sub>

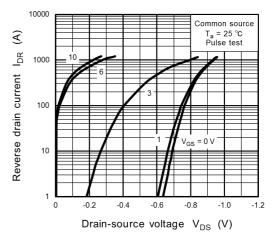


Fig. 8.10 I<sub>DR</sub> - V<sub>DS</sub>

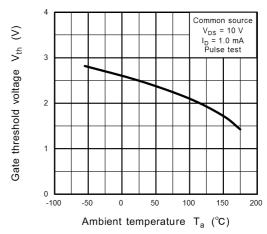


Fig. 8.12 V<sub>th</sub> - T<sub>a</sub>



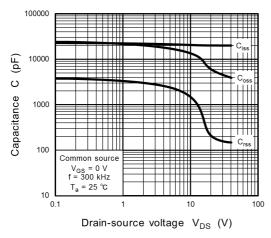


Fig. 8.13 Capacitance - V<sub>DS</sub>

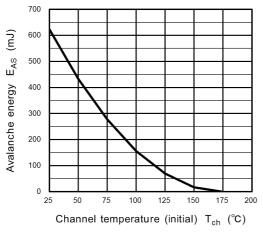


Fig. 8.15 E<sub>AS</sub> - T<sub>ch</sub>(Guaranteed Maximum)

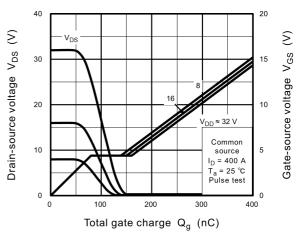


Fig. 8.14 Dynamic Input/Output Characteristics

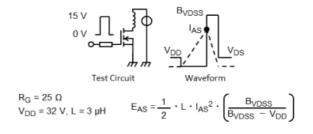


Fig. 8.16 Test Circuit/Waveform



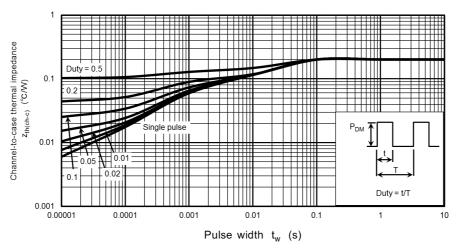


Fig. 8.17  $z_{th(ch-c)}$  -  $t_w$  (Guaranteed Maximum)

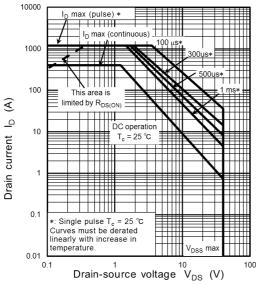


Fig. 8.18 Safe Operating Area (Guaranteed Maximum)

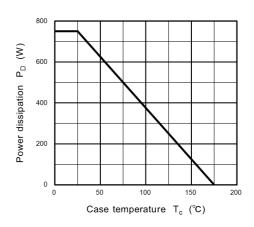


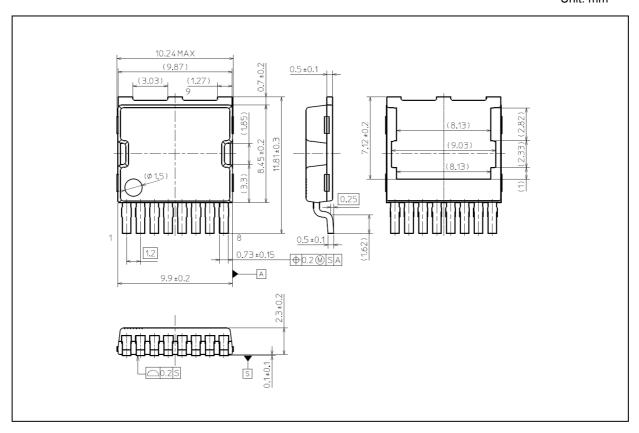
Fig. 8.19 P<sub>D</sub> - T<sub>c</sub> (Guaranteed Maximum)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



# **Package Dimensions**

Unit: mm



Weight: 0.803 g (typ.)

Package Name(s)
TOSHIBA: 2-10AG1A
Nickname: L-TOGL™



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