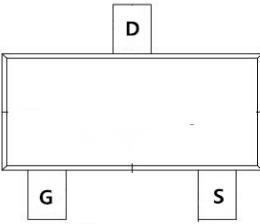
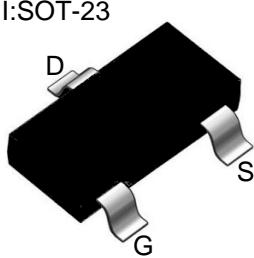
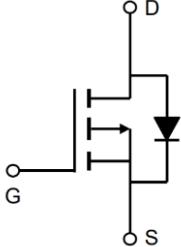


**TM3401AI**
**P-Channel Enhancement Mosfet**

<p><b>General Description</b></p> <ul style="list-style-type: none"> <li>• Low <math>R_{DS(ON)}</math></li> <li>• RoHS and Halogen-Free Compliant</li> </ul> <p><b>Applications</b></p> <ul style="list-style-type: none"> <li>• Load switch</li> <li>• PWM</li> </ul>	<p><b>General Features</b></p> <p><math>V_{DS} = -30V</math> <math>I_D = -4.6A</math></p> <p><math>R_{DS(ON)} = 40m\Omega</math> (Typ.) @ <math>V_{GS}=10V</math></p> <p>100% UIS Tested 100% <math>R_g</math> Tested</p> 
--	---

		
Marking: A19T OR 3401		

<b>Absolute Maximum Ratings (<math>T_c=25^\circ C</math> unless otherwise noted)</b>			
<b>Symbol</b>	<b>Parameter</b>	<b>Rating</b>	<b>Units</b>
$V_{DS}$	Drain-Source Voltage	-30	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D @ T_A=25^\circ C$	Continuous Drain Current	-4.6	A
$I_D @ T_A=70^\circ C$	Continuous Drain Current	-4	A
$IDM$	Pulsed Drain Current <sup>2</sup>	-20	A
$P_D @ T_A=25^\circ C$	Total Power Dissipation <sup>3</sup>	1.4	W
$P_D @ T_A=70^\circ C$	Total Power Dissipation <sup>3</sup>	0.9	W
$T_{STG}$	Storage Temperature Range	- 55 to 150	°C
$T_J$	Operating Junction Temperature Range	- 55 to 150	°C
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	125	°C/W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup> ( $t \leq 10s$ )	85	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=-250\mu\text{A}$	-30	-32	---	V
$\Delta BVDSS/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=-1\text{mA}$	---	-0.014	---	$\text{V}/^\circ\text{C}$
RDS(ON)	Static Drain-Source On-Resistance	$V_{GS}=-10\text{V}$ , $I_D=-3\text{A}$	---	40	48	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}$ , $I_D=-3\text{A}$	---	49	65	
		$V_{GS}=-2.5\text{V}$ , $I_D=-2\text{A}$	---	75	85	
VGS(th)	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=-250\mu\text{A}$	-0.5	-0.7	-1.1	V
$\Delta V_{GS(\text{th})}$	$V_{GS(\text{th})}$ Temperature Coefficient		---	2.6	---	$\text{mV}/^\circ\text{C}$
IDSS	Drain-Source Leakage Current	$V_{DS}=-24\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	-1	$\text{uA}$
		$V_{DS}=-24\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=55^\circ\text{C}$	---	---	-5	
IGSS	Gate-Source Leakage Current	$V_{GS}=\pm 12\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	nA
gfs	Forward Transconductance	$V_{DS}=-5\text{V}$ , $I_D=-3\text{A}$	---	5.6	---	S
Qg	Total Gate Charge (-4.5V)	$V_{DS}=-15\text{V}$ , $V_{GS}=-4.5\text{V}$ , $I_D=-3\text{A}$	---	11.9	---	nC
Qgs	Gate-Source Charge		---	1.8	---	
Qgd	Gate-Drain Charge		---	3	---	
Td(on)	Turn-On Delay Time	$V_{DD}=-15\text{V}$ , $V_{GS}=-4.5\text{V}$ , $R_G=3.3\Omega$ , $I_D=-3\text{A}$	---	6.6	---	ns
Tr	Rise Time		---	27.8	---	
Td(off)	Turn-Off Delay Time		---	46.2	---	
Tf	Fall Time		---	20.6	---	
Ciss	Input Capacitance	$V_{DS}=-15\text{V}$ , $V_{GS}=0\text{V}$ , $f=1\text{MHz}$	---	290	---	pF
Coss	Output Capacitance		---	73	---	
Crss	Reverse Transfer Capacitance		---	71	---	
IS	Continuous Source Current <sup>1,4</sup>	$V_G=V_D=0\text{V}$ , Force Current	---	---	-4.6	A
VSD	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0\text{V}$ , $I_S=-1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	-1.2	V

**Note :**

- 1.The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- 4.The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

## TM3401AI

## P-Channel Enhancement Mosfet

### Typical Characteristics

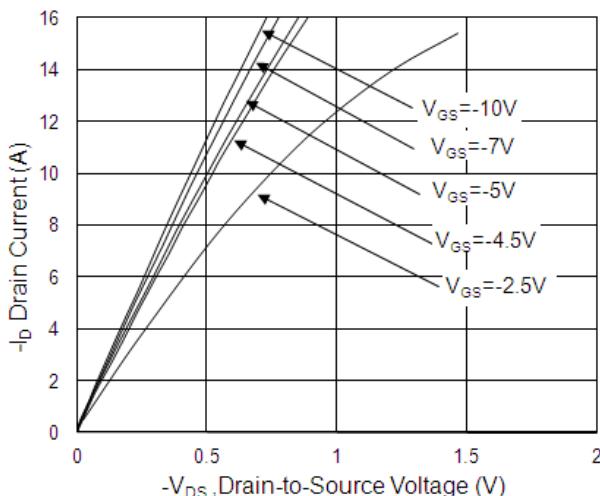


Fig.1 Typical Output Characteristics

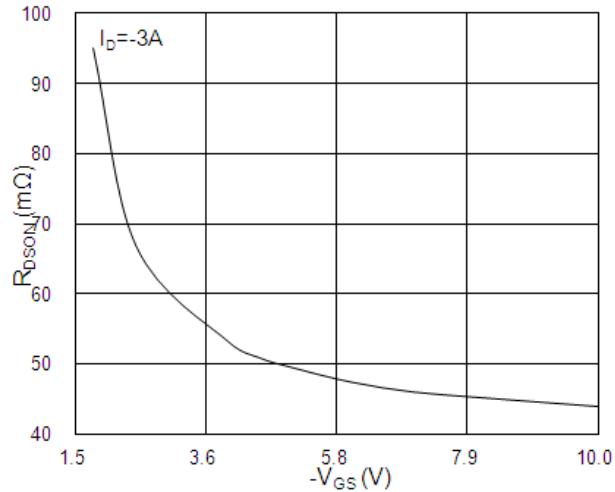


Fig.2 On-Resistance vs. G-S Voltage

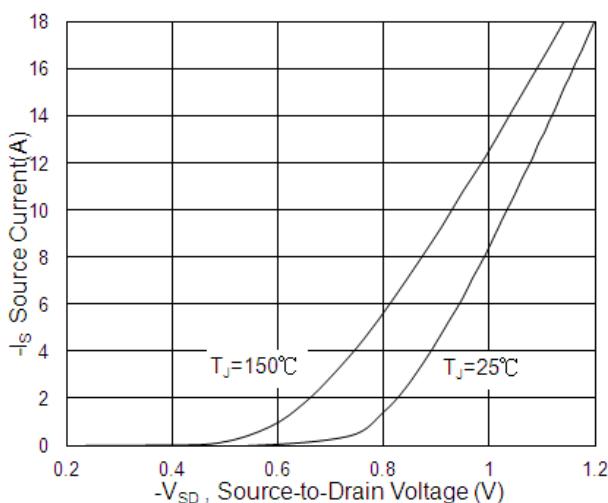


Fig.3 Forward Characteristics Of Reverse

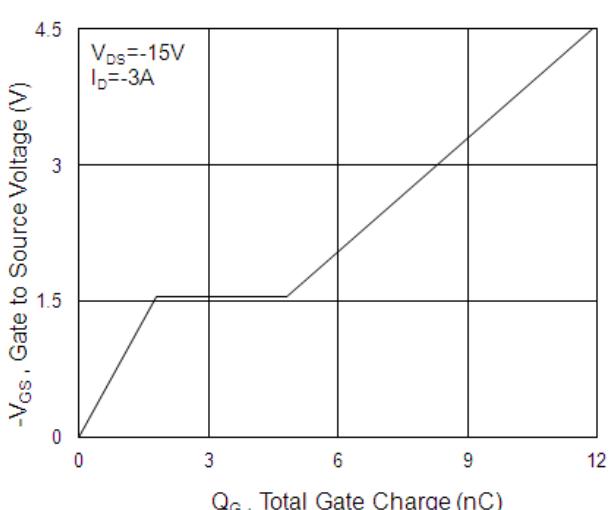


Fig.4 Gate-Charge Characteristics

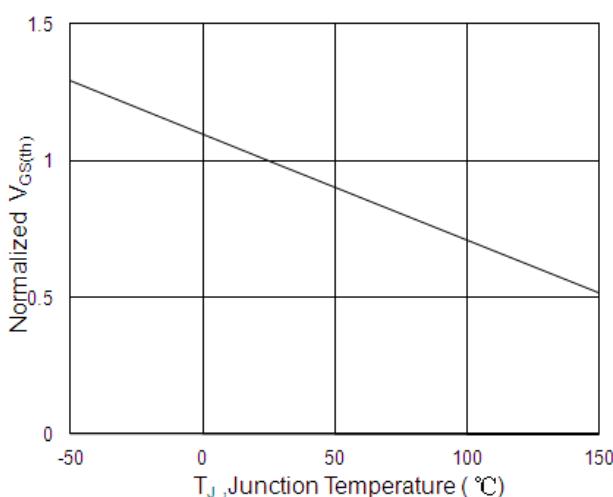


Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$

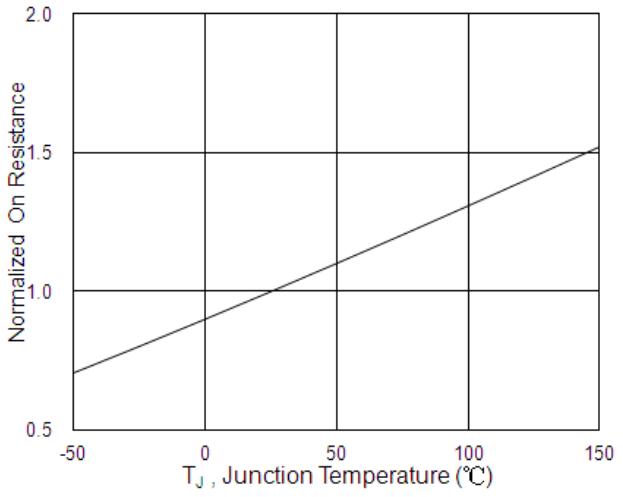
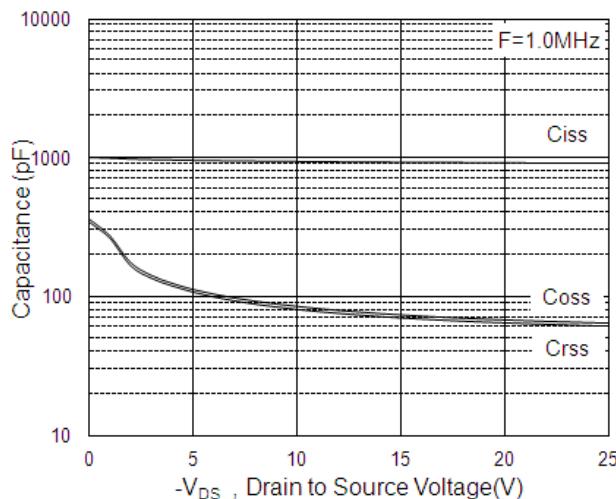


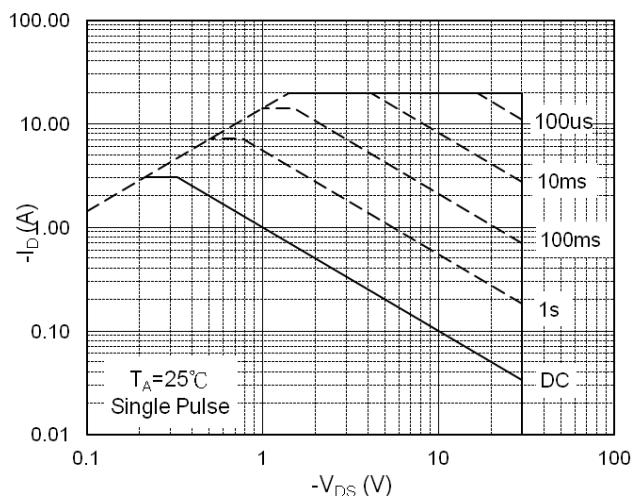
Fig.6 Normalized  $R_{DSON}$  vs.  $T_J$

## TM3401AI

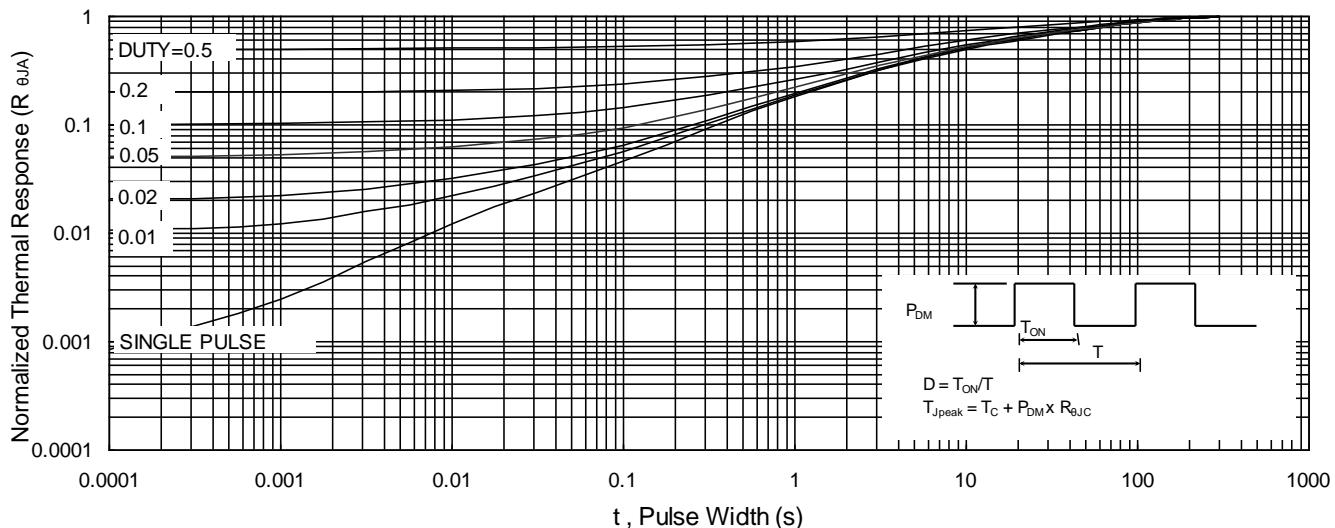
## P-Channel Enhancement Mosfet



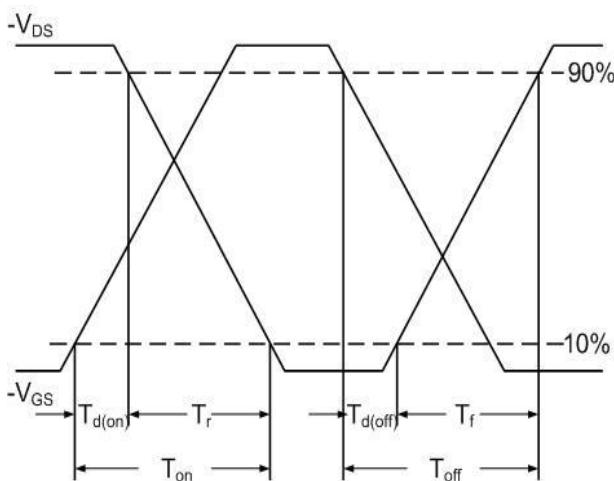
**Fig.7 Capacitance**



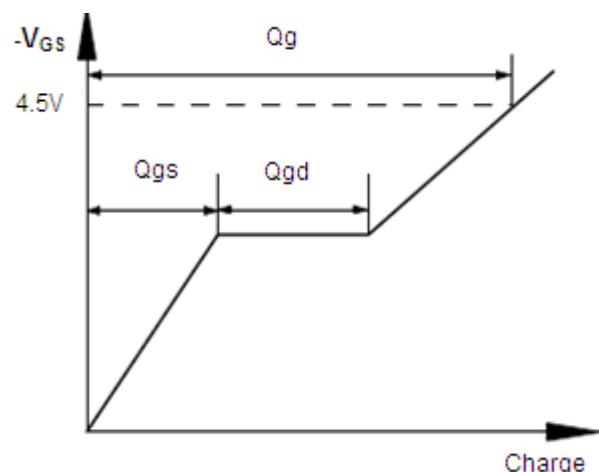
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**

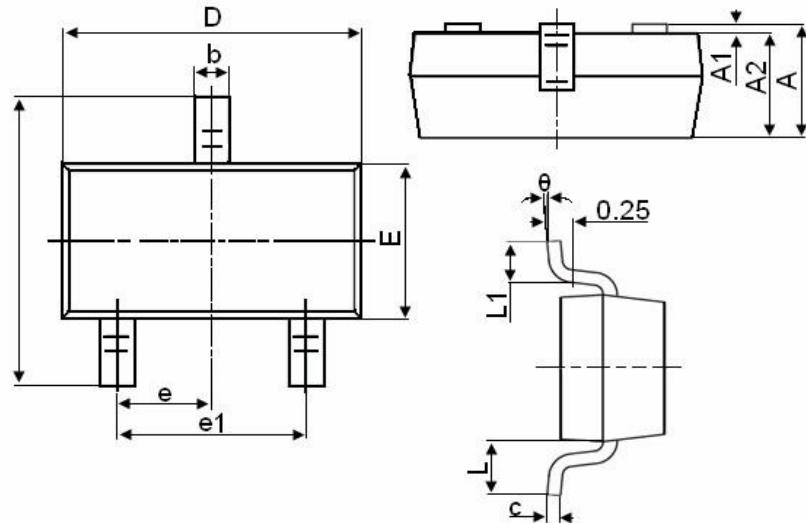


**Fig.10 Switching Time Waveform**



**Fig.11 Gate Charge Waveform**

## Package Mechanical Data:SOT-23



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°

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