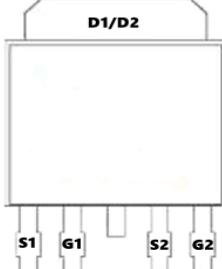
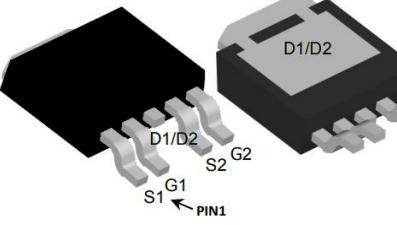
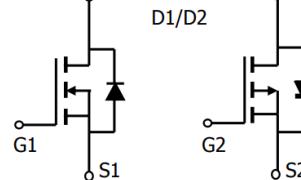


TM4050GD

N+P-Channel Enhancement Mode Mosfet

<p>General Description</p> <ul style="list-style-type: none"> • Low $R_{DS(ON)}$ • RoHS and Halogen-Free Compliant <p>Applications</p> <ul style="list-style-type: none"> • Load switch • PWM 	<p>General Features</p> <p>N Channel $V_{DS} = 40V$ $I_D = 50A$ $R_{DS(ON)} = 4.2m\Omega$ (typ.) @ $V_{GS} = 10V$</p> <p>P Channel $V_{DS} = -40V$ $I_D = -50A$ $R_{DS(ON)} = 11.5m\Omega$ (typ.) @ $V_{GS} = -10V$ 100% UIS Tested 100% R_g Tested</p> 
 <p>GD:TO-252-4L</p>   <p>Marking: 50G04</p>	

Absolute Maximum Ratings: ($T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	N-Channel	P-Channel	Units
V_{DS}	Drain-Source Voltage	40	-40	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
I_D	Continuous Drain Current $T_C=25^\circ C$	50	-50	A
	Continuous Drain Current- $T_C=100^\circ C$	35	-20	
	Pulsed Drain Current ¹	250	-130	
P_D	Power Dissipation $T_c=25^\circ C$	55	55	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150		°C

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance,Junction to Case	2.3	°C/W
$R_{\theta JA}$	Thermal Resistance,Junction to Ambient	62	

TM4050GD
N+P-Channel Enhancement Mode Mosfet

N-CH Electrical Characteristics: ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250 \mu\text{A}$	40	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS}=0\text{V}, V_{DS}=-40\text{V}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{A}$	---	---	± 100	nA
On Characteristics						
V_{GS(th)}	GATE-Source Threshold Voltage	$V_{GS}=V_{DS}, I_D=250 \mu\text{A}$	1	1.6	2.5	V
R_{DS(on)}	Drain-Source On Resistance ³	$V_{GS}=10\text{V}, I_D=20\text{A}$	---	4.2	5.5	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=10\text{A}$	---	5.3	7	
G_{FS}	Forward Transconductance	$V_{DS}=10\text{V}, I_D=10\text{A}$	---	16	---	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=25\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	---	2380	3400	pF
C_{oss}	Output Capacitance		---	230	385	
C_{rss}	Reverse Transfer Capacitance		---	148	210	
Switching Characteristics						
t_{d(on)}	Turn-On Delay Time ^{2,3}	$V_{DD}=20\text{V}, I_D=1\text{A}, R_{GEN}=3.3 \Omega, V_{GS}=10\text{V}$	---	14.2	28	ns
t_r	Rise Time ^{2,3}		---	18.3	36	ns
t_{d(off)}	Turn-Off Delay Time ^{2,3}		---	38.8	76	ns
t_f	Fall Time ^{2,3}		---	13.9	28	ns
Q_g	Total Gate Charge ³	$V_{GS}=4.5\text{V}, V_{DS}=32\text{V}, I_D=10\text{A}$	---	25	50	nC
Q_{gs}	Gate-Source Charge		---	6.5	13	nC
Q_{gd}	Gate-Drain "Miller" Charge		---	12.1	24	nC
Drain-Source Diode Characteristics						
V_{SD}	Drain Diode Forward Voltage ³	$V_{GS}=0\text{V}, I_S=1\text{A}$	---	---	1	V
L_S	Continuous Source Current	$V_G=V_D=0\text{V}, \text{Force Current}$	---	---	50	A
I_{sm}	Pulsed Source Current		---	---	160	A



TM4050GD

N+P-Channel Enhancement Mode Mosfet

Typical Characteristics: ($T_c=25^\circ\text{C}$ unless otherwise noted)

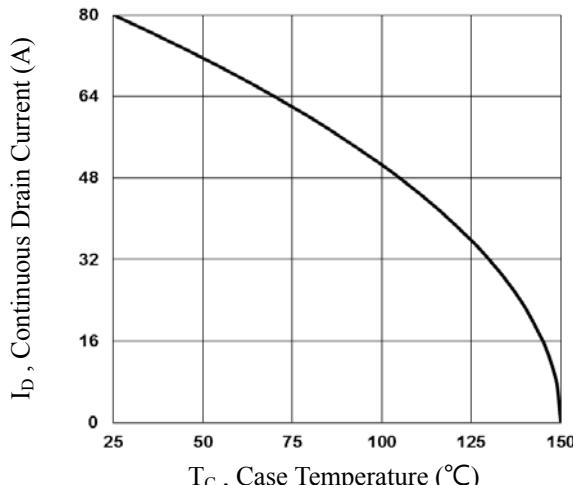


Fig.1 Continuous Drain Current vs. T_c

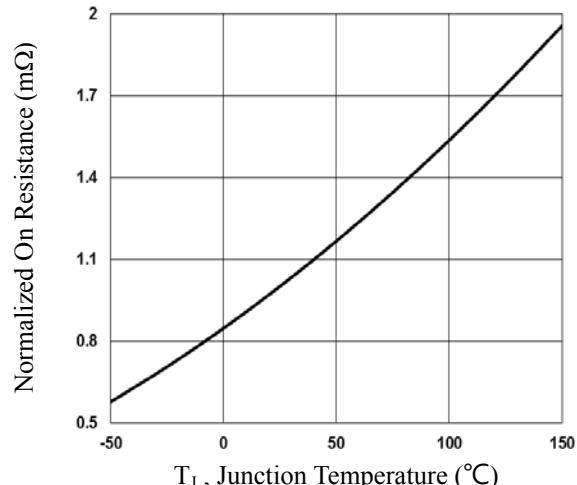


Fig.2 Normalized RD_{SON} vs. T_j

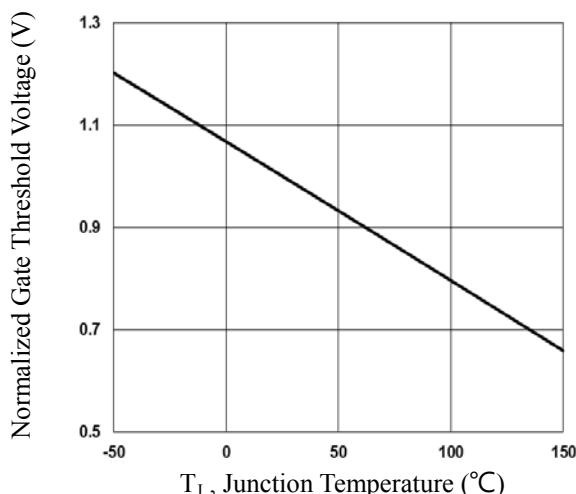


Fig.3 Normalized V_{th} vs. T_j

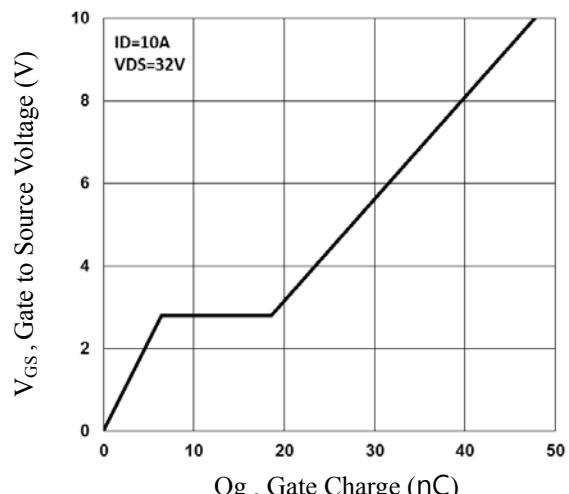


Fig.4 Gate Charge Waveform

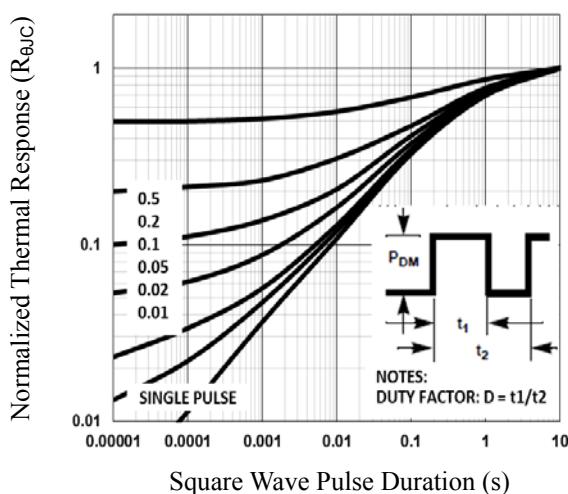


Fig.5 Normalized Transient Impedance

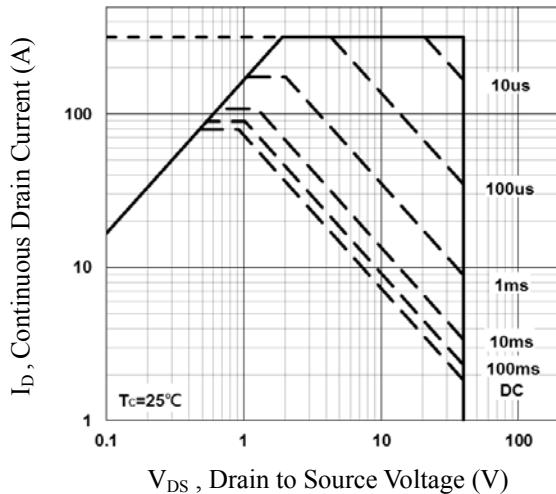


Fig.6 Maximum Safe Operation Area

TM4050GD

N+P-Channel Enhancement Mode Mosfet

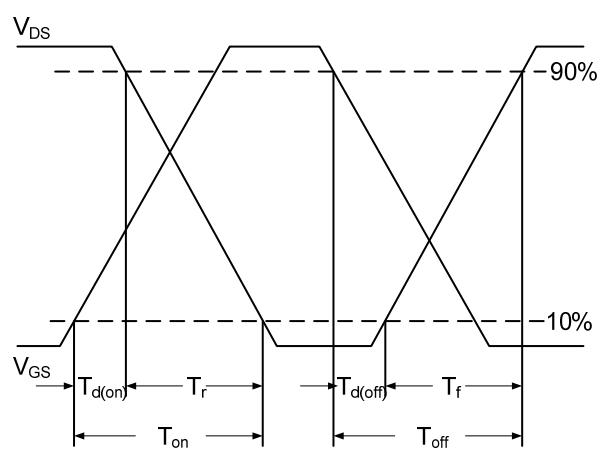


Fig.7 Switching Time Waveform

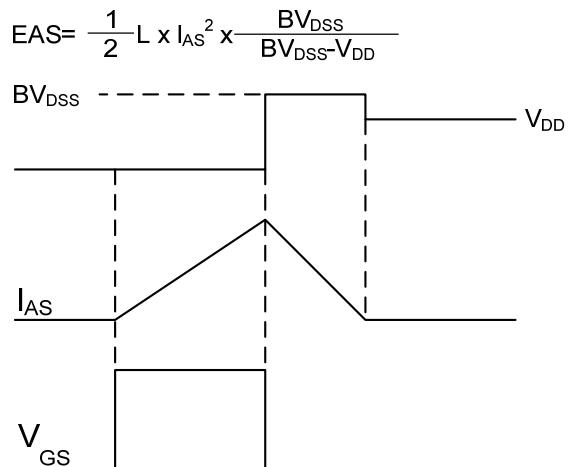


Fig.8 EAS Waveform

TM4050GD
N+P-Channel Enhancement Mode Mosfet
P-CH Electrical Characteristics: ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250 \mu\text{A}$	-40	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS}=0\text{V}, V_{DS}=-40\text{V}, T_J=25^\circ\text{C}$	---	---	-1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{A}$	---	---	± 100	nA
On Characteristics						
V_{GS(th)}	GATE-Source Threshold Voltage	$V_{GS}=V_{DS}, I_D=250 \mu\text{A}$	-1	-1.6	-2.5	V
R_{DS(on)}	Drain-Source On Resistance	$V_{GS}=-10\text{V}, I_D=-10\text{A}$	---	11.5	15	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}, I_D=-8\text{A}$	---	16	22	
G_{FS}	Forward Transconductance	$V_{DS}=-10\text{V}, I_D=-10\text{A}$	---	13	---	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=-25\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	---	2557	3800	pF
C_{oss}	Output Capacitance		---	220	320	
C_{rss}	Reverse Transfer Capacitance		---	127	180	
Switching Characteristics						
t_{d(on)}	Turn-On Delay Time ^{3,4}	$V_{DD}=-20\text{V}, I_D=-1\text{A}, R_G=6 \Omega, V_{GS}=-10\text{V}$	---	23	40	ns
t_r	Rise Time ^{3,4}		---	10	20	ns
t_{d(off)}	Turn-Off Delay Time ^{3,4}		---	135	250	ns
t_f	Fall Time ^{3,4}		---	46	90	ns
Q_g	Total Gate Charge ^{3,4}	$V_{GS}=-4.5\text{V}, V_{DS}=-32\text{V}, I_D=-10\text{A}$	---	22.2	40	nC
Q_{gs}	Gate-Source Charge ^{3,4}		---	8.2	16	nC
Q_{gd}	Gate-Drain "Miller" Charge ^{3,4}		---	8.8	16	nC
Drain-Source Diode Characteristics						
V_{SD}	Source Drain Diode Forward Voltage ²	$V_{GS}=0\text{V}, I_S=-1\text{A}, T_J=25^\circ\text{C}$	---	---	-1	V

Notes:

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=25\text{V}, V_{GS}=10\text{V}, L=0.1\text{mH}, I_{AS}=51\text{A}, R_G=25 \Omega$, Starting $T_J=25^\circ\text{C}$.
3. The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

TM4050GD

N+P-Channel Enhancement Mode Mosfet

Typical Characteristics: ($T_C=25^\circ\text{C}$ unless otherwise noted)

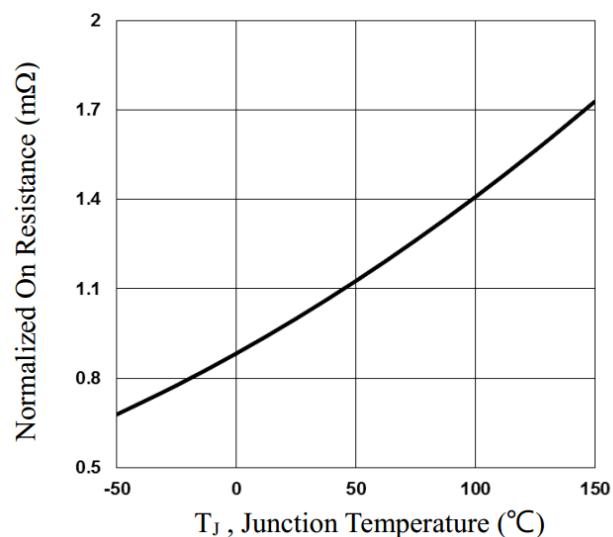
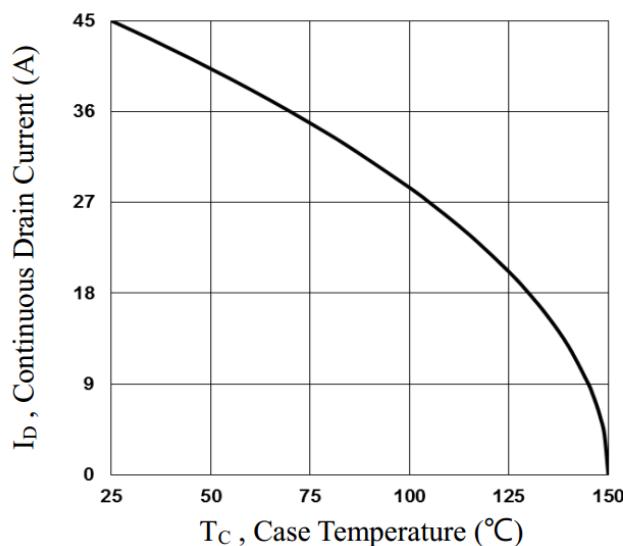


Fig.1 Continuous Drain Current vs. T_C

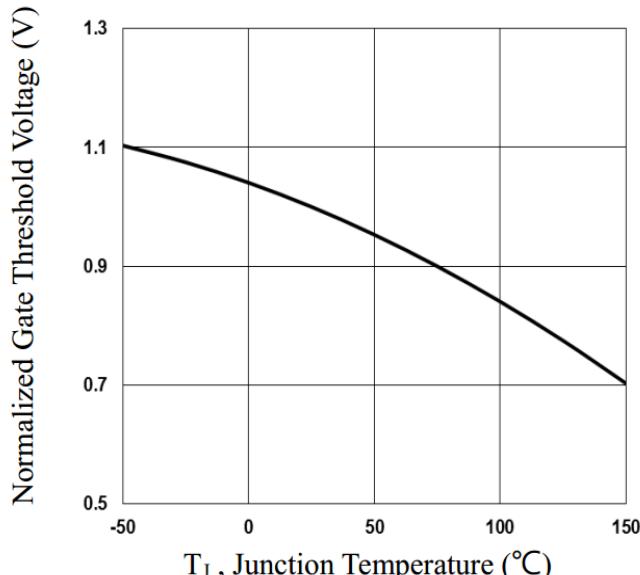


Fig.3 Normalized V_{th} vs. T_J

Fig.2 Normalized RDSON vs. T_J

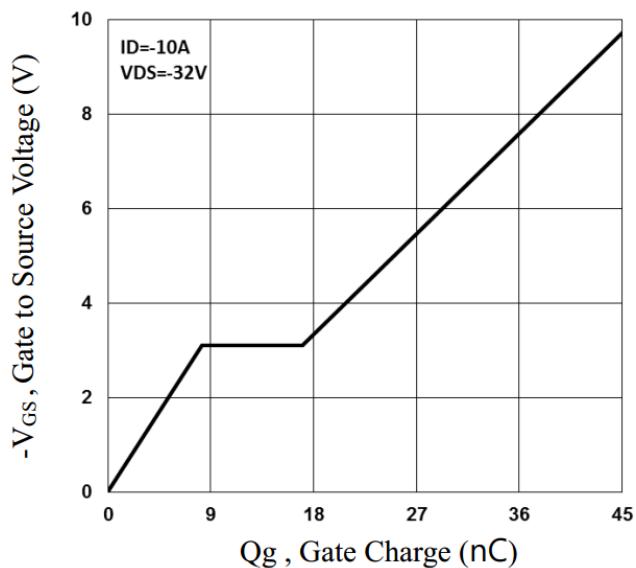


Fig.4 Gate Charge Waveform

TM4050GD

N+P-Channel Enhancement Mode Mosfet

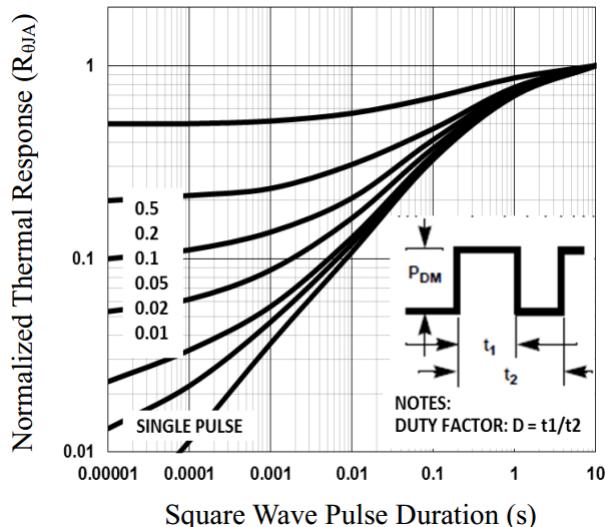


Fig.5 Normalized Transient Impedance

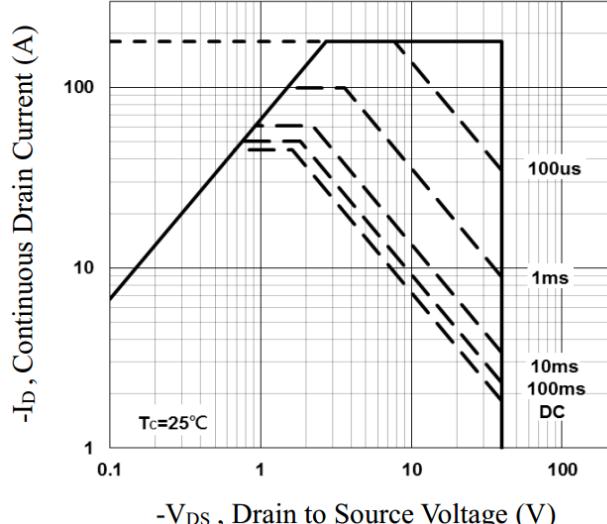


Fig.6 Maximum Safe Operation Area

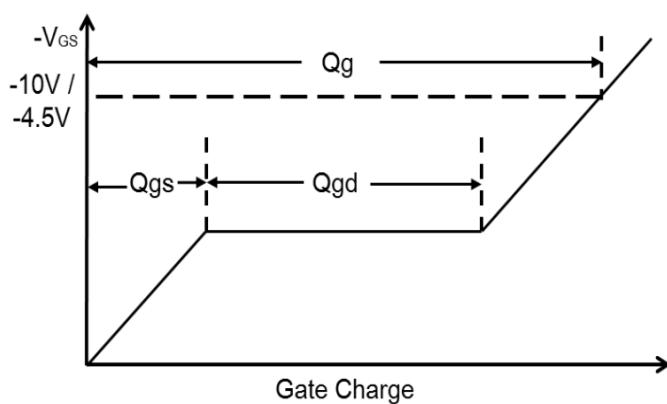


Fig.7 Switching Time Waveform

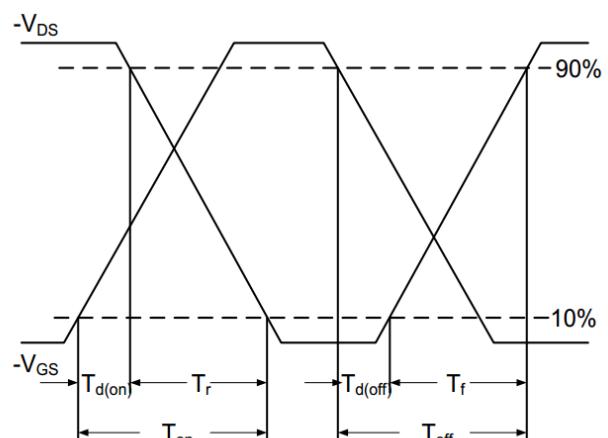
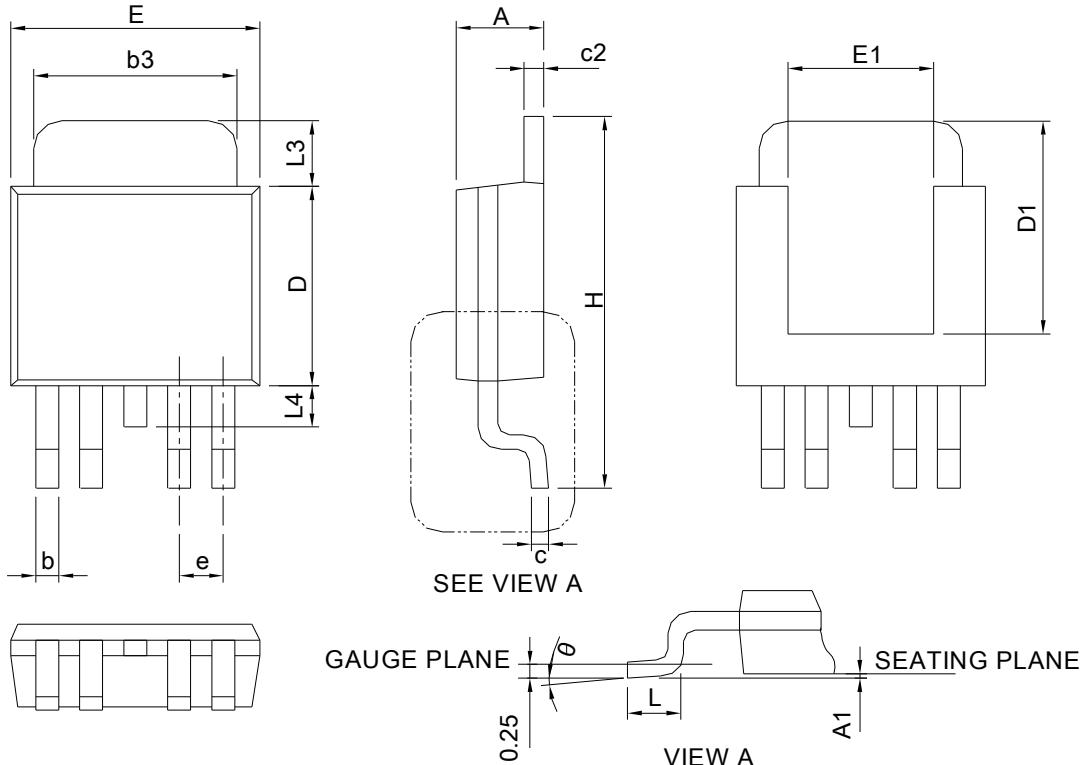


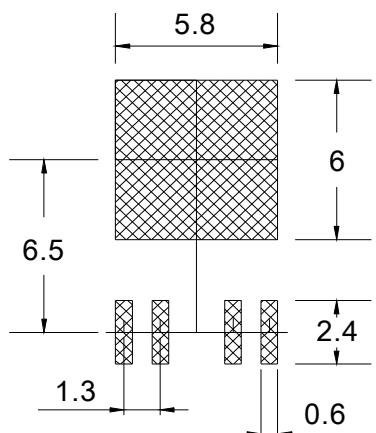
Fig.8 Gate Charge Waveform

Package Mechanical Data: TO-252-4L



SYMBOL	TO-252-4			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.39	0.086	0.094
A1	-	0.2	-	0.008
b	0.50	0.71	0.020	0.028
b3	4.32	5.46	0.170	0.215
c	0.46	0.61	0.018	0.024
c2	0.46	0.89	0.018	0.035
D	5.33	6.22	0.210	0.245
D1	4.57	6.00	0.180	0.236
E	6.35	6.73	0.250	0.265
E1	3.81	6.00	0.150	0.236
e	1.30 BSC		0.051 BSC	
H	9.40	10.41	0.370	0.410
L	1.40	1.78	0.055	0.070
L3	0.89	2.03	0.035	0.080
L4	-	1.02	-	0.040
θ	0°	8°	0°	8°

RECOMMENDED LAND PATTERN



UNIT: mm

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