

SJ-FET

TSF65R190S2/TSP65R190S2/TSA65R190S2/TSK65R190S2 650V N-Channel Super-Junction MOSFET Gen-II

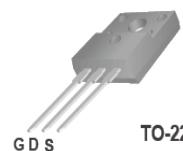
Description

SJ-FET is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance. This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. SJ-FET is suitable for various AC/DC power conversion in switching mode operation for higher efficiency.

Features

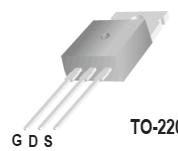
- Multi-Epi process SJ-FET
- 700V @ $T_J = 150^\circ\text{C}$
- Typ. $R_{DS(on)} = 0.16\Omega$
- Ultra Low Gate Charge (typ. $Q_g = 36.5\text{nC}$)
- 100% avalanche tested

TSF65R190S2



TO-220F

TSP65R190S2



TO-220

TSA65R190S2

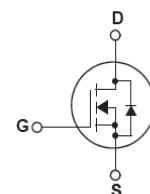


TO-3P

TSK65R190S2



TO-247



Absolute Maximum Ratings

Symbol	Parameter	TSP_A_K65R190S2	TSF65R190S2	Unit
V_{DSS}	Drain-Source Voltage	650		V
I_D	Drain Current - Continuous ($TC = 25^\circ\text{C}$)	20*		A
	- Continuous ($TC = 100^\circ\text{C}$)	12.6*		
I_{DM}	Drain Current - Pulsed (Note 1)	65		A
V_{GSS}	Gate-Source voltage	± 30		V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	485		mJ
I_{AS}	Avalanche current, repetitive or not-repetitive (pulse width limited by T_j max)	3.5		A
dv/dt	Peak Diode Recovery dv/dt (Note 3)	15		V/ns
$dVds/dt$	Drain Source voltage slope ($V_{ds}=480\text{V}$)	50		V/ns
P_D	Power Dissipation ($TC = 25^\circ\text{C}$)	150	34	W
T_j, T_{STG}	Operating and Storage Temperature Range	-55 to +150		°C
T_L	Maximum Lead Temperature for Soldering Purpose, $1/16''$ from Case for 10 Seconds	260		°C

* Drain current limited by maximum junction temperature. Maximum duty cycle D=0.75.

Thermal Characteristics

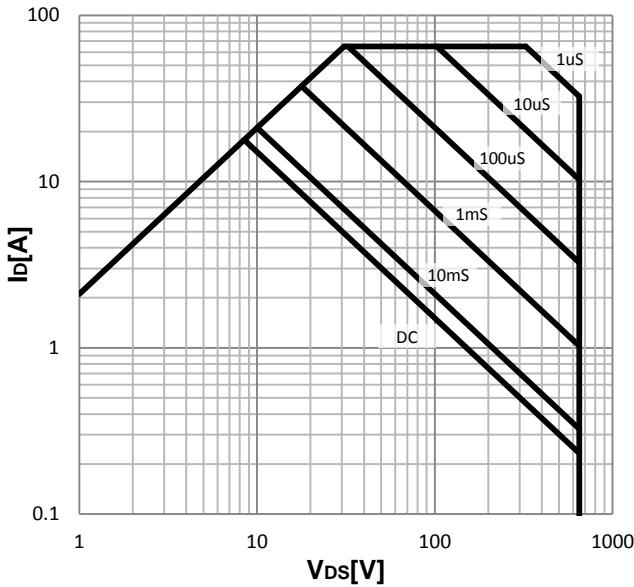
Symbol	Parameter	TSP_A_K65R190S2	TSF65R190S2	Unit
R_{eJC}	Thermal Resistance, Junction-to-Case	0.83	3.7	°C/W
R_{eCS}	Thermal Resistance, Case-to-Sink Typ.	0.5	-	°C/W
R_{eJA}	Thermal Resistance, Junction-to-Ambient	62	80	°C/W

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250µA, T _J = 25°C	650	-	-	V
		V _{GS} = 0V, I _D = 250µA, T _J = 150°C	-	700	-	V
ΔBV _{DSS} /ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250µA, Referenced to 25°C	-	0.6	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _D S = 650V, V _G S = 0V -T _C = 125°C	-	-	100	µA µA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _G S = 30V, V _D S = 0V	-	-	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _G S = -30V, V _D S = 0V	-	-	-100	nA
On Characteristics						
V _G S(th)	Gate Threshold Voltage	V _D S = V _G S, I _D = 250µA	2.0	3.0	4.0	V
R _D S(on)	Static Drain-Source On-Resistance	V _G S = 10V, I _D = 10A	-	-	0.19	Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _D S = 100V, V _G S = 0V, f = 1.0MHz	-	1505	-	pF
C _{oss}	Output Capacitance		-	68	-	pF
C _{rss}	Reverse Transfer Capacitance		-	2.1	-	pF
Q _g	Total Gate Charge	V _D S = 480V, I _D = 10A, V _G S = 10V (Note 4)	-	36.5	-	nC
Q _{gs}	Gate-Source Charge		-	8.7	-	nC
Q _{gd}	Gate-Drain Charge		-	12.5	-	nC
R _g	Gate resistance	f=1 MHz, open drain	-	9.8	-	Ω
Switching Characteristics						
t _d (on)	Turn-On Delay Time	V _D S = 400V, I _D = 10A R _G = 3.3Ω, V _G S = 10V (Note 4)	-	38	-	ns
t _r	Turn-On Rise Time		-	39	-	ns
t _d (off)	Turn-Off Delay Time		-	170	-	ns
t _f	Turn-Off Fall Time		-	47	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current	V _G S = 0V, I _S = 20A	-	-	20	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		-	-	65	A
V _{SD}	Drain-Source Diode Forward Voltage	V _G S = 0V, I _S = 20A	-	0.9	1.4	V
t _{rr}	Reverse Recovery Time	V _G S = 0V, V _D S = 400V, I _S = 10A, dI _F /dt = 100A/µs	-	318	-	ns
Q _{rr}	Reverse Recovery Charge		-	5.5	-	µC
I _{rrm}	Peak Reverse Recovery Current		-	24.9	-	A

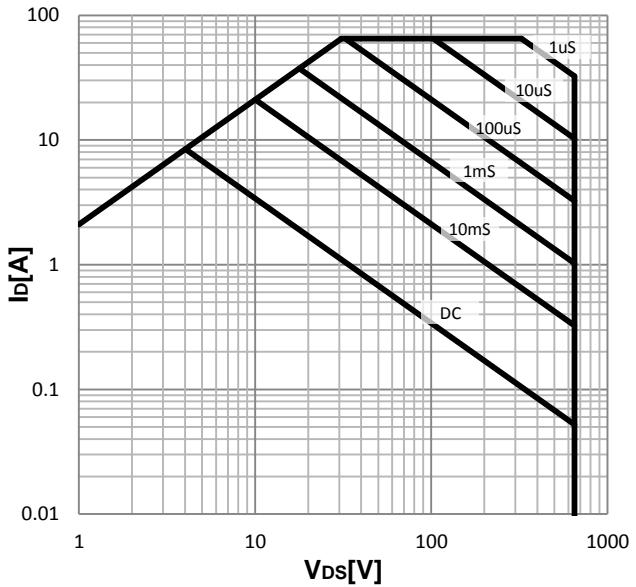
NOTES:

- Repetitive Rating: Pulse width limited by maximum junction temperature
- I_D=I_{AS}, V_DD=50V, Starting T_J=25 °C
- I_{SD}≤I_D, d_i/d_t ≤ 200A/us, V_DD ≤ BV_{DSS}, Starting T_J = 25 °C
- Essentially Independent of Operating Temperature Typical Characteristics

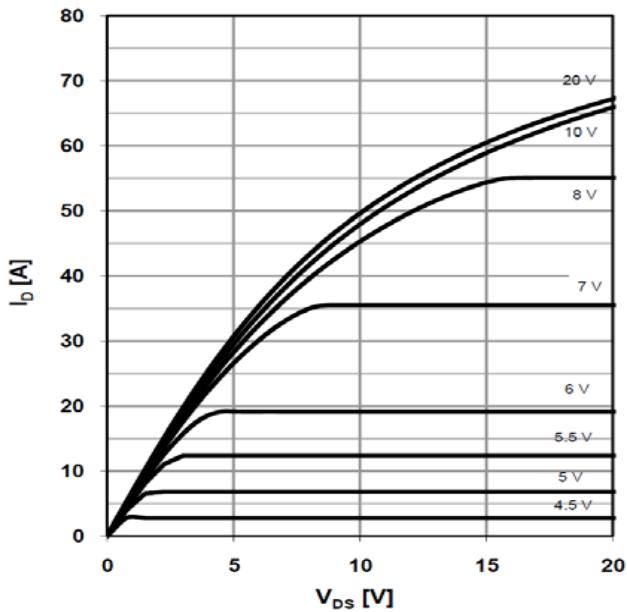
Safe operating area TC=25 °C
parameter: tp; TO-220, TO-247



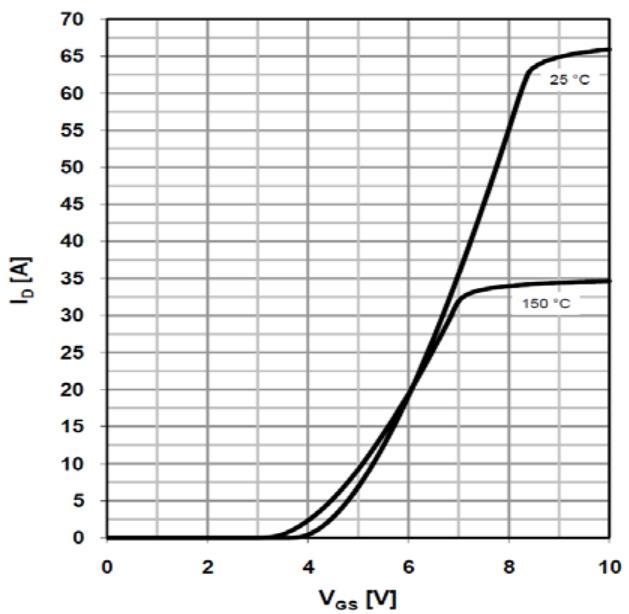
Safe operating area TC=25 °C
parameter: tp; TO-220FullPAK



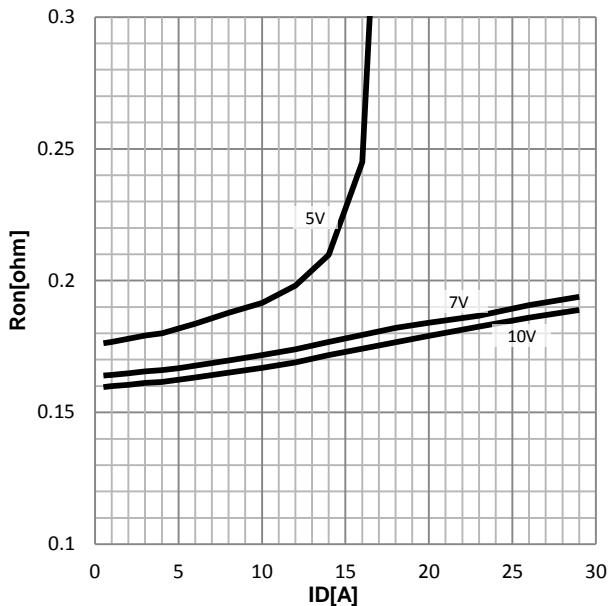
Typ. output characteristics $T_f=25$ °C
parameter: V_{GS}



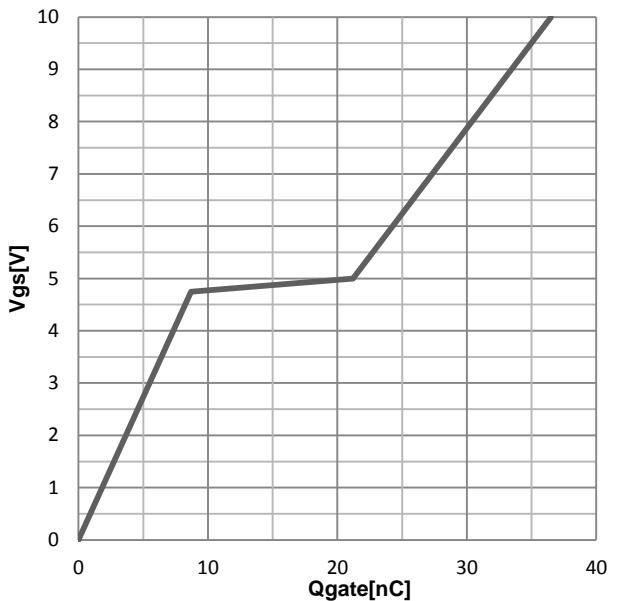
Typ. transfer characteristics



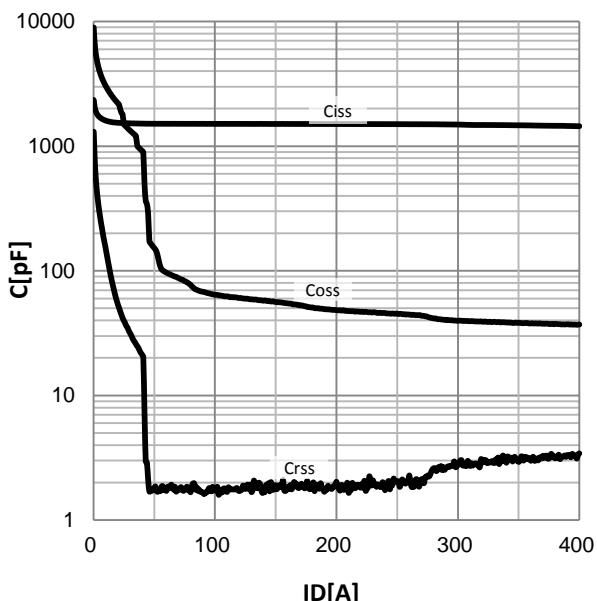
Typ. drain-source on-state resistance
parameter : V_{GS}



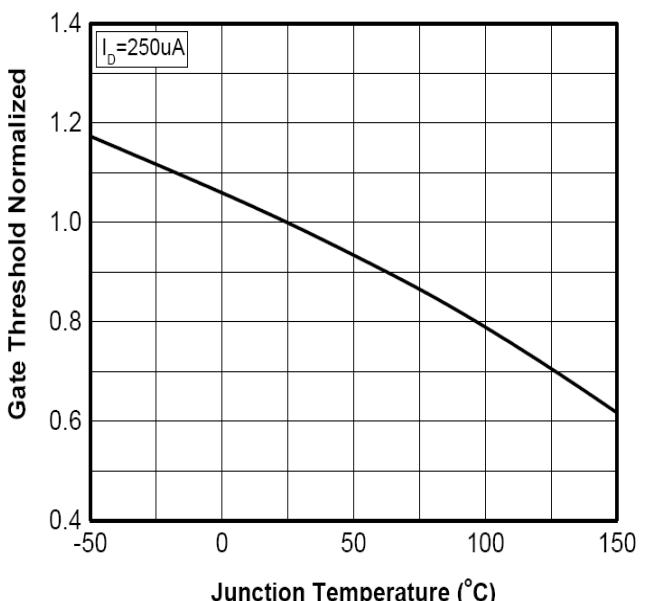
Typ. gate charge characteristics



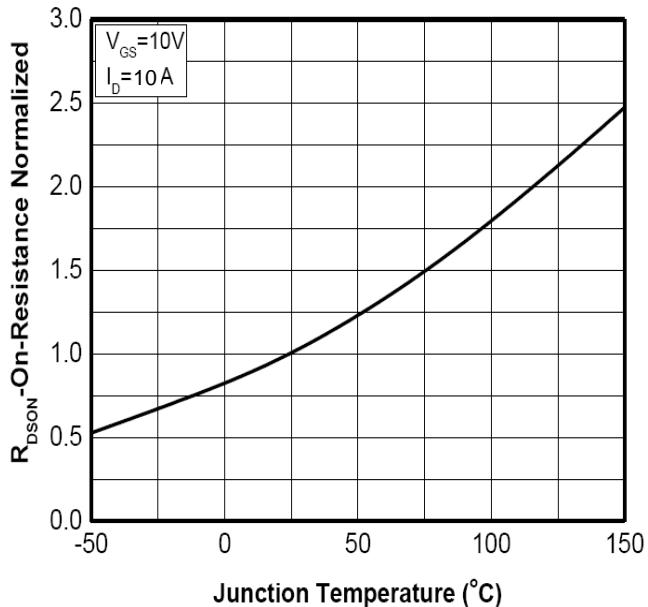
Typ. capacitances



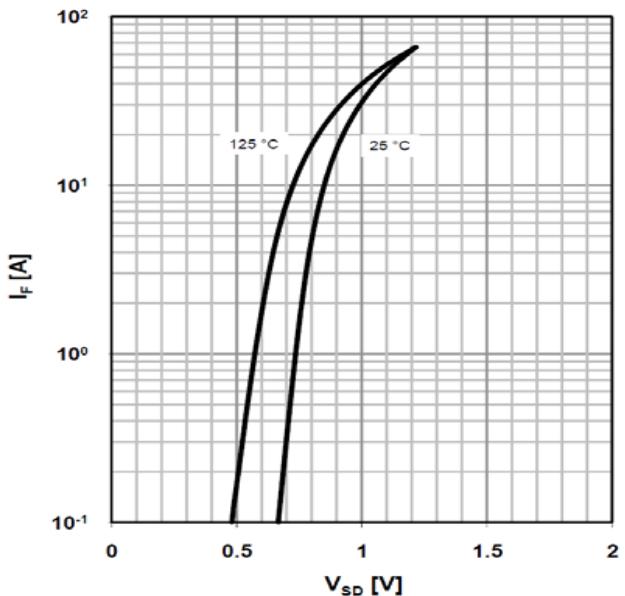
Normalized $V_{GS(th)}$ characteristics



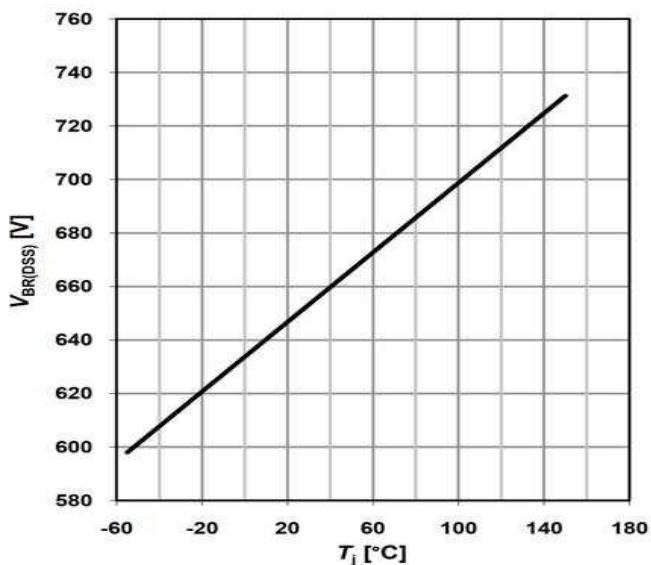
Normalized on resistance vs temperature



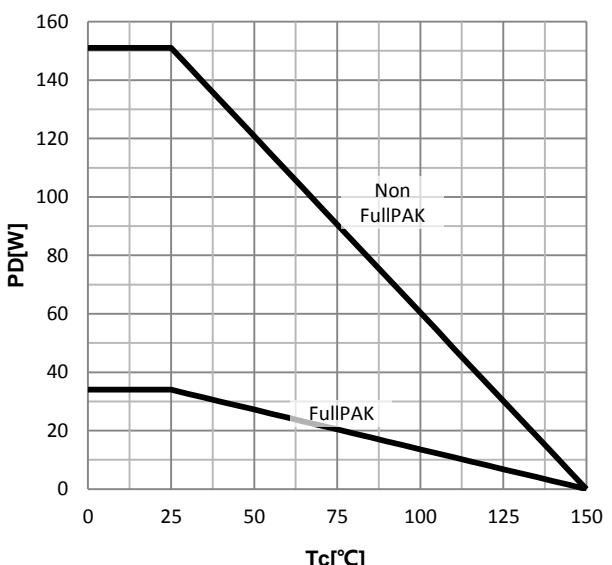
Forward characteristics of reverse diode



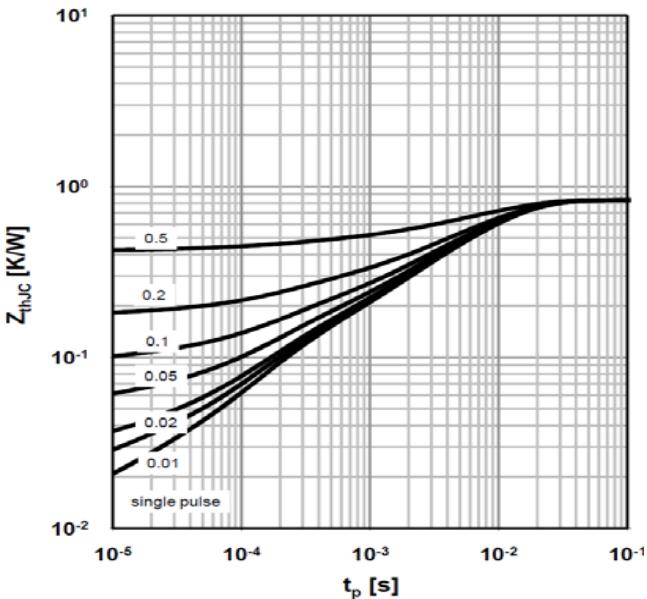
Drain-source breakdown voltage



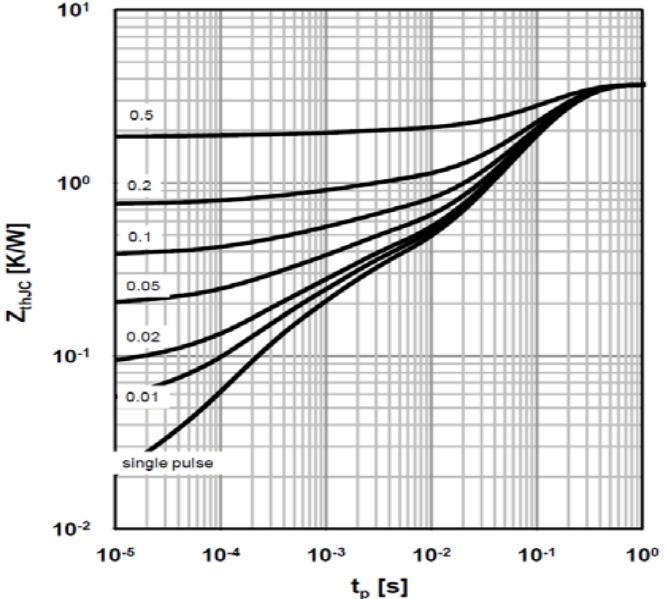
Power dissipation



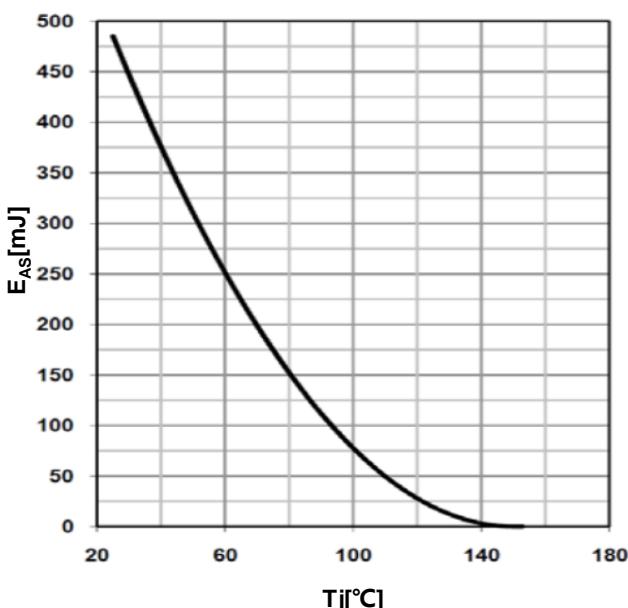
Max. transient thermal impedance parameter: D=tp/T; TO-220/3P/247

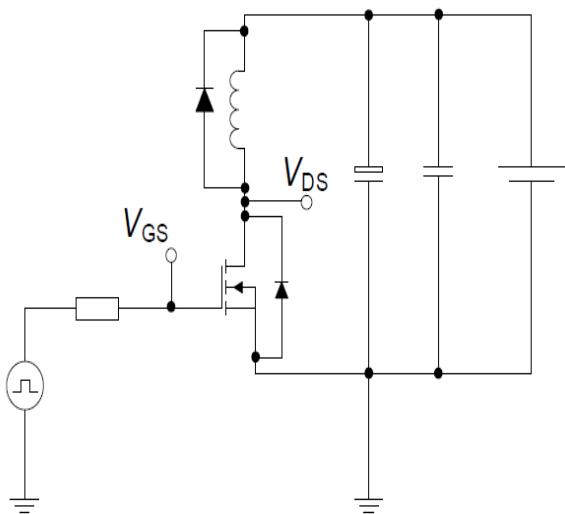
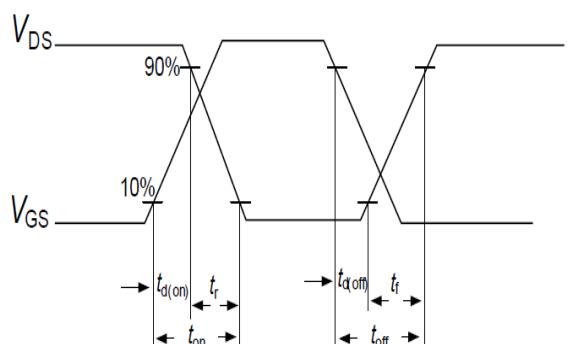
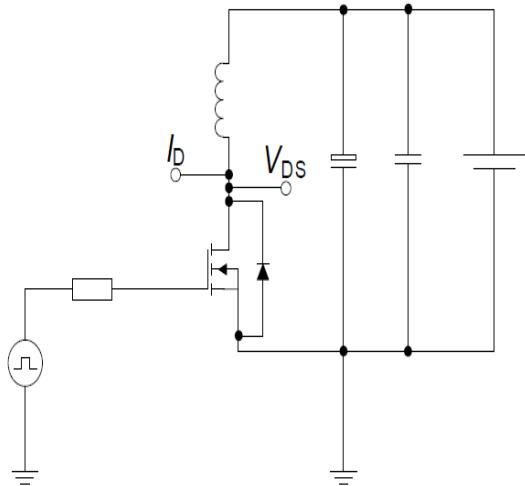
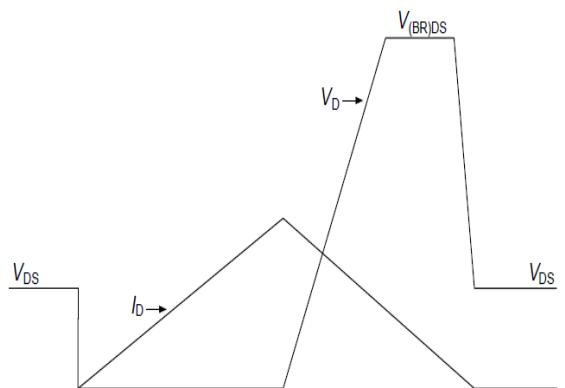


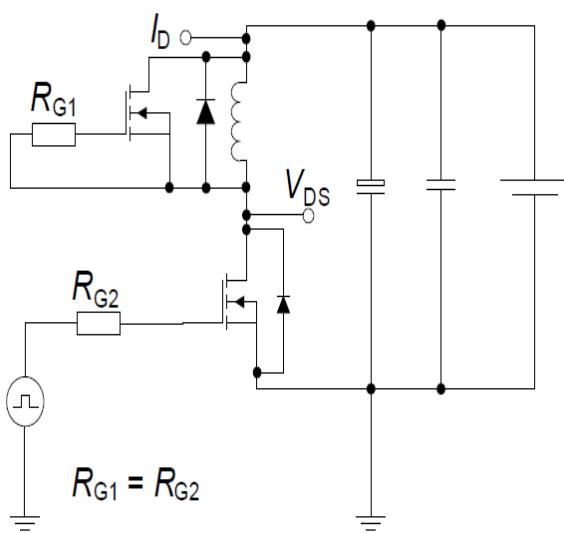
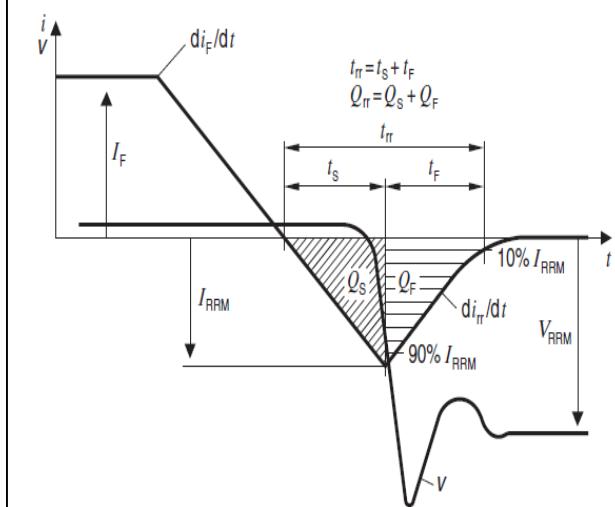
Max. transient thermal impedance parameter: D=tp/T; TO-220FullPAK



Avalanche energy



Switching times test circuit and waveform for inductive load
Switching times test circuit for inductive load

Switching time waveform

Unclamped inductive load test circuit and waveform
Unclamped inductive load test circuit

Unclamped inductive waveform


Test circuit and waveform for diode characteristics
Test circuit for diode characteristics

Diode recovery waveform




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