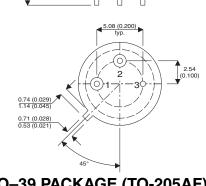


0.41 (0.016)



N-CHANNEL ENHANCEMENT MODE TRANSISTOR

FEATURES

- V_{(BR)DSS} = 200V
- I_D = 5.5A
- R_{DSON} = 0.40Ω

TO-39 PACKAGE (TO-205AF)

Underside View

PIN 1 – Source PIN 2 – Gate

PIN 3 – Drain

ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}C$ unless otherwise stated)

V _{DS}	Drain-Source Voltage	200V		
V _{GS}	Gate-Source Voltage	±20V		
I _D	Drain Current Continuous T _{Case} = 25°C	5.5A		
	T _{Case} = 100°C	3.5A		
I _{DM}	Drain Current Pulsed	22A		
P _D	Total Device Dissipation @ T _{Case} = 25°C	25W		
	T _{Case} = 100°C	10W		
T _J , T _{STG}	Operating and Storage Junction Temperature Range	–55 to +150°C		
THERMAL C	HARACTERISTICS			
$R_{ extsf{ heta}JC}$	Thermal Resistance Junction to Case	5.0°C/W		
$R_{ extsf{ heta}JA}$	Thermal Resistance Junction to Ambient	175°C/W		
ΤL	Maximum Lead Temperature 1.6mm from Case for	300°C		
_	10 secs.			

Semelab Plc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

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IRFF230 2N6798





ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise stated)

	Parameter	Test Co	Min.	Тур.	Max.	Unit	
V _{(BR)DSS}	Drain–Source Breakdown Voltage	$V_{GS} = 0$	I _D = 1mA	200			v
V _{GS(th)}	Gate Thresshold Voltage	V _{DS} =V _{GS}	$I_D = 250 \mu A$	2.0		4.0	v
I _{GSS}	Gate-Body Leakage	$V_{DS} = 0$	$V_{GS} = \pm 20V$			±100	nA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =0.8 x V _{(BR)DSS}				25	
		$V_{GS} = 0$	T _j = 125°C			250	μΑ
r _{DS(on)}	Drain–Source On–Resistance ¹	V _{GS} = 10V	I _D = 3.5A		0.25	0.4	Ω
gf _s	Forward Transconductance ¹	V _{DS} = 15V	I _D = 3.5A	2.5	3.0		s(ឋ)
C _{iss}	Input Capacitance		V – 0		600		
C _{oss}	Output capacitance	$V_{DS} = 25V$ $V_{GS} = 0$ f = 1.0MH ₇			250		pF
C _{rss}	Reverse Transfer Capacitance	- '=			80		1
t _{don}	Turn–On Delay Time	V _{DD} = 77V	$RL = 22\Omega$		8	30	
t _r	RiseTime	I _D = 3.5A	$V_{GEN} = 10V$		42	50	
t _{d(of)}	Turn off Delay Time	R _G = 7.5 ohms			12	50	ns
t _f	FallTime				30	40	
	SOURCE DRAIN DIODE RATING	CHARACTE	RISTICS				
V _{SD}	Diode Forward Voltage ¹	I _F = 5.5A	$V_{GS} = 0$			1.4	V
I _S	Continues Current					5.5	_
I _{SM}	Pulsed Current ²					22	A
t _{rr}	Reverse Recovery Time	I _F = 5.5A	$V_{DD} = 50V$		150	500	ns
Q _{rr}	Reverse Recovered Charge	dl _F /DT = 100)A/μS			6	μC

1) Pulse test : Pulse Width < 300μ s ,Duty Cycle < 2%

2) Pulse width limited by maximum juction temperature

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