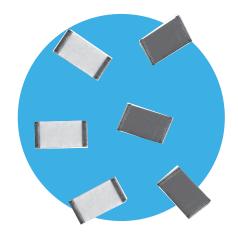
Resistors

High Voltage Chip Resistors

HVC Series

- Continuous voltages up to 3kV
- Overload voltages up to 4kV
- Values up to 1G0
- Tolerances to ±0.5%
- TCR to ±50ppm/°C
- Robust thick film construction
- Anti-sulphur version available







All Pb-free parts comply with EU Directive 2011/65/EU (RoHS2)

Electrical Data

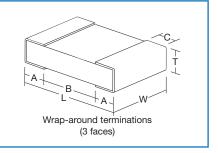
		1206	2010	2512	Notes	
Power rating @70°C	Watts	0.3	0.5	1		
Limiting element voltage	Volts	1000	2000	3000	DC 46 1	
Overload voltage (2s)	Volts	1500	3000	4000	DC or AC peak	
Resistance range	nce range Ohms 10K to 1G0				Consult factory for out of range values	
Resistance tolerance	%		See table of value ranges			
TCR	ppm/°C		50, 100			
Ambient temperature range	bient temperature range °C -55 to +155					
Values		E24 & E96 preferred			Any value to order	
Thermal Impedance	°C/W	200	80	70		

Value Ranges (Ohms)

Cino	TCD (nnm/9C)	Tolerance (%)					
Size	TCR (ppm/°C)	0.5	1 & 2	5 & 10			
1206	100	-	10K to 10M	10K to 100M			
1206		10K to 2M		10K to 1G0			
2040 8 2542	50	-		10K to 100M			
2010 & 2512	100			10K to 100M	10K to 1G0		

Physical Data

Dimensions (mm) & Weight (g)												
	L	W	Т	Α	В	С	Wt.					
1206	3.2±0.2	1.6±0.2	0.6±0.1	0.35±0.2	1.95 min	0.35±0.2	0.010					
2010	5.1±0.2	2.5±0.2	0.7±0.1	0.45±0.2	3.70 min	0.4±0.25	0.035					
2512	6.5±0.2	3.2±0.2	0.7±0.1	0.45±0.2	5.00 min	0.4±0.2	0.055					



General Note

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High Voltage Chip Resistors

HVC Series



Construction

Resistive thick film material, overglaze and organic protection are screen printed on a 96% alumina substrate. The design and laser adjustment of the resistive element optimises the limiting element voltage of the resistor.

Terminations

The chips are supplied with wrap-around terminations suitable for soldering. Consult factory for alternative termination options.

Solderability

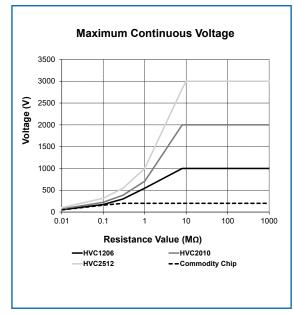
The terminations have an electroplated nickel barrier and tin finish. This ensures excellent 'leach' resistance properties and solderability.

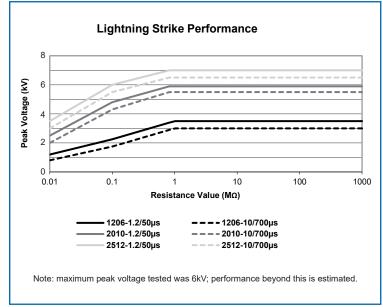
Marking

The body protection is resistant to all normal cleaning solvents suitable for printed circuits. The chips are not marked and the relevant information on type, value, tolerance date code and quantity are recorded on the reel.

Performance Data

		Maximum	Typical
	A D 0/	1206 : 2	1206 : 1
Load at rated power: 1000 hours rated load @ 70°C	ΔR%	2010/2512 : 1	2010/2512 : 0.25
Shelf life test: 12 months at room temperature	ΔR%	0.1	0.02
Derating from rated power at 70°C	•••••	Zero	at 155°C
Short term overload: Lesser of 6.25 x rated power or Maximum overload voltage	ΔR%	2	0.2
Lightning strike: 1.2/50µs & 10/700µs - see graph for peak voltage	ΔR%	0.5	0.2
Dry heat: 1000 hours at 155°C	ΔR%	0.5	0.1
Long term damp heat	ΔR%	1	0.25
Temperature rapid change	ΔR%	0.25	0.05
Resistance to solder heat	ΔR%	0.25	0.05
Resistance to sulphur-bearing gas (AS version only): ASTM-B-80	9	0.25	0.05
Voltage proof	Volts	500	
		1206 : -25	1206 : -15
Voltage coefficient of resistance	ppm/V	2010 : -15	2010 : -5
		2512 ≤100M: -5 2512 >100M: -15	2512 ≤100M: -1.5 2512 >100M: -8





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BI Technologies IRC Welwyn

HVC Series



Application Notes

HVC resistors are ideally suited for handling by automatic methods due to their rectangular shape and the small dimensional tolerances. Electrical connection to a ceramic substrate or to a printed circuit board can be made by reflow or wave soldering of wrap-around terminations.

Wrap-around terminations provide good leach properties and ensure reliable contact. Due to the robust construction, the HVC can be immersed in the solder bath for 30 seconds at 260°C. This enables the resistor to be mounted on one side of a printed circuit board and wire-leaded components applied on the other side.

HVC resistors themselves can operate at a maximum

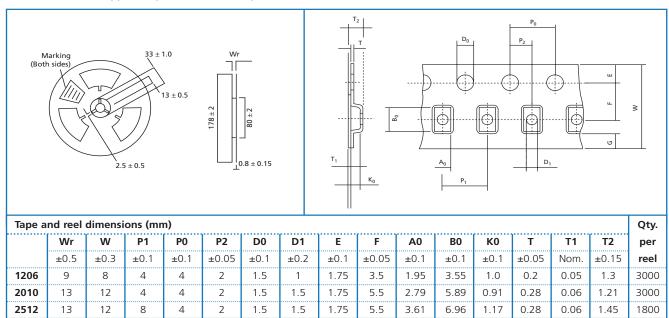
temperature of 155°C (see performance above). For soldered resistors, the joint temperature should not exceed 110°C. This condition is met when the stated power levels at 70°C are used.

The PCB layout should avoid tracks running between the HVC mounting pads, as this would compromise the LEV.

The LEV stated applies to operation at sea-level pressure, in a non-condensing atmosphere and non-contaminating environment. Voltage derating should be applied if low pressure, high humidity or contamination may be encountered. The termination clearance dimension (B) should be used in conjunction with the creepage limit applicable to the circuit application in order to determine the derated LEV.

Packaging

HVC Resistors are supplied taped and reeled as per IEC 286-3.



Ordering Procedure

Example: HVC2512-4M7FT18 (2512, 4.7 megohms ±1%, with ±100ppm/°C TCR and standard terminations, Pb-free)

Н	V C	2	5	1	2			Ш	-	4	M	7		F	Т	1	8	
	1		2	2		3	Ш	4			5		I	6		7		

1	2	3	3 4 5		3 4 5 6		6			7		
Type	Size	TCR	Anti-Sulphur	Value	Tolerance	Т	Termination & Pac					
HVC	1206	Omit for	Omit for standard	E24 = 3/4 characters	D = ±0.5%		Pb-free	finish (RoHS)				
	2010	±100ppm/°C	AS = Anti-sulphur	E96 = 3/4 characters	F = ±1%	Т3	1206,	3000/reel				
	2512	$C = \pm 50 \text{ppm/°C}$		K = kilohms	G = ±2%	13	2010	3000/1661				
				M = megohms	J = ±5%	T18	2512	1800/reel				
			•		K = ±10%		Snl	Pb finish				
						PB Quantities as for Pb-free						

General Note

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