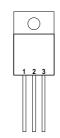


1 2 3

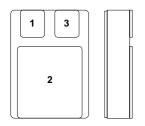
 $\begin{aligned} &\text{Pin 1} - \text{ADJ.} \\ &\text{Pin 2} - \text{V}_{\text{OUT}} \\ &\text{Pin 3} - \text{V}_{\text{IN}} \end{aligned}$

Case – V_{OUT} **G Package – TO257**



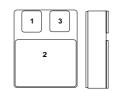
 $\begin{array}{l} \text{Pin 1} - \text{ADJ.} \\ \text{Pin 2} - \text{V}_{\text{OUT}} \\ \text{Pin 3} - \text{V}_{\text{IN}} \\ \text{Case} - \text{Isolated} \end{array}$

IG Package - TO257



 $\begin{aligned} &\text{Pin 1} - \text{ADJ.} \\ &\text{Pin 2} - \text{V}_{\text{OUT}} \\ &\text{Pin 3} - \text{V}_{\text{IN}} \end{aligned}$

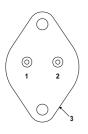
SMD1
CERAMIC SURFACE
MOUNT



 $\begin{aligned} &\text{Pin 2} - \text{V}_{\text{OUT}} \\ &\text{Pin 3} - \text{V}_{\text{IN}} \\ &\text{SMD05} \\ &\text{CERAMIC SURFACE} \end{aligned}$

MOUNT

Pin 1 - ADJ.



Pin 1 – ADJ. Pin 2 – V_{IN} Case – V_{OUT} **K Package – TO3**

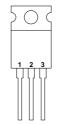
 $\begin{aligned} &\text{Pin 1} - \text{ADJ.} \\ &\text{Pin 2} - \text{V}_{\text{IN}} \\ &\text{Case} - \text{V}_{\text{OUT}} \end{aligned}$

R Package – TO66

1.5 AMP POSITIVE ADJUSTABLE VOLTAGE REGULATOR FOR HI-REL APPLICATIONS

FEATURES

- Output voltage range of:
 1.25 to 40V for standard version
 1.25 to 60V for HV version
- Output voltage tolerance 1%
- Load regulation 0.3%
- Line regulation 0.01%/V
- Complete series of protections:
 current limiting
 thermal shutdown
 soa control
- Also available in TO220 metal isolated package (1mm pins)



Pin 1 – ADJ. Pin 2 – V_{OUT} Pin 3 – V_{IN}

T Package - TO220 Plastic

ABSOLUTE MAXIMUM RATINGS (T_{case} = 25°C unless otherwise stated)

Input - Output Differential Voltage	Standard	40V
	HV Series	60V
Output Current		Internally limited
Power Dissipation		Internally limited
Operating Junction Temperature Range		See Table Above
Storage Temperature		−65 to 150°C
	Output Current Power Dissipation Operating Junction Temperature Range	- HV Series Output Current Power Dissipation Operating Junction Temperature Range

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						7A , IP11		IP1			
 Paran	neter	Test Conditions		Min.	17A , LM Typ.	Max.	Min.	17,LM11 Typ.	Max.	Units	
- aran		I _{OUT} = 10mA	0113		1.238	1.25	1.262	1411111	· yp.	- INIGAL	V
V_{REF}	Reference Voltage	$I_{OUT} = 10$ mA to I_{MAX}									
I KEF		$V_{IN} - V_{OUT} = 3V \text{ to } V_{MAX}$			1.225	1.250	1.270	1.200	1.250	1.300	_V
		$P \le P_{MAX}$ $T_J = -55 \text{ to } +150^{\circ}\text{C}$									
ΔVουτ		$V_{IN} - V_{OUT} = 3V \text{ to } V_{MAX}$ $T_{J} = -55 \text{ to } +150^{\circ}\text{C}$				0.005	0.010		0.010	0.020	
$\frac{\Delta V_{IN}}{\Delta V_{IN}}$	Line Regulation ¹					0.010	0.020		0.020	0.050	- % / V
IIN		I _{OUT} = 10mA t		V _{OUT} ≤ 5V		5	15		5	15	mV
ΔV_{OUT}			IVIAX	V _{OUT} ≥ 5V		0.1	0.3		0.1	0.3	%
ΔI_{OUT}	Load Regulation ¹	$I_{OUT} = 10$ mA to I_{MAX}		V _{OUT} ≤ 5V		15	50		20	50	mV
		$T_J = -55 \text{ to } +150^{\circ}\text{C}$ $V_{OUT} \ge 5\text{V}$				0.3	1		0.3	1	%
	Thermal Regulation	$t_p = 20 \text{ms}$		001		0.002	0.020		0.030	0.070	%/W
		'		$C_{ADJ} = 0$		65			65		dB
	Ripple Rejection	$V_{OUT} = 10V$ f = 120Hz		C _{ADJ} = 10μF	66	80		66	80		dB
		1 = 120112	$T_J = -5$	55 to +150°C	00						"
I _{ADJ}	Adjust Pin Current	$T_J = -55 \text{ to } +150^{\circ}\text{C}$				50	100		50	100	μΑ
Δ1	Adjust Pin Current Change	I _{OUT} = 10mA to I _{MAX}				0.2	5		0.2	5	μА
ΔI_{ADJ}		$T_J = -55 \text{ to } +150^{\circ}\text{C}$									
		$V_{IN} - V_{OUT} = 2$									
I _{MIN}	Minimum Load Current	$V_{IN} - V_{OUT} = 40V$				3.5	5 7		3.5 5	5	
		$T_{J} = -55 \text{ to } +150^{\circ}\text{C}$								mA	
		$V_{IN} - V_{OUT} = 60V$ (HV SERIES) $T_{J} = -55 \text{ to } +150^{\circ}\text{C}$						3.5	7		
									3.5 <i>i</i>	,	
		$V_{IN} - V_{OUT} \le 15V$ $T_{J} = -55 \text{ to } +150^{\circ}\text{C}$			1.5	2.2		1.5	2.2		A
l _{lo} .	Current Limit				1.0	2.2		1.0	2.2		
I _{CL}		$V_{IN} - V_{OUT} = 40V$			0.30	0.50		0.30	0.50		A
		$V_{IN} - V_{OUT} = 60V$ (HV SERIES)				0.10			0.10		
	Temperature	$T_{J} = -55 \text{ to } +1$	50°C			1	2		1		%
	Stability						_		•		
$\frac{\Delta V_{OUT}}{\Delta TIME}$	Long Term Stability	T _A = +125°C		t = 1000 Hrs		0.3	1		0.3	1	%
e _n	RMS Output Noise (% of V _{OUT})	f = 10 Hz to 10) kHz			0.001			0.001		%
D	Thermal Resistance	K Package			2.3	3		2.3	3		
$R_{\theta JC}$	Junction to Case	R Package			5	7		5	7	°C/W	
		G, IG Packages				3	5		3	5	

¹⁾ Regulation is measured at constant junction temperature, using pulse testing at a low duty cycle. Changes in output voltage due to heating effects are covered under thermal regulation specifications. Load regulation is measured from the bottom of the package for the TO–3 and TO–66 packages, at the junction of the wide and narrow portion of the output lead for the TO–220 package, and 1/8" below the base of the package on the output pin of the TO–257 package.

 $V_{IN} - V_{OUT} = 5V \; , \; T_J = 25^{\circ}C \; , \; I_{OUT} = 0.5A \; , \; \; I_{MAX} = \; 1.5A. \label{eq:vinitarity}$

 $P_{MAX} = 10W$ for SMD1, 20W for all other package styles.

 $V_{MAX} = 40V$ for standard series , 60V for HV series.

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²⁾ Test Conditions unless othewise stated:



					IP317A IP317AHV			IP317 IP317HV			
Paran	neter	Test Conditions			Min.	Тур.	Max.	Min.	Тур.	Max.	Units
		I _{OUT} = 10mA			1.238	1.25	1.262				V
\/	Deference Veltage	I _{OUT} = 10mA to									
V_{REF}	Reference Voltage	$V_{IN} - V_{OUT} = 3V \text{ to } V_{MAX}$			1.225	1.25	1.270	1.200	1.250	1.300	V
		$P \le P_{MAX}$ $T_J = 0 \text{ to } 125^{\circ}C$									
ΔV_{OUT}	Line Regulation ¹	$V_{IN} - V_{OUT} = 3V \text{ to } V_{MAX}$ $T_{J} = 0 \text{ to } 125^{\circ}C$				0.005	0.010		0.010	0.040	%/V
ΔV_{IN}	Line Regulation					0.010	0.020		0.020	0.070	
		I _{OUT} = 10mA to	o I _{MAX}	V _{OUT} ≤ 5V		5	25		5	25	mV
ΔV_{OUT}	Land Danidation 1			V _{OUT} ≥ 5V		0.1	0.5		0.1	0.5	%
ΔI_{OUT}	Load Regulation ¹			V _{OUT} ≤ 5V		15	50		20	70	mV
		$T_J = -55 \text{ to } +150^{\circ}\text{C}$ $V_{OUT} \ge 5V$		V _{OUT} ≥ 5V		0.3	1		0.3	1.5	%
	Thermal Regulation	t _p = 20ms			0.002	0.020		0.030	0.070	%/W	
		10)/		$C_{ADJ} = 0$	65				65		dB
	Ripple Rejection	V _{OUT} = 10V		C _{ADJ} = 10μF	66	00		00	00		40
		f = 120Hz	$T_J = 0 \text{ to } 125^{\circ}\text{C}$			80		66	80		dB
I _{ADJ}	Adjust Pin Current	$T_{\rm J} = 0$ to 125°	5°C			50	100		50	100	μΑ
,,,,,,	Adinat Dia Comant	$I_{OUT} = 10$ mA to I_{MAX} $T_{J} = 0$ to 125 °C				0.2	5		0.2	5	μА
	Adjust Pin Current Change										
		$V_{IN} - V_{OUT} = 2.5V \text{ to } V_{MAX}$									
	Minimum Load Current	$V_{IN} - V_{OUT} = 40V$ $T_J = 0 \text{ to } 125^{\circ}\text{C}$ $V_{IN} - V_{OUT} = 60V \text{ (HV SERIES)}$				3.5	10		2.5	10	
I _{MIN}									3.5		
						3.5	12		3.5	12	mA
		T _J = 0 to 125°C									
	Current Limit	$V_{IN} - V_{OUT} \le 15V$ $T_{J} = 0 \text{ to } 125^{\circ}\text{C}$				2.2		1 5	2.2		A A
								1.5			
I _{CL}		$V_{IN} - V_{OUT} = 40V$				0.40		0.15	0.40		
		V _{IN} - V _{OUT} = 60V (HV SERIES)				0.10			0.10		
ΔV_{OUT}	Temperature	T 0 to 125%				1			1		0/
ΔTEMF	Stability	$T_{J} = 0 \text{ to } 125^{\circ}$	125°C			ı	1 2		1		%
ΔV _{OUT} ΔTIME	Long Term Stability	t = 1000 Hrs			0.3	1		0.3	1	%	
e _n	RMS Output Noise	f = 10 Hz to 10 kHz			0.003			0.003		%	
D	(% of V _{OUT}) Thermal Resistance	istance K Package				2.3	3		2.2	2	
$R_{\theta JC}$		K Package						2.3	7	°C/W	
	Junction to Case T Package			4	5		5	1	1		

¹⁾ Regulation is measured at constant junction temperature, using pulse testing at a low duty cycle. Changes in output voltage due to heating effects are covered under thermal regulation specifications. Load regulation is measured from the bottom of the package for the TO–3 and TO–66 packages, at the junction of the wide and narrow portion of the output lead for the TO–220 package, and 1/8" below the base of the package on the output pin of the TO–257 package.

2) Test Conditions unless othewise stated:

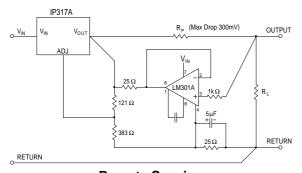
 $V_{IN} - V_{OUT} = 5V \; , \; T_J = 25^{\circ}C \; , \; I_{OUT} = 0.5A \; , \; \; P_{MAX} = 20W \; , \; I_{MAX} = \; 1.5A$

 $V_{MAX} = 40V$ for standard series , 60V for HV series.

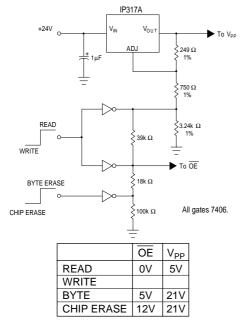
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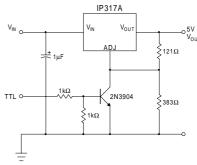
APPLICATIONS INFORMATION



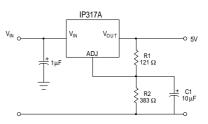
Remote Sensing



2816 EEPROM Supply Programmer for **Read/Write Control**

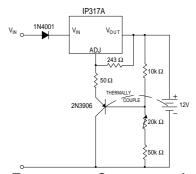


5V Regulator with Shut Down



C1 improves ripple rejection. $X_{\mathbb{C}}$ should be small compared to R2.

Improving Ripple Rejection



Temperature Compensated Lead-Acid Battery Charger

Order Information

Part Number LM117	IG–Pack G–Pack (TO257) ✔	SMD1	SMD05	K-Pack (TO3)	R-Pack (TO66)	T-Pack (TO220)	Temp. Range -55 to +150°C	Note:
LM117HV LM117A	<i>V</i>	<i>V</i>	<i>'</i>	V V	<i>V</i>		"	To order, add the package
LM117AHV	~	~	~	~	~		"	identifier to the part number.
IP117	~	~	~	~	~		-55 to +150°C	eg. IP117AHVK
IP117HV	V	'	~	'	~		"	IP1175SMD05
IP117A	~	~	~	~	~		"	IP317T
IP117AHV	V	~	'	~	'		"	11 3171
LM317				~		V	0 to 125°C	
LM317HV				~		V	"]
IP317				~		V	"	1
IP317HV				~		V	"]
IP317A				~		~	"	
IP317AHV				~		V	"	1

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