## Photologic ${ }^{\circledR}$ Slotted Optical Switch

## OPB916 Series

## Features:

- Low power consumption
- Data rates to 250 kBaud
- Choice of two logic states and two electrical outputs
- $24^{\prime \prime}$ (610 mm) minimum 26 AWG UL listed wires
- Slot width $0.20^{\prime \prime}$ ( 5.08 mm )
- Slot Depth 0.635 " ( 16.13 mm )



## Description:

The OPB916 series of Photologic ${ }^{\circledR}$ photo integrated circuit switches provide optimum flexibility. Each switch consists of an infrared Light Emitting Diode (LED) and a Photologic ${ }^{\circledR}$ photo integrated circuit, mounted in an opaque housing with clear windows for dust protection. The deep slot allows for a longer reach of the optical path from the $0.650^{\prime \prime}$ ( 16.5 mm ) mounting plane. Internal apertures are 0.010 " x .060 " (. $25 \mathrm{~mm} \times 1.52 \mathrm{~mm}$ ) for the Photologic's " S " side and 0.05 " x 0.06 " ( $1.27 \mathrm{~mm} \times 1.52 \mathrm{~mm}$ ) for the LED "E" side.

Devices in this series exhibit stable performance over supply voltages ranging from 4.5 V to 16.0 V , and may be specified as buffered or inverted with an internal $10 \mathrm{k} \Omega$ pull-up resistor or open collector output. Devices are TTL/LSTTL compatible and can drive up to 10 TTL loads.

Custom electrical, wire or cabling are available. Contact your local representative or OPTEK for more information.

## Applications:

- Mechanical switch replacement
- Speed indication (tachometer)
- Mechanical limit indication
- Edge sensing

| Ordering Information |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Part <br> Number | LED Peak Wavelength | Sensor Photologic ${ }^{\circledR}$ | Slot <br> Width / <br> Depth | Aperture Emitter / Sensor | Lead <br> Length / <br> Wire |
| OPB916BZ | 880 nm | 10K Pull-Up | $\begin{gathered} 0.200 " / \\ 0.635 \prime \prime \end{gathered}$ | $\begin{gathered} 0.05 " / \\ 0.01 " \end{gathered}$ | $\text { 24" / } 26$ <br> AWG Wire |
| OPB916IZ |  | Inv-10K Pull-Up |  |  |  |
| OPB916BOCZ |  | Open-Collector |  |  |  |



## Photologic ${ }^{\otimes}$ Slotted Optical Switch



| Absolute Maximum Ratings $\left(\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}\right.$ unless otherwise noted) |  |
| :--- | ---: |
| Storage \& Operating Temperature Range | $-40^{\circ} \mathrm{C}$ to $+80^{\circ} \mathrm{C}$ |
| Input Infrared LED |  |
| Diode Reverse DC Voltage | 2 V |
| Input Diode Power Dissipation ${ }^{(2)}$ | 75 mW |
| Forward DC Current | 50 mA |
| Output Photologic ${ }^{\circledR}$ | 30 V |
| Supply Voltage, $\mathrm{V}_{\text {CC }}$ (not to exceed 3 seconds) | 90 mW |
| Voltage at Output Lead (Open Collector Output) |  |
| Output Photologic ${ }^{\circledR}$ Power Dissipation ${ }^{(3)}$ |  |

## Notes:

(1) RMA flux is recommended. Duration can be extended to 10 seconds maximum when flow soldering.
(2) Derate linearly $1.67 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $25^{\circ}$.
(3) Derate linearly $2.67 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $25^{\circ}$.
(4) Normal application would be with light source blocked, simulated by $\mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}$.
(5) All parameters tested using pulse technique.

## Photologic ${ }^{\circledR}$ Slotted Optical Switch

## TT Electronics

Electrical Characteristics ( $T_{A}=25^{\circ} \mathrm{C}$ unless otherwise noted)

| SYMBOL | PARAMETER | MIN | TYP | MAX | UNITS | TEST CONDITIONS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Input Diode |  |  |  |  |  |  |
| $V_{\text {F }}$ | Forward Voltage | - | 1.3 | 1.8 | V | $\mathrm{I}_{\mathrm{F}}=20 \mathrm{~mA}$ |
| $I_{R}$ | Reverse Current | - | - | 100 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{R}}=2 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |
| Output Photologic ${ }^{\circledR}$ Sensor |  |  |  |  |  |  |
| $\mathrm{V}_{\text {cc }}$ | Operating DC Supply Voltage | 4.5 | - | 16 | V | - |
| $I_{\text {cCL }}$ | Low Level Supply Current: <br> Buffered with 10k pull-up ${ }^{(1)}$ <br> Buffered Open-Collector Output ${ }^{(1)}$ | - | - | 7 | mA | $\mathrm{V}_{\mathrm{CC}}=16 \mathrm{~V}, \mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}$, No Output Load |
|  | Inverted with 10k pull-up: <br> Inverted Open-Collector Output | - | - | 7 | mA | $\mathrm{V}_{\mathrm{CC}}=16 \mathrm{~V}, \mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$, No Output Load |
| $\mathrm{I}_{\mathrm{CCH}}$ | High Level Supply Current: <br> Buffered with 10k pull-up <br> Buffered Open-Collector Output | - | - | 6 | mA | $\mathrm{V}_{\mathrm{CC}}=16 \mathrm{~V}, \mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$, No Output Load |
|  | Inverted with 10k pull-up: <br> Inverted Open-Collector Output ${ }^{(1)}$ | - | - | 6 | mA | $\mathrm{V}_{\mathrm{CC}}=16 \mathrm{~V}, \mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}$, No Output Load |
| $\mathrm{V}_{\text {OL }}$ | Low Level Output Voltage: <br> Buffered with 10k pull-up <br> Buffered Open-Collector Output | - | - | 0.4 | V | $\mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V}, \mathrm{I}_{\mathrm{OL}}=16 \mathrm{~mA}, \mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}$ |
|  | Inverted with 10k pull-up: <br> Inverted Open-Collector Output | - | - | 0.4 | V | $\mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V}, \mathrm{I}_{\mathrm{OL}}=16 \mathrm{~mA}, \mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ |
| $\mathrm{V}_{\mathrm{OH}}$ | High Level Output Voltage: <br> Buffered with 10k pull-up | $\begin{aligned} & \mathrm{V}_{\mathrm{cc}}- \\ & 2.0 \end{aligned}$ | - | - | V | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V} \text { to } 16 \mathrm{~V}, \mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}, \\ & \mathrm{I}_{\mathrm{OH}}=100 \mu \mathrm{~A} \end{aligned}$ |
|  | Inverted with 10k pull-up: | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}- \\ & 2.0 \end{aligned}$ | - | - | V | $\mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V}$ to $16 \mathrm{~V}, \mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}$, |
| Іон | High Level Output Current: <br> Buffered with 10k pull-up <br> Buffered Open-Collector Output | - | 1.0 | 10 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V}, \mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}, \mathrm{~V}_{\mathrm{OH}}=30 \mathrm{~V}$ |
|  | Inverted with 10k pull-up: <br> Inverted Open-Collector Output ${ }^{(1)}$ | - | 1.0 | 10 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V}, \mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}, \mathrm{~V}_{\mathrm{OH}}=30 \mathrm{~V}$ |

## Photologic ${ }^{\circledR}$ Slotted Optical Switch

| Electrical Characteristics ( $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ unless otherwise noted) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| SYMBOL | PARAMETER | MIN | TYP | MAX | UNITS | TEST CONDITIONS |
| Output Photologic® ${ }^{\text {® }}$ Sensor |  |  |  |  |  |  |
| $\mathrm{I}_{\mathrm{F}(+)}$ | LED Positive-Going Threshold Current Buffered with 10k pull-up Inverted with 10k pull-up | - | 5 | 10 | mA | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$, No Output Load |
|  | Buffered Open-Collector Output Inverted Open-Collector Output ${ }^{(1)}$ | - | 5 | 10 | mA | $\mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V}, \mathrm{I}_{\mathrm{OL}}=16 \mathrm{~mA}$ |
| $\mathrm{I}_{\mathrm{F}(+) / \mathrm{F}(-)}$ | Hysteresis | - | 1.5 | - | - | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ |
| $\mathrm{t}_{\mathrm{r}} \mathrm{t}_{\mathrm{f}}$ | Rise Time, Fall Time | - | 50 | - | ns | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}, \mathrm{I}_{\mathrm{F}}=0 \text { or } 10 \mathrm{~mA}, \\ & \mathrm{R}_{\mathrm{L}}=300 \Omega \text { to } 5 \mathrm{~V}, \mathrm{C}_{\mathrm{L}}=50 \mathrm{pF} \end{aligned}$ |
| $\mathrm{t}_{\text {PLH }} \mathrm{t}_{\text {PHL }}$ | Propagation Delay | - | 3 | - | $\mu \mathrm{s}$ |  |

Notes:
(1) Normal application would be with light source blocked, simulated by $\mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}$.
(2) All parameters tested using pulse technique.

## Photologic ${ }^{\circledR}$ Slotted Optical Switch



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| Issue | Change Description | Approval | Date |
| :---: | :--- | :---: | :---: |
| A | Initial Release, new format | Steve Coble | 12/13/06 |
| A.1 | Fixed Ordering Table page 1 | Bob Procsal | $1 / 25 / 08$ |
| A.2 | Fixed nomenclature on schematics page 1 | Mark Miller | 8/04/08 |
| A.3 | Changed mechanical drawing, Absolute Max Ratings and some <br> Electrical Characteristics | Mark Miller | 08/07/08 |
| B | Transferred to the new TT Electronics template | L. Timpa | 10/6/16 |
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