### **OPI1268**

#### Features:

- TTL compatible output
- 16 kV dc isolation •
- 2Mbit/s
- t<sub>PHL</sub>-t<sub>PLH</sub> ≤ 500 ns
- Creepage path: 0.970" (24.64 mm) •
- Air path: 0.970" (24.64 mm) •
- UL recognized file No. E58730 •



#### **Description:**

The OPI1268 is a high voltage isolator with a digital output that is capable of high speed data transmission. The input of the OPI1268 consists of a high-efficiency GaAIAs LED with a peak wavelength of 850 nm, which is optically coupled to the output optical IC. A photodiode in the output IC detects the incoming modulated light and converts it to a proportionate current. This current is fed into a high-gain linear amplifier which is temperature, current and voltage compensated. The result is a highly stable digital output with an open collector inverter configuration. This device produces DC and AC voltage isolation between the input and output circuitry while providing TTL signal integrity.

#### **Applications:**

- Data transmission for Hi voltage isolation
- PCBoard power system isolation
- Industrial equipment po isolation
- Medical equipment pow isolation

Pin

1 2

3

4

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Office equipment

				0				
for High stem	Part Number	LED Peak Wavelength	Sensor Photologic®	Isolation Voltage kVDC	<b>t<sub>PLH</sub> / t<sub>PHL</sub></b> Max (ns)	<b>I<sub>F</sub> (mA)</b> Typ / Max	V <sub>ce</sub> (V) Max	Lead Length / Spacing
ent power	OPI1268	850 nm	Open Collector	16	100 / 200	10 / 50	18	0.12" / 0.98"
nt power		+ <sub>E</sub>	[27.94]	ē,	[6.35] .250 *		1	
Function	+	-	1.100 *					[0.64] .025 X 45°
Anode	[8.89]						T	
Vcc								
Output	<b>F</b> 1 <b>1</b> 2 <b>1</b> 2		[0.51±0.10]	Į_į	[2.80]	Ĩ	$\mathbb{U}\mathbb{U}^{2}$	
Ground	_1.02±0.13 .040±.005	┘_ <b>=</b>     <del>-</del> _  -	.020±.004 [24.89] .980 *		.110 * MIN			
	[0.76±0.13] .030±.005 		[2.54]	30 40 50	3.81] .150 			
		5x [0.5	.100 51±0.13 20±.005 SQ.	[7.6	2] 0 NOTES:	Ŧ		

**Ordering Information** 



General Note

TT Electronics reserves the right to make changes in product specification without notice or liability. All information is subject to TT Electronics' own data and is considered accurate at time of going to print.

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#### Absolute Maximum Ratings (T<sub>A</sub> = 25° C unless otherwise noted)

-40° C to +100° C
-40° C to +100° C
16 kVDC
260° C
30 mA
3.0 A
3.0 V
100 mW
7 V
40 mW
18 V
25 mA

#### Electrical Characteristics (T<sub>A</sub> = 0° C to 70° C unless otherwise noted)

SYMBOL	PARAMETER	MIN	ТҮР	МАХ	UNITS	TEST CONDITIONS	
Input Diode							
V <sub>F</sub>	Forward Voltage	-	1.3	1.6	V	I <sub>F</sub> = 20 mA	
I <sub>R</sub>	Reverse Current	-	0.1	100	μA	V <sub>R</sub> = 2.0 V	
<b>Output IC</b> ( $V_{cc}$ = 4.5 V to 5.25 V) (See OPL550 for additional information—for reference only.)							
I <sub>ОН</sub>	High Level Output Current	-	0.20	10	μA	$I_F$ = 0.0 mA, $V_{OH}$ = 18.0 V, Vcc = 5.25 V	
V <sub>OL</sub>	Low Level Output Voltage	-	0.44	0.55	V	$I_{F} = 10.0 \text{ mA}, I_{OL} = 8.0 \text{ mA}, \text{Vcc} = 4.5 \text{ V}$	
I <sub>CCH</sub>	High Level Supply Current	-	4.2	7	m۸	I <sub>F</sub> = 0, Vcc = 5.25V	
I <sub>CCL</sub>	Low Level Supply Current	-	6.7	10	IIIA	I <sub>F</sub> = 10.0 mA, Vcc = 5.25 V	
Coupled Characteristics (V <sub>CC</sub> = 5 V)							
C <sub>IO</sub>	Coupling Capacitance	-	-	2	pF	Input and output leads shorted.	
t <sub>PLH</sub>	Propagation Delay to Low Output Level	-	-	200		Vcc=5V, $I_F$ =30mA, $R_L$ =560 $\Omega$	
t <sub>PHL</sub>	Propagation Delay to High Output Level	-	-	100	115		
I <sub>ISO</sub>	Isolation Leakage Current <sup>(5)</sup>	-	-	20	μΑ	VISO = 19.2kV dc	
I <sub>F</sub> +	LED Positive Going Threshold Current	0.8	1.7	5.0	mA	V <sub>CC</sub> = 5V, I <sub>OL</sub> = 8.0mA	

Notes:

(1) Derate LED linearly 1.33 mW/°C above 25°C.

(2) UL recognition is for 16kV dc for one minute.

(3) RMA flux is recommended. The duration can be extended to 10 seconds maximum when flow soldering.

(4) Derate linearly 0.54m W/°C above 25°C.

(5) Measured with input leads shorted together and output leads shorted together in air with a maximum relative humidity of 50%.

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### OPI1268



dV/dT





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### OPI1268

Issue	Change Description	Approval	Date
	Initial Release of OPI1266 (July 1996).		July 1996
А	OPI1268 (from PDF Catalog)		07/04
A.1	Combined OPI1266 and OPI1268 and put in new format. Inserted new package drawing.	Trevor Schelp	05/17/07
A.2	Added Note 3 on page 2	Bob Procsal	08/21/07
A.3	Updated Table on page 3	Trevor Schelp	10/01/07
A.4	Separated OPI1266 form the OPI1268	Trevor Schelp	11/27/07
A.5	Reversed tPHL & tPLH times, Removed reference to Input Diode	Trevor Schelp	05/07/2008
В	Change .01 $\mu$ F to 1.0 $\mu$ F on Figure 1. Change Peak Forward Current from 50 mA to 30 mA.	Trevor Schelp	11/11/10
С	Change "2 Mbd transfer rate" to 2MBit/s.	Trevor Schelp	11/23/10
D	Add the UL certification E58730	Cosmin Suciu	4/9/15
E	Page 2 Note 1: added "m" to 1.33m W Page 2 added Note 4: Derate linearly 0.54m W/°C above 25°C Removed (1) on "Input-to-Output Isolation Voltage"	Tom Osborne	
F	Deleted asterisk		
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