

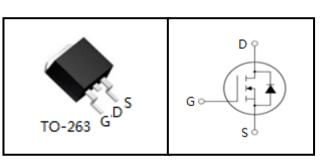
100V N-Channel Trench MOSFET

FEATURES

- High Density Cell Design for Ultra Low Rdson
- Fully Characterized Avalanche Voltage and Current
- Good Stability with High E_{AS}
- Excellent Package for Good Heat Dissipation

APPLICATIONS

- Power Switching Application
- Hard Switched and High Frequency Circuits
- Uninterruptible Power Supply





Device Marking and Package Information				
Device	Package Marking			
TMB140N10A	TO-263	140N10A		

Absolute Maximum Ratings $T_c = 25^{\circ}C$, unless otherwise noted					
Parameter		Symbol	Value	Unit	
Drain-Source Voltage (V _{GS} = 0V)		V _{DSS}	100	V	
Continuous Drain Current (Package Limited	l)	I _D	130	A	
Pulsed Drain Current	(note1)	I _{DM}	520	A	
Gate-Source Voltage		V _{GSS}	±20	V	
Single Pulse Avalanche Energy	(note2)	E _{AS}	1300	mJ	
Avalanche Current	(note1)	I _{AS}	70	A	
Power Dissipation (T _C = 25°C)		P _D	235	W	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55~+150	°C	

Thermal Resistance					
Parameter	Symbol	Value	Unit		
Thermal Resistance, Junction-to-Case	R _{thJC}	0.64	•C/W		
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62.5			



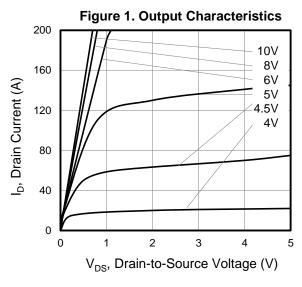
Specifications $T_J = 25^{\circ}C$, ur				Value			
Parameter	Symbol	Test Conditions	Value			Unit	
			Min.	Тур.	Max.		
Static		· · · · · ·					
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	100			V	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 100V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μΑ	
2010 Outo Voltago Dialin Ouriont		$V_{DS} = 100V, V_{GS} = 0V, T_{J} = 150^{\circ}C$			100		
Gate-Source Leakage	I _{GSS}	V_{GS} = $\pm 20V$			±100	nA	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2		4	V	
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	$V_{GS} = 10V, I_{D} = 30A$		6.0	7.0	mΩ	
Forward Transconductance (Note3)	g _{fs}	$V_{DS} = 5V, I_{D} = 20A$	50			S	
Dynamic							
Input Capacitance	C _{iss}	V _{GS} = 0V,		6320		pF	
Output Capacitance	C _{oss}	$V_{DS} = 25V,$		540			
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		359			
Total Gate Charge	Q _g			146		nC	
Gate-Source Charge	Q _{gs}	$V_{DD} = 50V, I_D = 20A, V_{GS} = 10V$		27			
Gate-Drain Charge	Q_{gd}			54			
Turn-on Delay Time	t _{d(on)}			31.5		ns	
Turn-on Rise Time	t _r	$V_{DD} = 50V, I_{D} = 2A,$		33			
Turn-off Delay Time	$t_{d(off)}$	$R_{G} = 2.5\Omega$		46			
Turn-off Fall Time	t _f			17.5			
Drain-Source Body Diode Characteris	stics			-			
Continuous Body Diode Current	I _s	T 05%0			140	A	
Pulsed Diode Forward Current	I _{SM}	T _C = 25°C			560		
Body Diode Voltage	V _{SD}	$T_{J} = 25^{o}C, I_{SD} = 20A, V_{GS} = 0V$			1.2	V	
Reverse Recovery Time	t _{rr}	I _F = 20A,		51		ns	
Reverse Recovery Charge	Q _{rr}	di _F /dt = 500A/µs		61		nC	

Notes

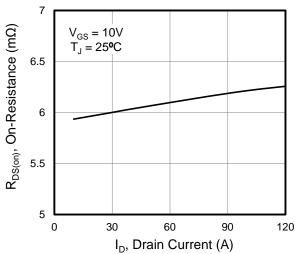
- 1. Repetitive Rating: Pulse Width limited by maximum junction temperature
- 2. I_{AS} = 70A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C
- 3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 1%



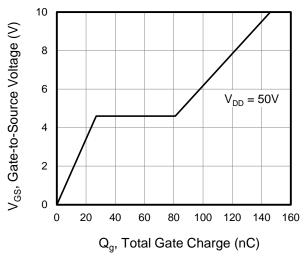
Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

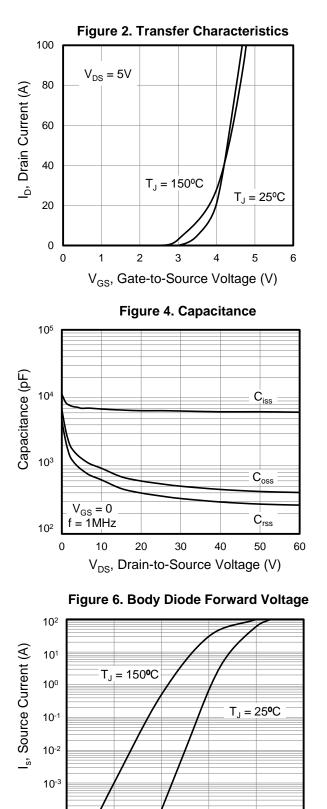












V_{SD}, Source-to-Drain Voltage (V)

0.6

0.4

1

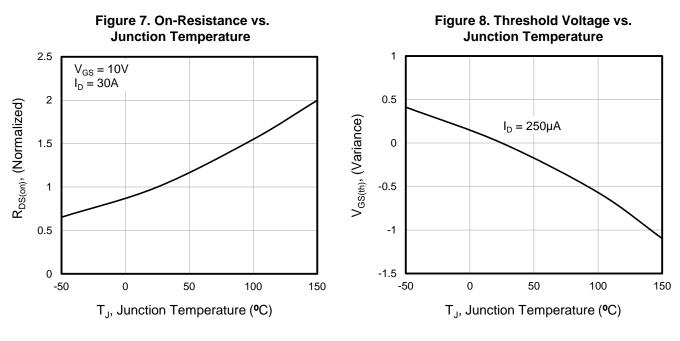
0.8

10-4

0

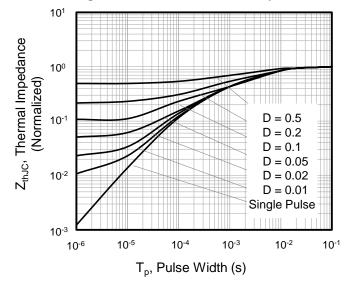
0.2

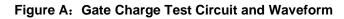




Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

Figure 9. Transient Thermal Impedance





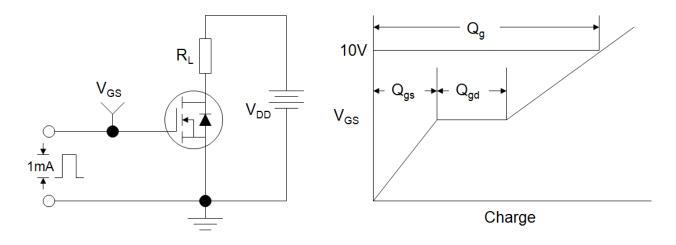


Figure B: Resistive Switching Test Circuit and Waveform

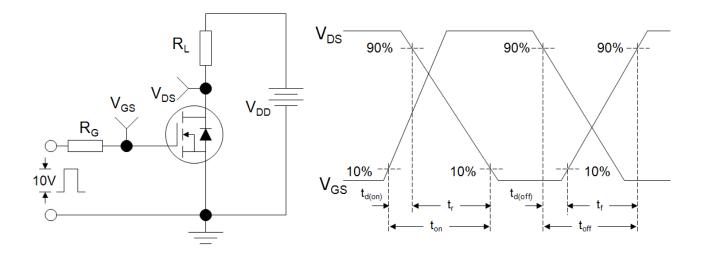
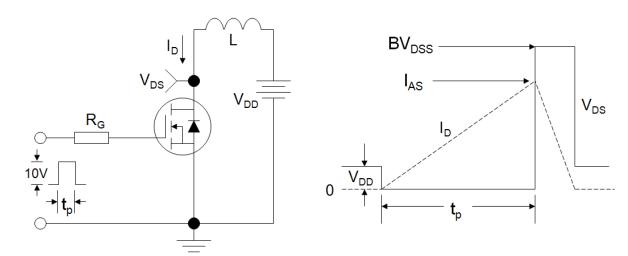
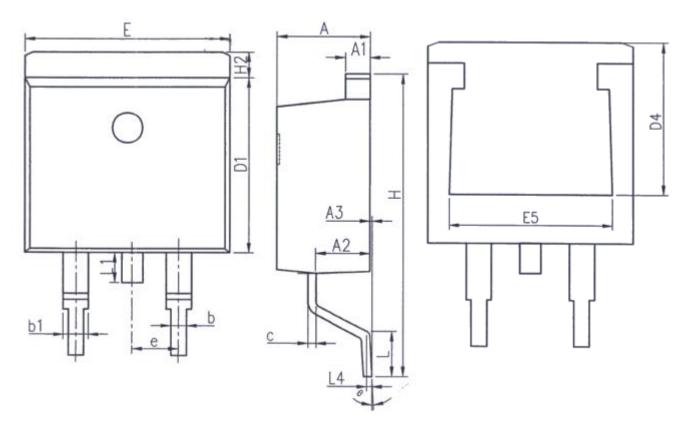


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





TO-263



	Unit: mm		l	Unit: mm	n
Symbol	Min.	Max.	Symbol	Min.	Max.
Α	4. 37	4. 77	E	9.86	10.36
A1	1.22	1.42	E5	7.06	-
A2	2.49	2.89	e	2.54BSC	
A3	0.00	0. 25	Н	14. 70	15.50
b	0.70	0.96	H2	1.07	1.47
b1	1.17	1.47	L	2.00	2.60
с	0.30	0.53	L1	1.40	1.70
D1	8.50	8.90	L4	0. 25BSC	
D4	6. 60	-	θ	0°	9 °



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