

650V N-Channel MOSFET

Description

650V N-Channel MOSFET

VDMOSFET is a double-diffusion device which the current flows is vertically, and is a voltage-controlled device. Under the control of the appropriate gate voltage, the semiconductor surface is inverted, forming a conductive channel and an appropriate amount of current flows between Drain and Source. Compared with bipolar transistor, its switching speed and switching loss are small. High input impedance, low driving power, good frequency characteristics, In particular, it has a negative temperature coefficient.

Features

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

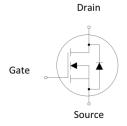
Applications

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction(PFC)

TO-220F









Device Marking and Package Information

Device	Package	Marking
TMA20N65HG	TO-220F	A20N65HG
TMW20N65HG	TO-247	W20N65HG



Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted				
Parameter	Symbol	Values	Unit	
Drain-Source Voltage (V _{GS} = 0V)	V_{DSS}	650	V	
Continuous Drain Current	I _D	20	Α	
Pulsed Drain Current (note1)	I _{DM}	80	А	
Gate-Source Voltage	V_{GSS}	±30	V	
Single Pulse Avalanche Energy (note2)	E _{AS}	605	mJ	
Avalanche Current (note1)	I _{AR}	11	Α	
Repetitive Avalanche Energy (note1)	E _{AR}	363	mJ	
Power Dissipation For TO-220F		120	· W	
Power Dissipation For TO-247	P_{D}	120		
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150	°C	

Thermal Resistance For TO-220F			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R _{thJC}	1.04	°C/W
Thermal Resistance, Junction-to-Ambient	R_{thJA}	62.5	30/00

Thermal Resistance For TO-247			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R _{thJC}	1.04	°C/W
Thermal Resistance, Junction-to-Ambient	R _{thJA}	50	30/00

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			Value				
Parameter	Symbol	Symbol Test Conditions		Тур.	Max.	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = 250\mu A$	650			V	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 650V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μΑ	
Gate-Source Leakage Current	I _{GSS}	$V_{GS} = \pm 30V$			±100	nA	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	V	
Drain-SourceOn-State-Resistance (Note3)	R _{DS(on)}	V _{GS} = 10V, I _D = 10A		0.35	0.4	Ω	
Dynamic Characteristics	•		•	•			
Input Capacitance	C _{iss}	V 0V		2701			
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 25V,$		249.5		pF	
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		24.8			
Total Gate Charge	Q_g	V 520V		80			
Gate-Source Charge	Q_{gs}	$V_{DD} = 520V,$ $I_{D} = 20A,$		12		nC	
Gate-Drain Charge	Q_{gd}	V _{GS} = 10V		34			
Turn-on Delay Time	t _{d(on)}			54			
Turn-on Rise Time	t _r	$V_{DD} = 325V,$		48.4			
Turn-off Delay Time	t _{d(off)}	$I_D = 20A$, $R_G = 25 \Omega$		301		ns	
Turn-off Fall Time	t _f			85			
Drain-Source Body Diode Characte	ristics		•	-	-		
Continuous Body Diode Current	Is	T 0500			20	^	
Pulsed Diode Forward Current	I _{SM}	T _C = 25 °C			80	Α	
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}C$, $I_{SD} = 10A$, $V_{GS} = 0V$			1.4	V	
Reverse Recovery Time	t _{rr}	$V_{GS} = 0V, I_{S} = 20A,$		741		ns	
Reverse Recovery Charge	Q _{rr}	di _F /dt =100A/μs		4.71		μC	

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}$ C
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%



Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

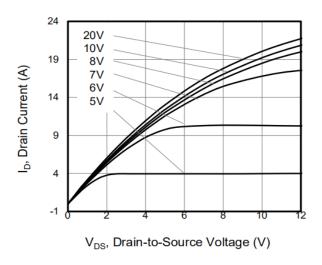


Figure 2. Body Diode Forward Voltage

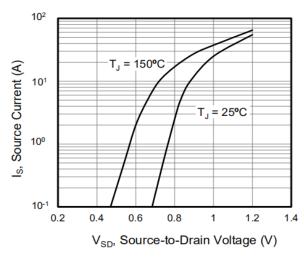


Figure 3. Drain Current vs. Temperature

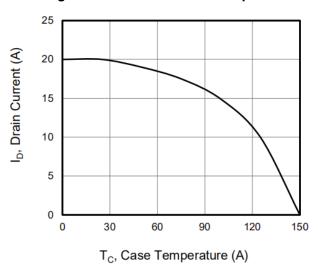


Figure 4. BV_{DSS} Variation vs. Temperature

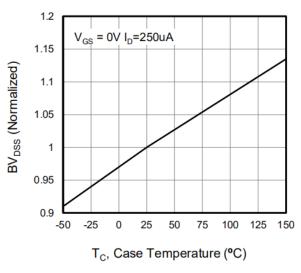


Figure 5. Transfer Characteristics

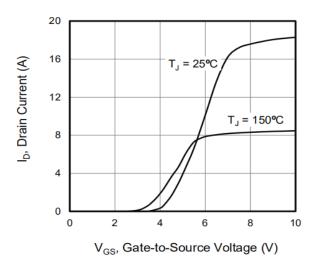
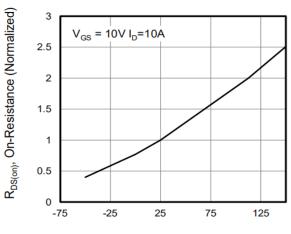


Figure 6. On-Resistance vs. Temperature



T_J, Junction Temperature (°C)

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Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

Figure 7. Capacitance

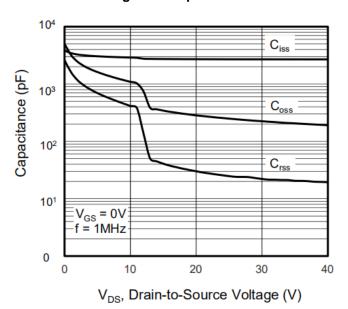


Figure 8. Gate Charge

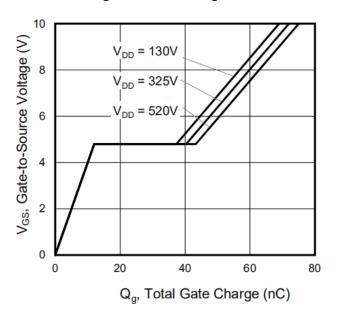




Figure A: Gate Charge Test Circuit and Waveform

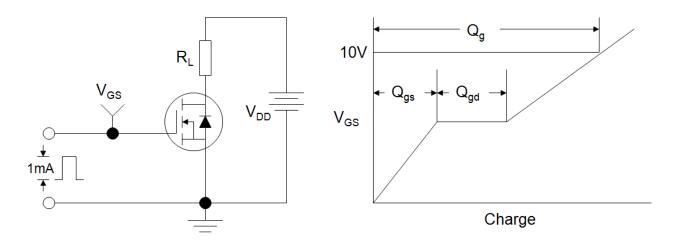


Figure B: Resistive Switching Test Circuit and Waveform

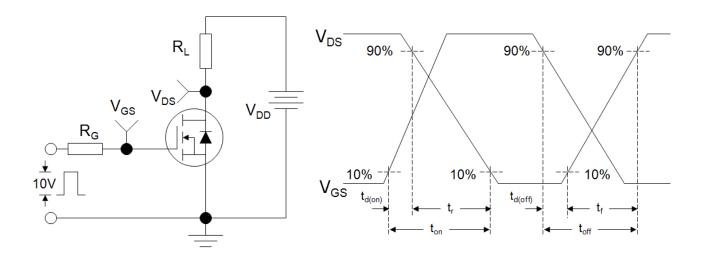
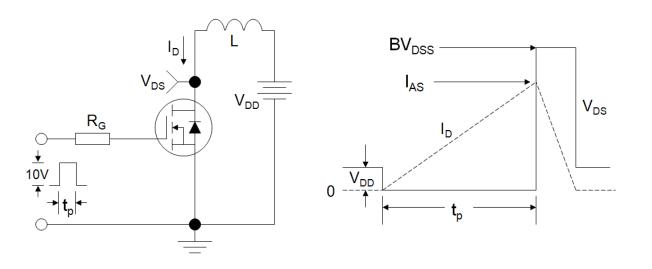


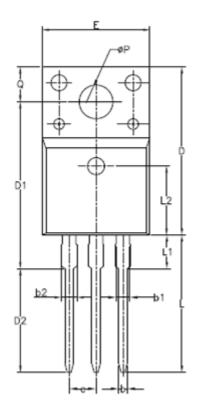
Figure C: Unclamped Inductive Switching Test Circuit and Waveform

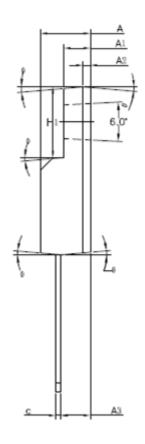


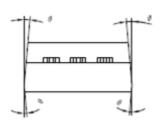
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TO-220F



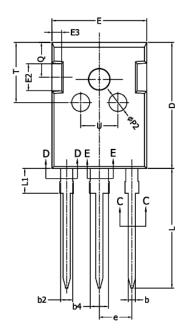


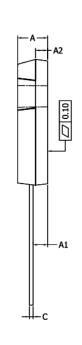


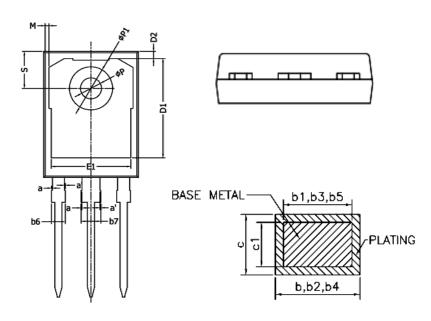
SYMBOL	MIN	NOM	MAX
Α	4.50	4.70	4.83
A1	2.34	2.54	2.74
A2		0.70 R	EF
A3	2.56	2.76	2.93
b	0.70	-	0.90
b1	1.18	_	1.38
b2	_	_	1.47
С	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.55	15.75	15.95
D2	9.60	9.80	10.0
Ε	9.96	10.16	10.36
е	2	2.54BSC	
H1	6.48	6.68	6.88
L	12.68	12.98	13.28
L1	_	_	3.50
L2	6.50REF		
ØΡ	3.08	3.18	3.28
Q	3.20	_	3.40
θ 1	1*	3*	5*



TO-247







SYMBOL	MIN	NOM	MAX
Α	4.90	5.00	5.10
A1	2.31	2.41	2.51
A2	1.90	2.00	2.10
a	0		0.15
a'	0		0.15
b	1.16		1.26
b1	1.15	1.2	1.22
b2	1.96		2.06
b3	1.95	2.00	2.02
b4	2.96		3.06
b5	2.96	3.00	3.02
b6			2.25
b7			3.25
С	0.59		0.66
c1	0.58	0.60	0.62
D	20.90	21.00	21.10
D1	16.25	16.55	16.85
D2	1.05	1.17	1.35
E	15.70	15.80	15.90
E1	13.10	13.30	13.50
E2	4.40	4.50	4.60
E3	2.40	2.50	2.60
е		5.436 BSC	
L	19.80	19.92	20.10
L1			4.30
М	0.35		0.95
Р	3.40	3.50	3.60
P1	7.00		7.40
P2	2.40	2.50	2.60
Q	5.60		6.00
S	6.05	6.15	6.25
Т	9.80		10.20
U	6.00		6.40



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