

1200V Super-Junction Power MOSFET

DESCRIPTION

1200V super-junction Power MOSFET

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle. The SJ MOSFET is a price-performance optimized product enabling to target cost sensitive applications in Consumer and Lighting markets, designed by Wuxi Unigroup Microelectronics Company.

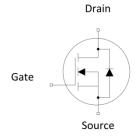
FEATURES

- Very low FOM $R_{DS(on)} \times Q_g$
- 100% avalanche tested
- RoHS compliant

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)







Device Marking and Package Information

Device	Package	Marking
TPA120R800A	TO-220F	120R800A

Key Performance Parameters

Parameter	Value	Unit
V _{DS} @ T _{j,max}	1200	V
R _{DS(on),max}	0.8	Ω
I _D	12	A
$Q_{g,typ}$	60	nC
I _{DM}	36	A



Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted					
Parameter		Symbol	Value	Unit	
Drain-Source Voltage (V _{GS} = 0V)		V _{DSS}	1200	V	
Continuous Drain Current	$T_{\rm C} = 25^{\rm o}{\rm C}$	I _D	12	A	
Continuodo Brain Carront	$T_{\rm C} = 100^{\rm o}{\rm C}$	טי	7.2] ^`	
Pulsed Drain Current (note1)		I _{DM}	36	А	
Gate-Source Voltage		V _{GSS}	±30	V	
Single Pulse Avalanche Energy (note2)		E _{AS}	180	mJ	
Avalanche Current		I _{AS}	6	A	
Power Dissipation		P _D	34	W	
Continuous Body Diode Current		I _S	12	A	
Pulsed Diode Forward Current (note1)		I _{SM}	36	7 ^	
MOSFET dv/dt ruggedness, V _{DS}	= 0960V	dv/dt	50	V/ns	
Reverse diode dv/dt, V _{DS} = 096	60V, I _{SD} ≤ I _D	dv/dt	5	A/us	
Operating Junction and Storage	Temperature Range	T _J , T _{stg}	-55~+150	∘C	

Thermal Resistance				
Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	R _{thJC}	3.67	00.444	
Thermal Resistance, Junction-to-Ambient	R _{thJA}	80	°C/W	



Specifications $T_J = 25^{\circ}$ C, unless otherwise noted								
Donomotor		-	Value					
Parameter	Symbol Test Conditions		Min.	Тур.	Max.	Unit		
Static								
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = 250\mu A$	1200			V		
	I _{DSS}	$V_{DS} = 1200V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1			
Zero Gate Voltage Drain Current		$V_{DS} = 1200V, V_{GS} = 0V, T_{J} = 150^{\circ}C$			100	μΑ		
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 30V$			±100	nA		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5		4.5	V		
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 3A		0.62	0.8	Ω		
Forward Transconductance (Note3)	g _{fs}	$V_{DS} = 10V, I_{D} = 4A$		10		S		
Dynamic				•				
Input Capacitance	C _{iss}	$V_{GS} = 0V$,		2573		pF		
Output Capacitance	C _{oss}	V _{DS} = 100V,		66				
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		2.3				
Total Gate Charge	Q_g			60		nC		
Gate-Source Charge	Q_{gs}	$V_{DD} = 400V, I_{D} = 4A, V_{GS} = 10V$		14				
Gate-Drain Charge	Q_{gd}	55		22				
Turn-on Delay Time	t _{d(on)}			51				
Turn-on Rise Time	t _r	$V_{DD} = 400V, I_{D} = 4A,$		71		ns		
Turn-off Delay Time	t _{d(off)}	$R_G = 25\Omega$		154				
Turn-off Fall Time	t _f			67				
Drain-Source Body Diode Characteri	stics							
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}C$, $I_{SD} = 4A$, $V_{GS} = 0V$		0.9	1.2	V		
Reverse Recovery Time	t _{rr}			675		ns		
Reverse Recovery Charge	Q _{rr}	$V_R = 100V, I_F = I_S,$ $di_F/dt = 100A/\mu s$		9		μC		
Peak Reverse Recovery Current	I _{rrm}			25		Α		

Notes

- 1. Repetitive Rating: Pulse Width limited by maximum junction temperature
- 2. V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}$ C
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 1%



Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

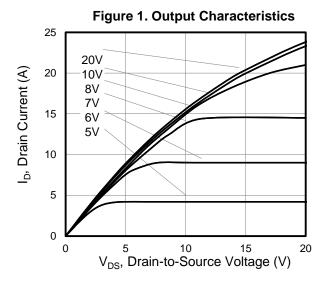


Figure 3. On-Resistance vs. Drain Current

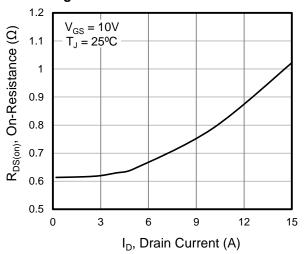


Figure 5. Gate Charge

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Figure 2. Transfer Characteristics 18 $V_{DS} = 10V$ 16 $T_{J} = 25^{\circ}C$ I_D, Drain Current (A) 14 12 10 8 6 $T_{J} = 150^{\circ}C$ 4 2 0 0 6 10 V_{GS}, Gate-to-Source Voltage (V)

Figure 4. Capacitance

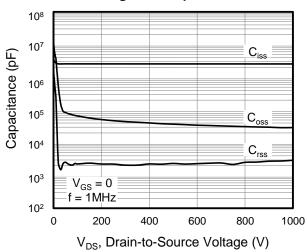
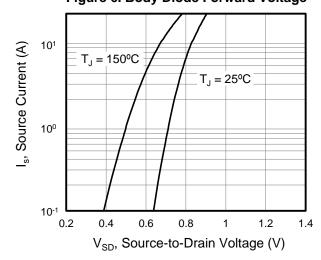


Figure 6. Body Diode Forward Voltage





Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

Figure 7. On-Resistance vs. **Junction Temperature** 3 2.5 R_{DS(on)}, (Normalized) 2 $V_{GS} = 10V$ $I_D = 3A$ 1.5 1 0.5 0 0 50 100 -100 -50 150 200 T_J, Junction Temperature (°C)

Figure 9. Transient Thermal Impedance

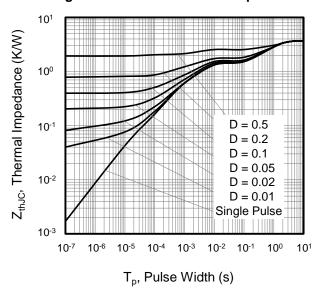


Figure 8. Breakdown voltage vs. **Junction Temperature**

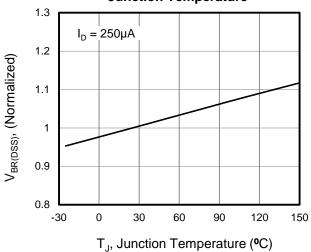
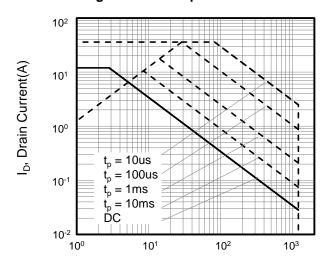


Figure 10. Safe operation area for



V_{DS}, Drain-Source Voltage(V)



Figure A: Gate Charge Test Circuit and Waveform

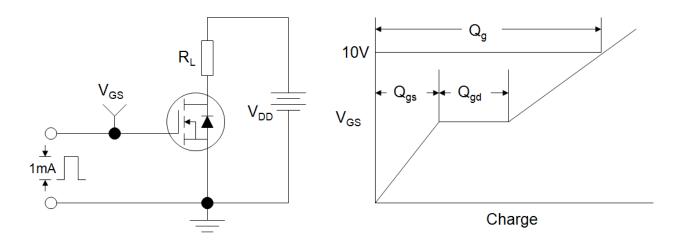


Figure B: Resistive Switching Test Circuit and Waveform

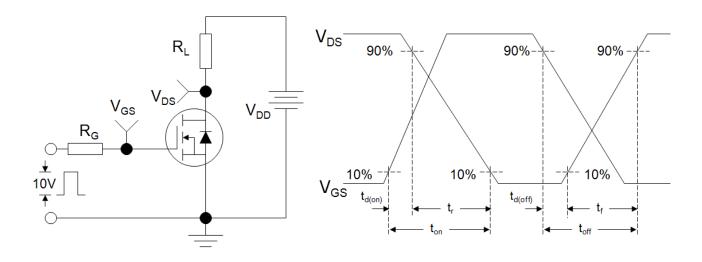
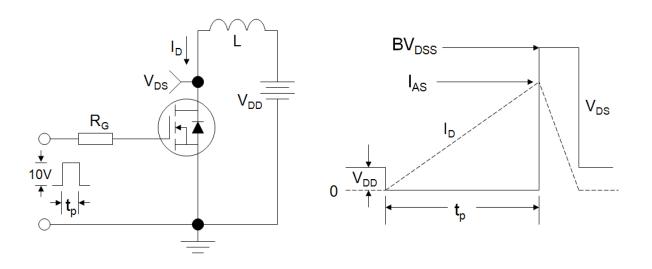
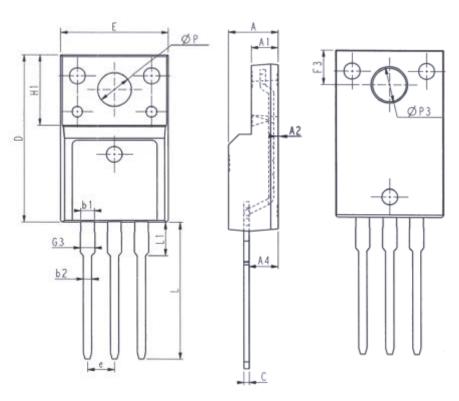


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





TO-220F



Unit: mm			Unit: mm		
Symbol	Min.	Max.	Symbol	Min.	Max.
E	9.96	10.36	L	12. 68	13. 28
Α	4. 50	4. 90	L1	2. 93	3. 13
A1	2. 34	2. 74	Р	3. 03	3. 38
A2	0.30	0.60	P3	3. 15	3. 65
A4	2. 56	2. 96	F3	3. 15	3. 45
С	0.40	0. 65	G3	1. 25	1. 55
D	15. 57	16. 17	b1	1. 18	1. 43
H1	6. 70REF		b2	0. 70	0. 95
e	2. 54BSC				



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