

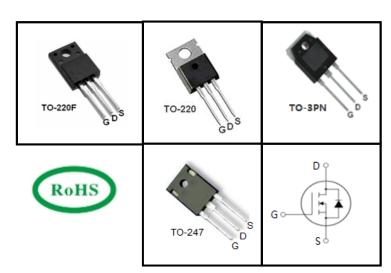
650V Super-Junction Power MOSFET

FEATURES

- $\bullet \quad \text{Very low FOM R}_{\text{DS(on)}} \times \text{Q}_{\text{g}} \\$
- 100% avalanche tested
- RoHS compliant

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



Device Marking and Package Information					
Device	TPA65R170M	TPP65R170M	TPV65R170M	TPW65R170M	
Package	TO-220F	TO-220	TO-3PN	TO-247	
Marking	65R170M	65R170M	65R170M	65R170M	

Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted						
Barrantar	Cumahad	Value			11.24	
Parameter	Symbol	TO-220	TO-3PN	TO-247	TO-220F	Unit
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	650 V			٧	
Continuous Drain Current	I _D	20 A			А	
Pulsed Drain Current (note1)	I _{DM}	60 A			Α	
Gate-Source Voltage	V _{GSS}	±30		V		
Single Pulse Avalanche Energy (note2)	E _{AS}	484 m		mJ		
Avalanche Current (note1)	I _{AR}	3.5 A		Α		
Repetitive Avalanche Energy (note1)	E _{AR}	0.7 mJ		mJ		
Power Dissipation (T _C = 25°C)	P _D	, 151 34 V			W	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150 °C			°C	

Thermal Resistance						
Parameter	Cumbal	Value				1114
Parameter	Symbol	TO-220	TO-3PN	TO-247	TO-220F	Unit
Thermal Resistance, Junction-to-Case	R _{thJC}	0.83 3.7		°C/W		
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62 80		°C/VV		

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TPA65R170M, TPP65R170M, TPV65R170M, TPW65R170M

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, and the second			Value			
Parameter	Symbol Test Conditions		Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_{D} = 250\mu A$	650			V
Zeus Cota Valta de Dusia Cumant		I_{DSS} $V_{DS} = 650V, V_{GS} = 0V, T_{J} = 25^{\circ}C$ $V_{DS} = 650V, V_{GS} = 0V, T_{J} = 150^{\circ}C$			1	μΑ
Zero Gate Voltage Drain Current	IDSS				100	
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 30V$			±100	nA
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5		4.5	V
Drain-Source On-Resistance (Note3)	R _{DS(on)}	V _{GS} = 10V, I _D = 10A		0.15	0.17	Ω
Forward Transconductance (Note3)	g _{fs}	V _{DS} = 10V, I _D = 10A		23		S
Dynamic						
Input Capacitance	C _{iss}	$V_{GS} = 0V,$ $V_{DS} = 50V,$		1840		pF
Output Capacitance	C _{oss}			197		
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		8		
Total Gate Charge	Q_g			54		nC
Gate-Source Charge	Q_{gs}	$V_{DD} = 520V, I_{D} = 20A,$ $V_{GS} = 10V$		12		
Gate-Drain Charge	Q_{gd}			18		
Turn-on Delay Time	t _{d(on)}			66		
Turn-on Rise Time	t _r	$V_{DD} = 400V, I_{D} = 20A,$		27		ns
Turn-off Delay Time	t _{d(off)}	$R_G = 25\Omega$		167		
Turn-off Fall Time	t _f			21		
Drain-Source Body Diode Characteris	stics					
Continuous Body Diode Current	I _s	T 0500			20	Δ.
Pulsed Diode Forward Current	I _{SM}	$T_C = 25^{\circ}C$			60	Α
Body Diode Voltage	V_{SD}	$T_J = 25^{\circ}C$, $I_{SD} = 20A$, $V_{GS} = 0V$		0.9	1.2	V
Reverse Recovery Time	t _{rr}			336		ns
Reverse Recovery Charge	Q _{rr}	$V_R = 480V, I_F = I_S,$ $di_F/dt = 100A/\mu s$		3.4		μC
Peak Reverse Recovery Current	I _{rrm}	3. _F , 3. 100, 4 po		20		А

Notes

- 1. Repetitive Rating: Pulse Width limited by maximum junction temperature
- 2. I_{AS} = 3.5A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 1%

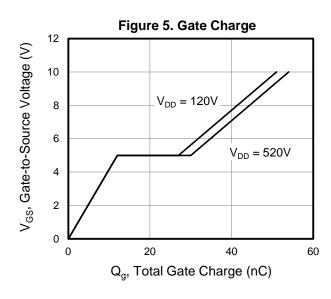


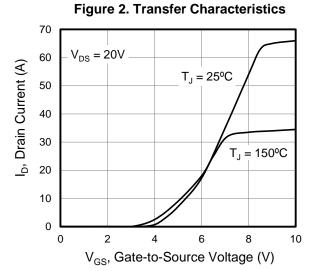
Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

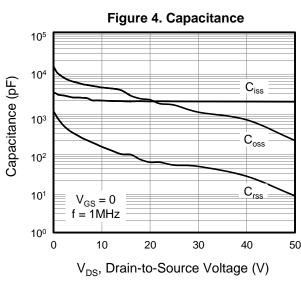
Figure 1. Output Characteristics 80 20V 70 10V 8V 60 7V 6V 50 5.5V 40 5ν 30 20

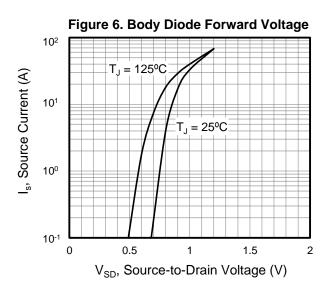
I_D, Drain Current (A) 10 0 5 10 15 20 0 V_{DS}, Drain-to-Source Voltage (V)

Figure 3. On-Resistance vs. Drain Current 0.19 $V_{GS} = 10V$ R_{DS(on)}, On-Resistance (Ω) 0.18 $T_J = 25^{\circ}C$ 0.17 0.16 0.15 0.14 0.13 0.12 0 5 10 15 20 I_D, Drain Current (A)











Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

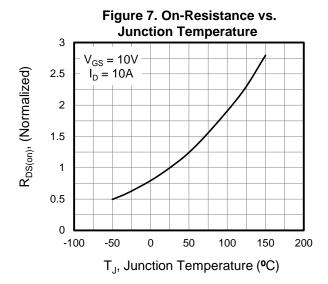


Figure 9. Transient Thermal Impedance TO-220/TO-3PN/TO-247

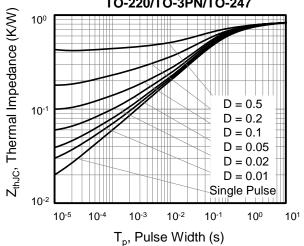


Figure 8. Threshold Voltage vs. Junction Temperature

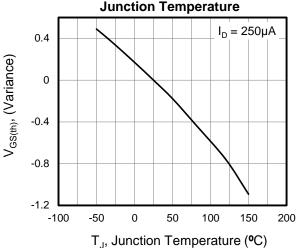


Figure 10. Transient Thermal Impedance TO-220F

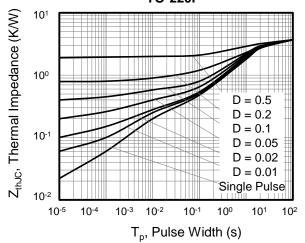




Figure A: Gate Charge Test Circuit and Waveform

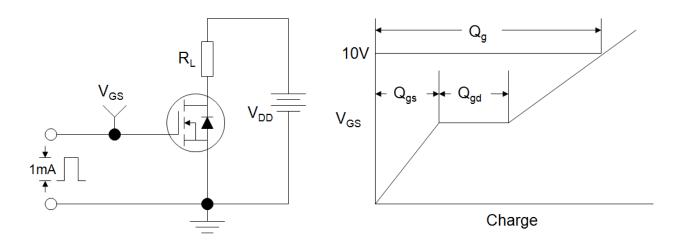


Figure B: Resistive Switching Test Circuit and Waveform

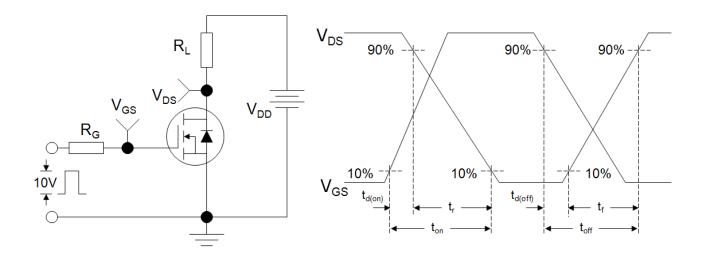
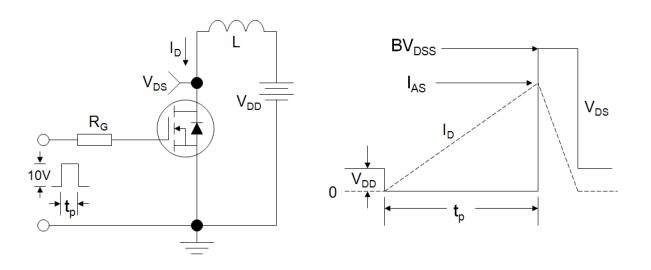


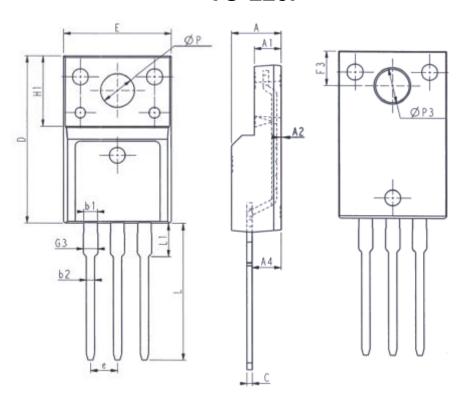
Figure C: Unclamped Inductive Switching Test Circuit and Waveform



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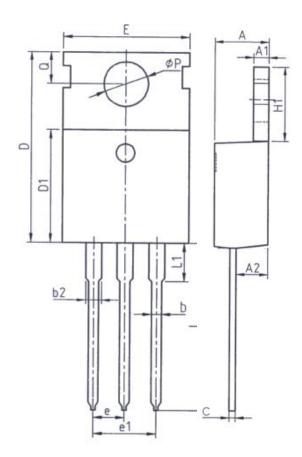


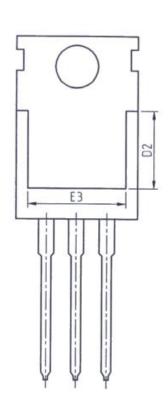




l	Unit: mm Unit			Jnit: mn	1
Symbol	Min.	Max.	Symbol	Min.	Max.
E	9.96	10.36	L	12. 68	13. 28
Α	4. 50	4. 90	L1	2. 93	3. 13
A 1	2. 34	2. 74	Р	3. 03	3. 38
A2	0.30	0.60	P3	3. 15	3. 65
A4	2. 56	2. 96	F3	3. 15	3. 45
С	0.40	0. 65	G3	1. 25	1. 55
D	15. 57	16. 17	b1	1. 18	1. 43
H1	6. 70	OREF	b2	0. 70	0. 95
e	2. 54	4BSC			

TO-220



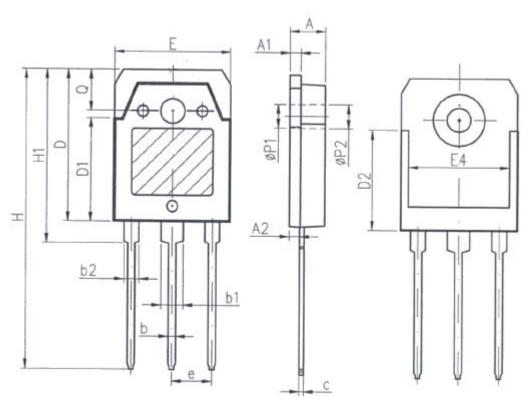


Unit: mm			
Symbol	Min.	Max.	
Α	4. 37	4. 77	
A1	1. 25	1. 45	
A2	2. 20	2. 60	
ь	0. 70	0. 95	
b2	1. 17	1. 47	
С	0. 40	0. 65	
D	15. 10	16. 10	
D1	8. 80	9. 40	
D2	5. 50	_	

Unit: mm			
Symbol	Min.	Max.	
E	9. 70	10. 30	
E3	7. 00	-	
е	2. 54BSC		
e1	5. 08	BBSC	
H1	6. 25	6. 85	
L	12. 75	13.80	
L1	-	3. 40	
P	3. 40	3. 80	
Q	2. 60	3. 00	



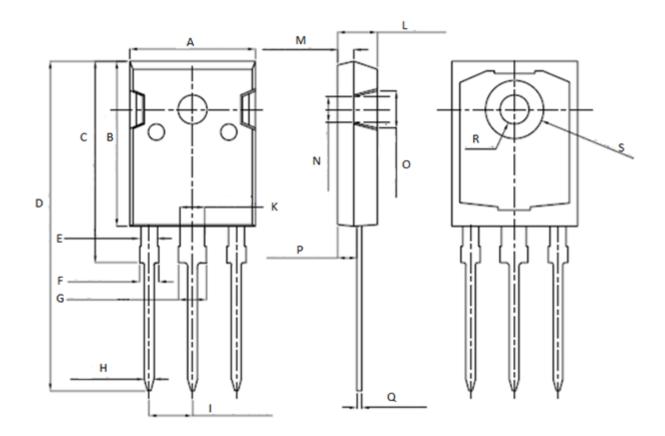




Unit:mm			
Symbol	Min.	Max.	
Α	4. 6	5	
A1	1. 4	1. 65	
A2	1. 18	1. 58	
b	0.8	1. 2	
b1	2. 8	3. 2	
b2	1.8	2. 2	
С	0. 5	0. 75	
D	19. 6	20. 2	
D1	13. 55	14. 25	
D2	12. 9	PREF	
E	15. 35	15. 85	
E4	12. 6	-	
е	5. 45TYP		
Н	40. 1	40. 9	
H1	23. 15	23. 65	
P1	3. 2REF		
P2	3. 5REF		



TO-247



Unit: mm				
Symbol	Min.	Max.		
Α	15. 95	16. 25		
В	20. 85	21. 25		
С	20. 95	21. 35		
D	40.5	40. 9		
E	1. 9	2. 1		
F	2. 1	2. 25		
G	3. 1	3. 25		
Н	1.1	1. 3		
I	5. 40	5. 50		

Unit: mm			
Symbol	Min.	Max.	
K	2. 90	3. 10	
L	4. 90	5. 30	
M	1. 90	2. 10	
N	4. 50	4. 70	
0	5. 40	5. 60	
Р	2. 29	2. 49	
Q	0. 51	0. 71	
R	ф 3. 5	ф 3. 7	
S	ф 7. 1	ф 7. 3	

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