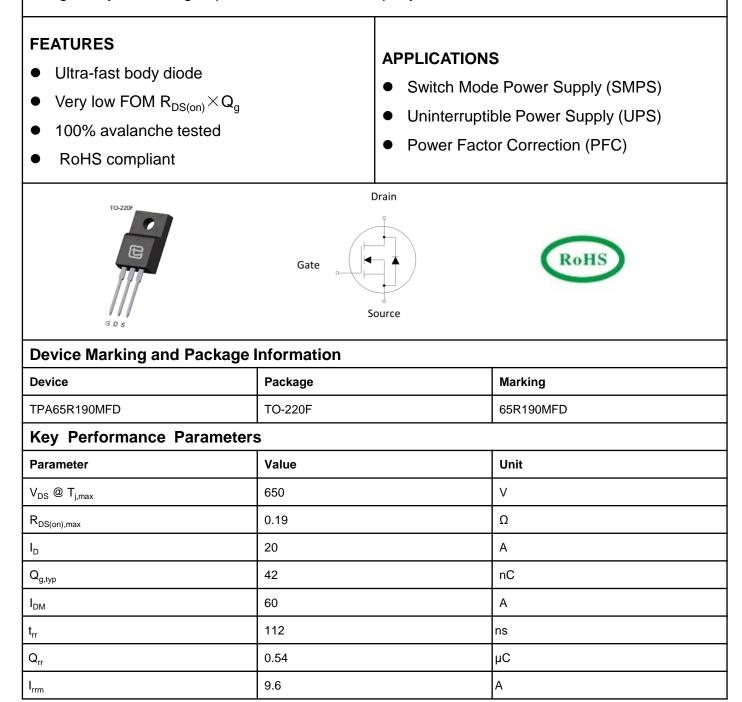


650V Super-Junction Power MOSFET

650V super-junction Power MOSFET

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle. The SJ MOSFET is a price-performance optimized product enabling to target cost sensitive applications in Consumer and Lighting markets, designed by Wuxi Unigroup Microelectronics Company.





| Absolute Maximum Ra | tings $T_c = 25^{\circ}C$, | unless oth | erwise noted | |
|--|-----------------------------|-----------------------------------|--------------|------|
| Parameter | | Symbol V _{DSS} | Value | Unit |
| Drain-Source Voltage ($V_{GS} = 0V$ |) | | 650 | V |
| Continuous Drain Current | T _C = 25°C | l _D | 20 | А |
| Continuous Drain Current | TC = 100°C | | 12 | |
| Pulsed Drain Current (note1) | | I _{DM} | 60 | A |
| Gate-Source Voltage | | V _{GSS} | ±30 | V |
| Single Pulse Avalanche Energy (note2) | | E _{AS} | 484 | mJ |
| Repetitive Avalanche Energy (note2) | | E _{AR} | 0.7 | mJ |
| Avalanche Current | | I _{AR} | 3.5 | A |
| MOSFET dv/dt ruggedness, V _{DS} | = 0480V | dv/dt | 50 | V/ns |
| Power Dissipation | | P _D | 34 | W |
| Continuous Body Diode Current | | ۱ _s | 17 | |
| Pulsed Diode Forward Current (note1) | | I _{SM} | 60 | A |
| Reverse diode dv/dt | (note3) | dv/dt | 50 | V/ns |
| Maximum diode commutation speed (note3) | | di _f /dt | 900 | A/us |
| Operating Junction and Storage | Temperature Range | T _J , T _{stg} | -55~+150 | °C |

| Thermal Resistance | | | | |
|---|-------------------|-------|-------|--|
| Parameter | Symbol | Value | Unit | |
| Thermal Resistance, Junction-to-Case | R _{thJC} | 3.7 | 00000 | |
| Thermal Resistance, Junction-to-Ambient | R _{thJA} | 80 | °C/W | |



| Deremeter | Cumula al | T (0, "" | Value | | | | |
|----------------------------------|----------------------|---|-------|------|------|------|--|
| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit | |
| Static | - | | | | | | |
| Drain-Source Breakdown Voltage | V _{(BR)DSS} | $V_{GS} = 0V, I_{D} = 250\mu A$ | 650 | | | V | |
| Zara Cata Valtaga Drain Currant | | $V_{DS} = 650V, V_{GS} = 0V, T_{J} = 25^{\circ}C$ | | | 5 | | |
| Zero Gate Voltage Drain Current | I _{DSS} | $V_{DS} = 650V, V_{GS} = 0V, T_{J} = 150^{\circ}C$ | | | 2500 | μA | |
| Gate-Source Leakage | I _{GSS} | $V_{GS} = \pm 30 V$ | | | ±100 | nA | |
| Gate-Source Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_D = 250 \mu A$ | 3 | | 5 | V | |
| Drain-Source On-Resistance | R _{DS(on)} | V _{GS} = 10V, I _D = 10A | | 0.17 | 0.19 | Ω | |
| Gate resistance | R _G | f = 1.0MHz open drain | | 12 | | Ω | |
| Dynamic | • | | | | | | |
| Input Capacitance | C _{iss} | | | 1834 | | pF | |
| Output Capacitance | C _{oss} | $V_{GS} = 0V,$ $V_{DS} = 100V,$ | | 57 | | | |
| Reverse Transfer Capacitance | C _{rss} | f = 1.0MHz | | 1.7 | | | |
| Total Gate Charge | Qg | | | 42 | | | |
| Gate-Source Charge | Q _{gs} | $V_{DD} = 520V, I_D = 20A, V_{GS} = 10V$ | | 10 | | nC | |
| Gate-Drain Charge | Q _{gd} | | | 17 | | | |
| Turn-on Delay Time | t _{d(on)} | | | 34 | | | |
| Turn-on Rise Time | t _r | V _{DD} = 400V, I _D = 20A, | | 72 | | ns | |
| Turn-off Delay Time | t _{d(off)} | $R_{\rm G} = 25\Omega$ | | 114 | | | |
| Turn-off Fall Time | t _f | | | 41 | | | |
| Drain-Source Body Diode Characte | eristics | | | | | | |
| Body Diode Voltage | V _{SD} | T _J = 25°C, I _{SD} = 10A, V _{GS} = 0V | | 0.9 | 1.2 | V | |
| Reverse Recovery Time | t _{rr} | | | 112 | | ns | |
| Reverse Recovery Charge | Q _{rr} | V _R = 400V, I _F = I _S , di _c /dt = 100A/µs | | 0.54 | | μC | |
| Peak Reverse Recovery Current | I _{rrm} | | | 9.6 | | А | |

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. $I_{AS} = 3.5A, V_{DD} = 50V, R_G = 25\Omega$, Starting $T_J = 25^{\circ}C$
- 3. Identical low side and high side switch with identical ${\rm R}_{\rm G}$



T_J = 150°C

10

8

C_{iss}

 C_{oss}

C

500

600

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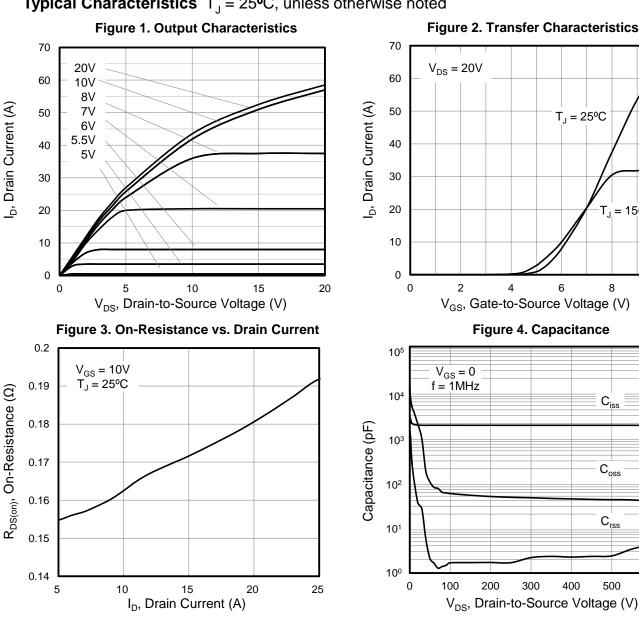


Figure 5. Gate Charge

V_{DD} = 120V

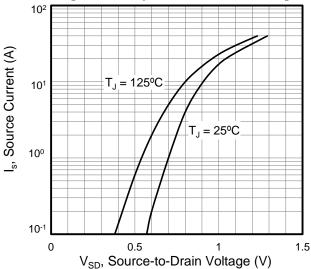
30

V_{DD} = 520V

40

Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted





V_{GS}, Gate-to-Source Voltage (V)

12

10

8

6

4

2

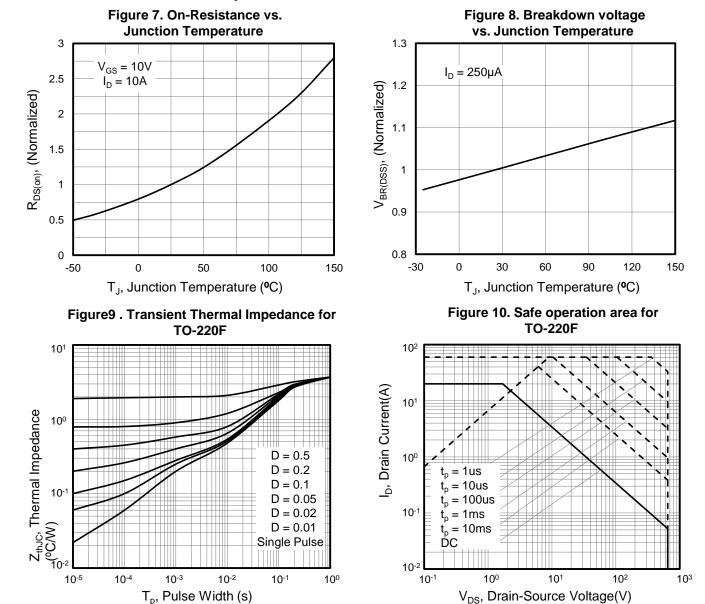
0 0

10

20

Q_g, Total Gate Charge (nC)





Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted



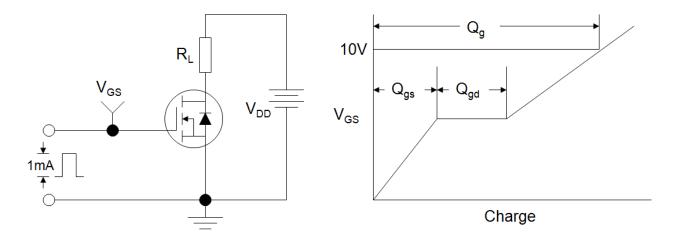


Figure B: Resistive Switching Test Circuit and Waveform

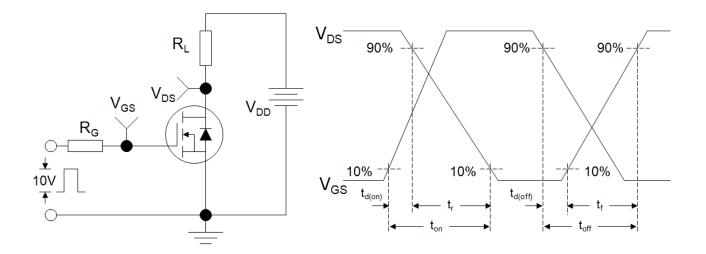
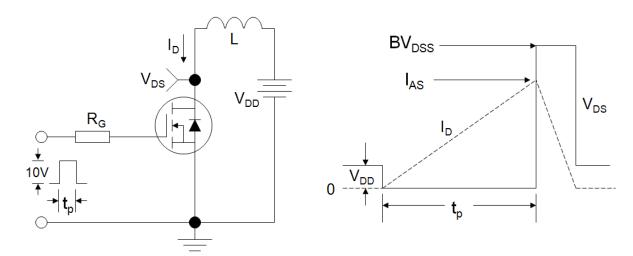
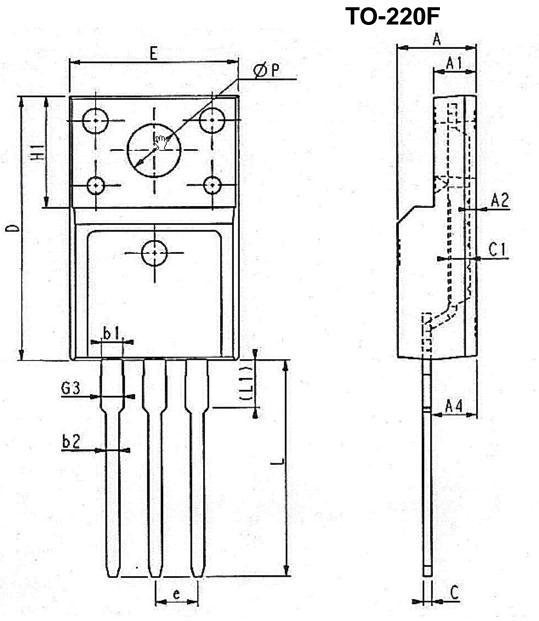
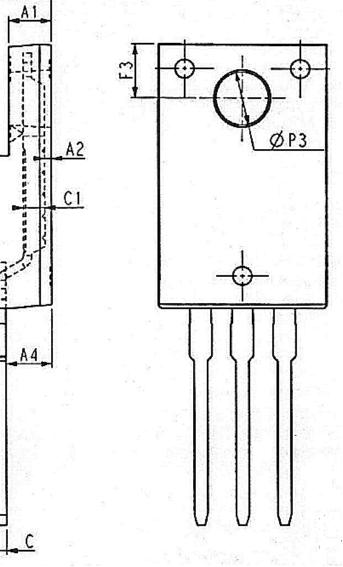


Figure C: Unclamped Inductive Switching Test Circuit and Waveform









| Unit:mm | | | Unit:mm | | | | |
|---------|-------|---------|---------|--------|---------|-------|-------|
| Symbol | Min. | Nom | Max. | Symbol | Min. | Nom | Max. |
| E | 9.96 | 10.16 | 10.36 | е | 2.54BSC | | |
| А | 4.50 | 4.70 | 4.90 | L | 12.68 | 12.98 | 13.28 |
| A1 | 2.34 | 2.54 | 2.74 | L1 | 2.88 | 3.03 | 3.18 |
| A2 | 0.30 | 0.45 | 0.60 | ΦΡ | 3.03 | 3.18 | 3.38 |
| A4 | 2.56 | 2.76 | 2.96 | ΦΡ3 | 3.15 | 3.45 | 3.65 |
| с | 0.40 | 0.50 | 0.65 | F3 | 3.15 | 3.30 | 3.45 |
| c1 | 1.20 | 1.30 | 1.35 | G3 | 1.25 | 1.35 | 1.55 |
| D | 15.57 | 15.87 | 16.17 | b1 | 1.18 | 1.28 | 1.43 |
| H1 | | 6.70REF | | b2 | 0.70 | 0.80 | 0.95 |

E



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