

730V Super-Junction Power MOSFET

DESCRIPTION

730V super-junction Power MOSFET

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle. The SJ MOSFET is a price-performance optimized product enabling to target cost sensitive applications in Consumer and Lighting markets, designed by Wuxi Unigroup Microelectronics Company.

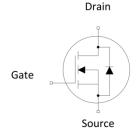
FEATURES

- Very low FOM $R_{DS(on)} \times Q_g$
- 100% avalanche tested
- RoHS compliant

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)







Device Marking and Package Information

Device	Package	Marking
TPA73R300M	TO-220F	73R300M

Key Performance Parameters

Parameter	Value	Unit
V _{DS} @ T _{j,max}	730	V
R _{DS(on),max}	0.30	Ω
I _D	15	A
$Q_{g,typ}$	28	nC
I _{DM}	45	A



Absolute Maximum Ratings T _C = 25°C, unless otherwise noted					
Parameter		Symbol	Value	Unit	
Drain-Source Voltage (V _{GS} = 0V)		V _{DSS}	730	V	
Continuous Drain Current	$T_{C} = 25^{\circ}C$		15	A	
Continuous Diain Current	TC = 100°C	. I _D	9		
Pulsed Drain Current	(note1)	I _{DM}	45	А	
Gate-Source Voltage		V _{GSS}	±30	V	
Single Pulse Avalanche Energy (note2)		E _{AS}	290	mJ	
Repetitive Avalanche Energy (note2)		E _{AR}	0.44	mJ	
Avalanche Current		I _{AR}	2.4	А	
MOSFET dv/dt ruggedness, V _{DS} = 0480V		dv/dt	50	V/ns	
Power Dissipation		P _D	32	W	
Continuous Body Diode Current		I _S	12.8	_	
Pulsed Diode Forward Current (note1)		I _{SM}	45	A	
Reverse diode dv/dt (note3)		dv/dt	15	V/ns	
Maximum diode commutation speed (note3)		di _f /dt	500	A/us	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55~+150	°C	

Thermal Resistance				
Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	R_{thJC}	3.9	0000	
Thermal Resistance, Junction-to-Ambient	R_{thJA}	80	°C/W	



Specifications $T_J = 25^{\circ}C$, t	unless othe	rwise noted					
	O. mah al	Total October 1985	Value				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	•						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = 250\mu A$	730			V	
Zero Gate Voltage Drain Current		$V_{DS} = 730V, V_{GS} = 0V, T_{J} = 25^{\circ}C$	1				
	I _{DSS}	$V_{DS} = 730V, V_{GS} = 0V, T_{J} = 150^{\circ}C$			100	μA	
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 30V$			±100	nA	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5		4.5	V	
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 7.5A		0.26	0.3	Ω	
Gate resistance	R _G	f = 1.0MHz open drain		12.5		Ω	
Dynamic				!	!		
Input Capacitance	C _{iss}	V 0V		1160		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 100V,$		42			
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		2.3			
Total Gate Charge	Q_g			28		nC	
Gate-Source Charge	Q_{gs}	$V_{DD} = 580V, I_{D} = 15A,$ $V_{GS} = 10V$		6			
Gate-Drain Charge	Q_{gd}	65 -		12			
Turn-on Delay Time	t _{d(on)}			15			
Turn-on Rise Time	t _r	$V_{DD} = 400V, I_{D} = 15A,$		49			
Turn-off Delay Time	t _{d(off)}	$R_G = 25\Omega$		134		ns	
Turn-off Fall Time	t _f			61			
Drain-Source Body Diode Characte	ristics						
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}C$, $I_{SD} = 15A$, $V_{GS} = 0V$		0.9	1.2	V	
Reverse Recovery Time	t _{rr}			355		ns	
Reverse Recovery Charge	Q _{rr}	$V_R = 400V, I_F = I_S,$ $di_F/dt = 100A/\mu s$		3.9		μC	
Peak Reverse Recovery Current	I _{rrm}	- F		22		Α	

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. I_{AS} = 2.4A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C
- 3. Identical low side and high side switch with identical ${\rm R}_{\rm G}$



Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

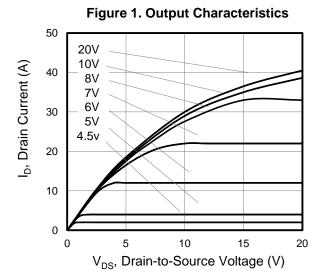
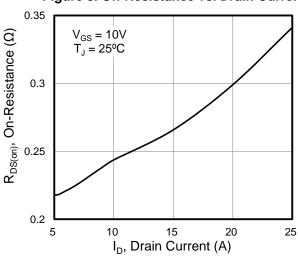


Figure 3. On-Resistance vs. Drain Current



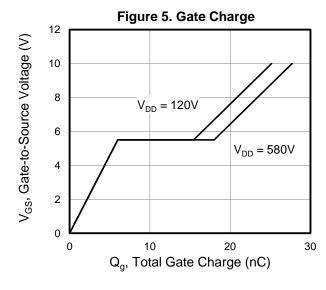


Figure 2. Transfer Characteristics

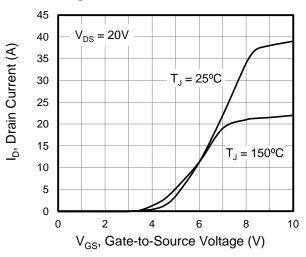


Figure 4. Capacitance

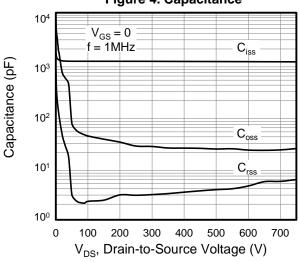
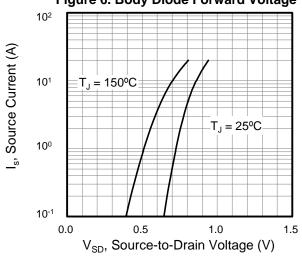


Figure 6. Body Diode Forward Voltage





Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

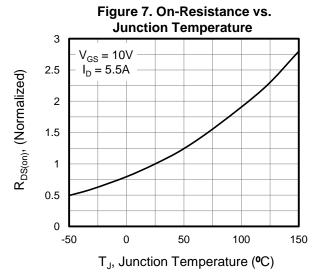


Figure 9. Transient Thermal Impedance

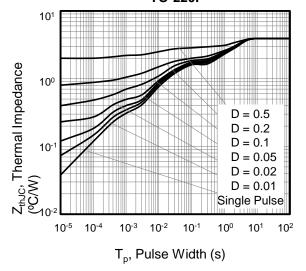


Figure 8. Breakdown voltage vs. Junction Temperature

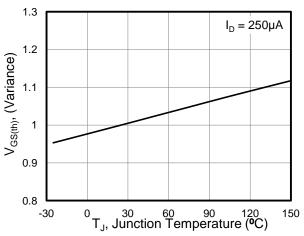


Figure 10. Safe operation area for

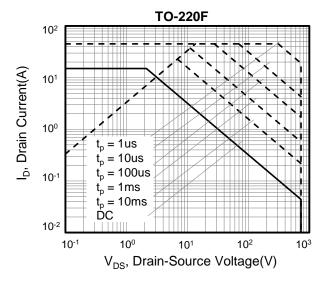




Figure A: Gate Charge Test Circuit and Waveform

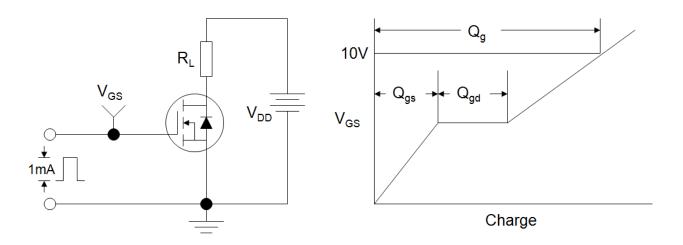


Figure B: Resistive Switching Test Circuit and Waveform

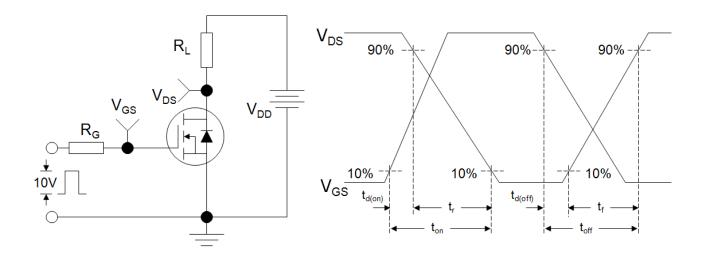
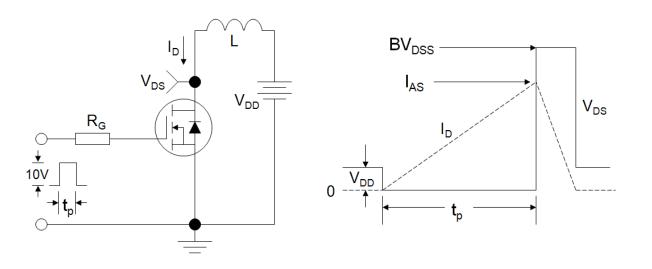
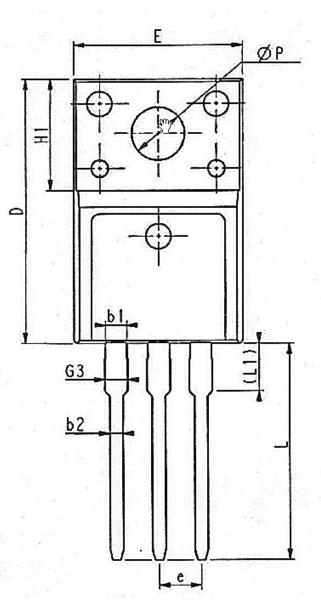


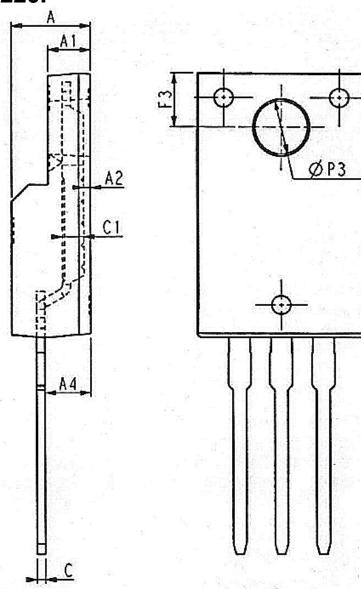
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





TO-220F





Max.

13.28

3.18

3.38

3.65

3.45

1.55

1.43

0.95

Unit:mm				Unit	:mm	_	
Symbol	Min.	Nom	Max.	Symbol	Min.	Nom	
E	9.96	10.16	10.36	е		2.54BSC	
А	4.50	4.70	4.90	L	12.68	12.98	
A1	2.34	2.54	2.74	L1	2.88	3.03	
A2	0.30	0.45	0.60	ФР	3.03	3.18	
A4	2.56	2.76	2.96	ФР3	3.15	3.45	
С	0.40	0.50	0.65	F3	3.15	3.30	
c1	1.20	1.30	1.35	G3	1.25	1.35	
D	15.57	15.87	16.17	b1	1.18	1.28	
H1		6.70REF		b2	0.70	0.80	



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