

650V Super-junction Power MOSFET

Description

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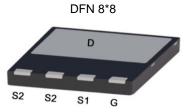
Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle. The Multi-EPI SJ MOSFET provide an extremely low switching, communication and conduction losses device with highest robustness make especially resonant switching applications more reliable, more efficient, lighter and cooler, also fits the industrial grade applications, like AC-DC SMPS requirements for PFC, AC/DC power conversion, designed by Wuxi Unigroup Microelectronics Company.

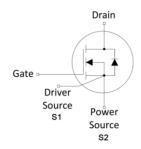
Features

- Very low FOM R_{DS(on)} × Q_a
- 100% avalanche tested
- Easy to use/drive
- RoHS compliant

Applications

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- Charger







Device Marking and Package Information

Device	Package	Marking	
TPG65R125MH	DFN 8*8	65R125M	

Key Performance Parameters

Parameter	Value	Unit
V _{DS} @ T _{j,max}	700	V
R _{DS(on),max}	0.125	Ω
$Q_{g,typ}$	57	nC
I _D	30	A
I _{D,pulse}	90	A
E _{OSS} @ 400V	6.54	μЈ
Body Diode di _F /dt	500	A/µs



Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted					
Parameter		Symbol	Value	Unit	
	$T_C = 25^{\circ}C$		- I _D	30	_
Continuous Drain Current	T _C = 100°C			18	А
Pulsed Drain Current		(note1)	I _{D,pulse}	90	Α
Gate-Source Voltage			V_{GSS}	±30	V
Single Pulse Avalanche Energy	,	(note2)	E _{AS}	636	mJ
Repetitive Avalanche Energy (note2)		E _{AR}	0.96	mJ	
Avalanche Current		I _{AR}	5.2	Α	
MOSFET dv/dt Ruggedness, V _{DS} = 0480V		dv/dt	50	V/ns	
Power Dissipation For DFN 8*8		P_{D}	219	W	
Continuous Diode Forward Current		I _S	26	Α	
Diode Pulsed Current (note1)		I _{S,pulse}	90		
Reverse Diode dv/dt (note3)		dv/dt	15	V/ns	
Maximum Diode Commutation Speed (note3)		di _f /dt	500	A/µs	
Operating Junction and Storage Temperature Range		T_J,T_stg	-55~+150	°C	

Thermal Resistance For DFN 8*8				
Parameter Symbol Value				
Thermal Resistance, Junction-to-Case	R _{thJC}	0.57	000	
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62	°C/W	



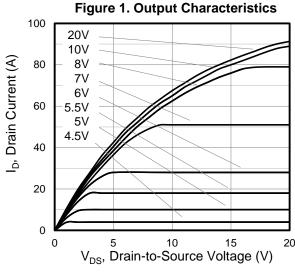
Electrical Characteristics	$\Gamma_{\rm J} = 25^{\rm o}{\rm C}$	unless otherwise noted					
Parameter	Symbol	Took Conditions	Value			l lmit	
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = 250\mu A$	650			٧	
		$V_{DS} = 650V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μА	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 650V, V_{GS} = 0V, T_{J} = 150^{\circ}C$			100		
Gate-Source Leakage Current	I _{GSS}	$V_{GS} = \pm 30V$			±100	nA	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5		4.5	>	
Drain-Source On-State-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 15A		0.116	0.125	Ω	
Gate Resistance	R_{G}	f = 1.0MHz open drain		1.5		Ω	
Dynamic Characteristics							
Input Capacitance	C _{iss}	\/ - 0\/		2393			
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 100V,$		90		pF	
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		4			
Total Gate Charge	Q_g			57			
Gate-Source Charge	Q_{gs}	$V_{DD} = 520V, I_{D} = 30A, V_{GS} = 10V$		13		nC	
Gate-Drain Charge	Q_{gd}	93		21			
Turn-on Delay Time	t _{d(on)}			24			
Turn-on Rise Time	t _r	$V_{DD} = 400V, I_{D} = 30A,$		40			
Turn-off Delay Time	t _{d(off)}	$R_G = 25\Omega$		191		ns	
Turn-off Fall Time	t _f			73			
Drain-Source Body Diode Character	istics						
Body Diode Forward Voltage	V _{SD}	$T_J = 25^{\circ}C$, $I_{SD} = 15A$, $V_{GS} = 0V$		0.9	1.2	V	
Reverse Recovery Time	t _{rr}			486		ns	
Reverse Recovery Charge	Q _{rr}	$V_R = 400V, I_F = I_S,$ $di_F/dt = 100A/\mu s$		7.4		μC	
Peak Reverse Recovery Current	I _{rrm}	·		30.6		А	

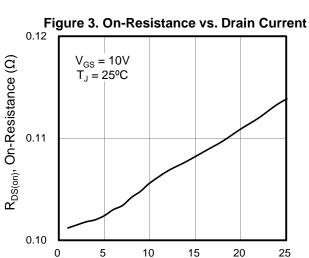
Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. I_{AS} = 5.2A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C
- 3. Identical low side and high side switch with identical $R_{\rm G}$

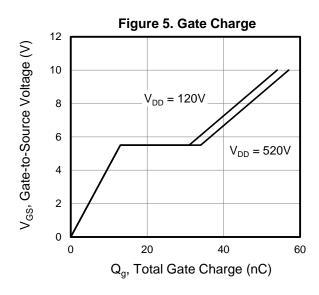


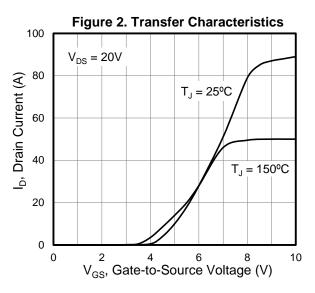
Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

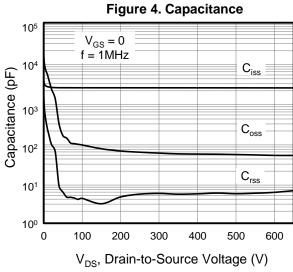


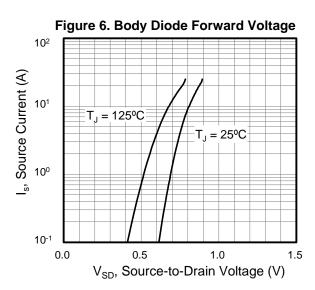


I_D, Drain Current (A)



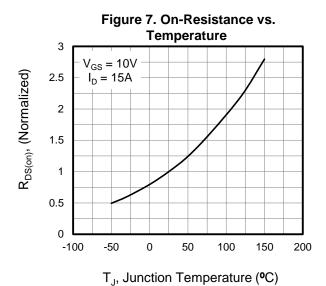








Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted



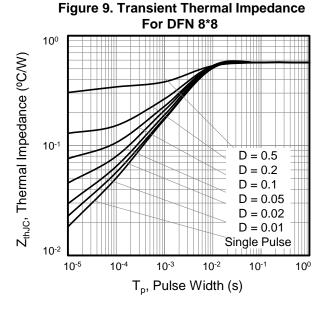


Figure 11. Typ. Coss Stored Energy

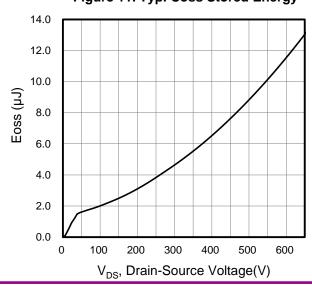


Figure 8. Breakdown voltage vs. **Junction Temperature**

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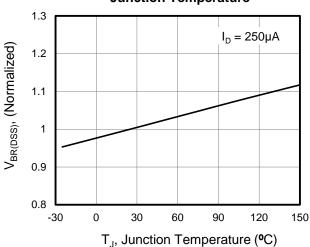


Figure 10. Safe Operation Area For

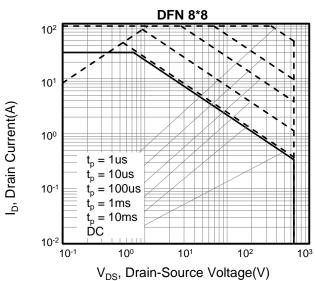




Figure A: Gate Charge Test Circuit and Waveform

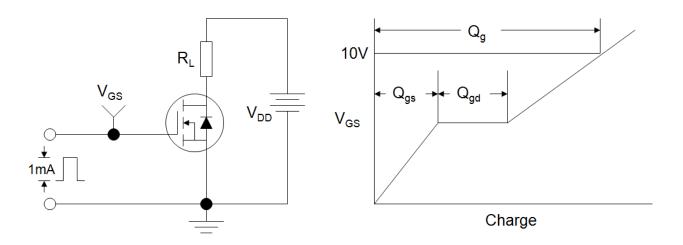


Figure B: Resistive Switching Test Circuit and Waveform

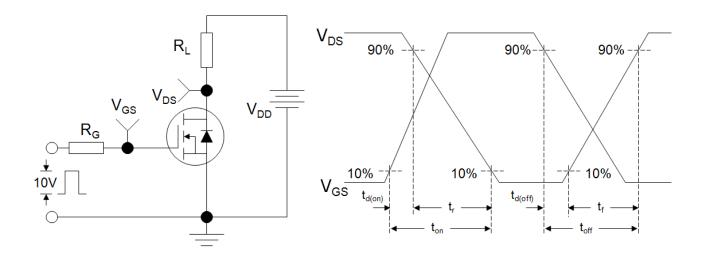
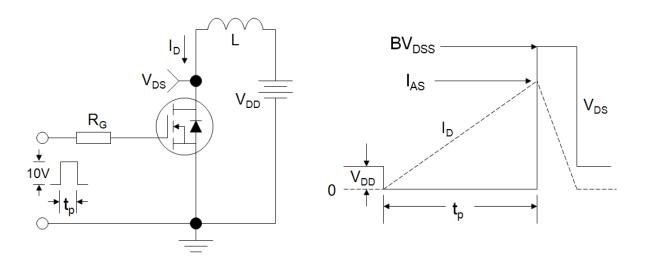
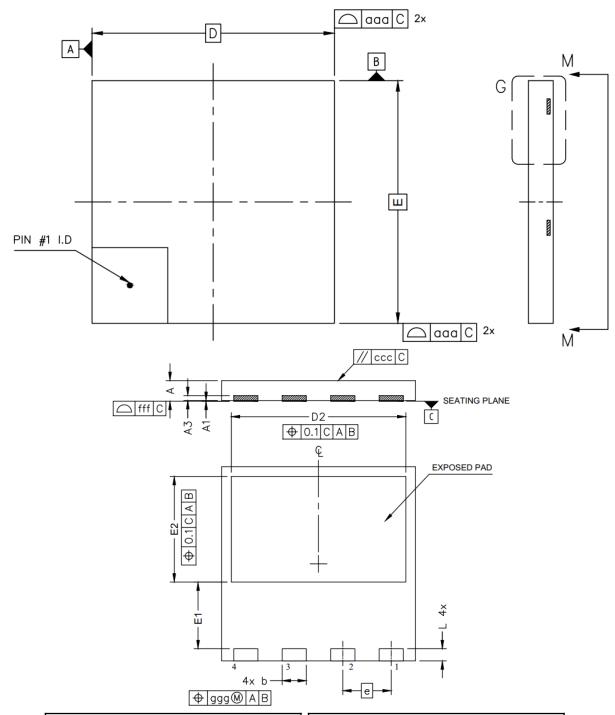


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





DFN 8*8



Unit:mm					
Symbol	Min.	Min. Nom			
Α	0.75	0.85	0.95		
A1	0.00	1	0.05		
А3	0.10	0.20	0.30		
b	0.90	1.00	1.10		
D	7.90	8.00	8.10		
Е	7.90	8.00	8.10		
D2	7.10	7.20	7.30		
E1	2.65	2.75	2.85		

Unit:mm				
Symbol	Min. Nom Ma			
E2	4.25	4.35	4.45	
е	2.00 BSC			
L	0.40	0.50	0.60	
aaa	0.10			
999	0.05			
ccc	0.05			
fff	0.05			



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