

600V Super-junction Power MOSFET

DESCRIPTION

600V Super-junction Power MOSFET

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle and pioneered. The Multi-EPI SJ MOSFET provide an extremely fast and robust body diode. Also provide an extremely low switching, communication and conduction losses device with highest robustness make especially resonant switching applications more reliable, more efficient, lighter and cooler, also fits the industrial grade applications, like AC-DC SMPS requirements for PFC, AC/DC power conversion, designed by Wuxi Unigroup Microelectronics Company.

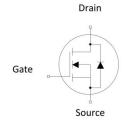
FEATURES

- Ultra-fast body diode
- Very low FOM $R_{DS(on)} \times Q_g$
- Easy to use/drive
- 100% avalanche tested
- RoHS compliant

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- LLC Half-bridge
- Charger







Device Marking and Package Information

Device	Package	Marking	
TPW60R040MFD	TO-247	60R040MFD	

Key Performance Parameters

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Parameter	Value	Unit		
V _{DS} @ T _{j,max}	600	V		
R _{DS(on),max}	0.04	Ω		
I _D	72	A		
$Q_{g,typ}$	169	nC		
I _{DM}	216	A		
t _{rr}	225	ns		
Q _{rr}	1.4	μC		
I _{rrm}	12.5	A		



Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted				
Parameter		Symbol	Value	Unit
Drain-Source Voltage (V _{GS} = 0V)		V_{DSS}	600	V
Continuous Drain Current	T _C = 25°C		72	A
	TC = 100°C	l _D	43.2	
Pulsed Drain Current	(note1)	I _{DM}	216	А
Gate-Source Voltage		V_{GSS}	±30	V
Single Pulse Avalanche Energy	(note2)	E _{AS}	2185	mJ
Repetitive Avalanche Energy (note2)		E _{AR}	3.31	mJ
Avalanche Current		I _{AR}	13.7	А
MOSFET dv/dt ruggedness, V _{DS} = 0480V		dv/dt	50	V/ns
Power Dissipation		P_{D}	500	W
Continuous Body Diode Current		I _S	72	
Pulsed Diode Forward Current (note1)		I _{SM}	216	A
Reverse diode dv/dt (note3)		dv/dt	50	V/ns
Maximum diode commutation speed (note3)		di _f /dt	900	A/us
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55~+150	°C

Thermal Resistance			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R_{thJC}	0.25	°C/W
Thermal Resistance, Junction-to-Ambient	R_{thJA}	62	C/vv



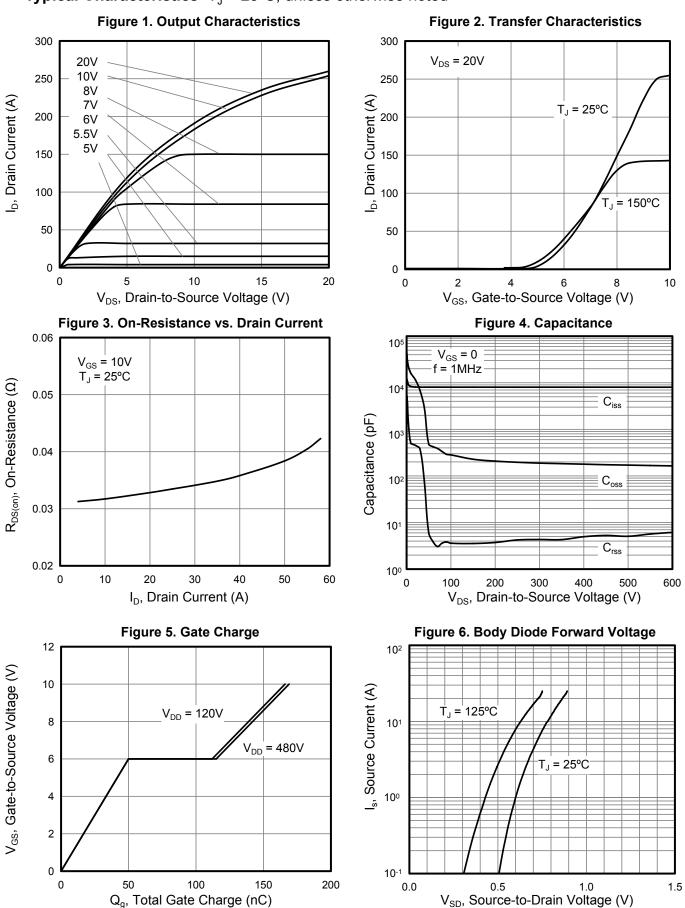
			Value			
Parameter	Symbol	Symbol Test Conditions		Тур.	Max. Unit	
Static	l			1		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_{D} = 250\mu A$	600			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 600V, V _{GS} = 0V, T _J = 25°C			10	μΑ
Gate-Source Leakage	I _{GSS}	V_{GS} = $\pm 30V$			±100	nA
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3		5	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 36A		0.035	0.04	Ω
Gate resistance	R_{G}	f = 1.0MHz open drain		0.3		Ω
Dynamic						
Input Capacitance	C _{iss}	V _{GS} = 0V,		8043		pF
Output Capacitance	C _{oss}	$V_{DS} = 100V$,		283		
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		3.6		
Total Gate Charge	Q_g			169		nC
Gate-Source Charge	Q_{gs}	$V_{DD} = 520V, I_D = 50A,$ $V_{GS} = 10V$		50		
Gate-Drain Charge	Q_{gd}	65		65		
Turn-on Delay Time	t _{d(on)}			91		
Turn-on Rise Time	t _r	V _{DD} = 400V, I _D = 50A,		90		
Turn-off Delay Time	$t_{d(off)}$	$R_G = 25\Omega$		583		ns
Turn-off Fall Time	t _f			133		
Drain-Source Body Diode Characte	ristics			_		
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}\text{C}, I_{SD} = 36\text{A}, V_{GS} = 0\text{V}$		1.0	1.5	V
Reverse Recovery Time	t _{rr}			225		ns
Reverse Recovery Charge	Q _{rr}	$V_R = 400V, I_F = 50A,$ $di_F/dt = 100A/\mu s$		1.4		μC
Peak Reverse Recovery Current	I _{rrm}	αι _τ ιας 100/ υμο		12.5		Α

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. I_{AS} = 13.7A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C
- 3. Identical low side and high side switch with identical $R_{\mbox{\scriptsize G}}$



Typical Characteristics $T_J = 25$ °C, unless otherwise noted



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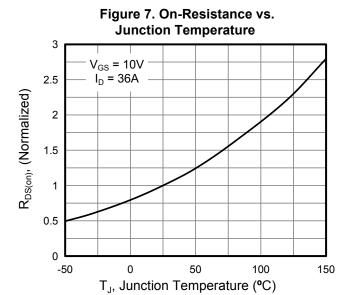


Figure 9. Transient Thermal Impedance for TO-247

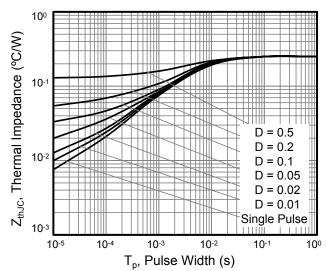


Figure 8. Breakdown voltage vs. Junction Temperature

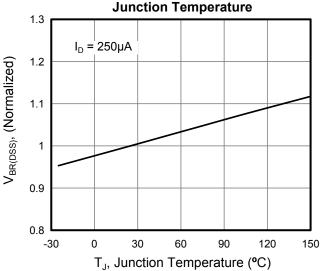


Figure 10. Safe operation area for TO-247

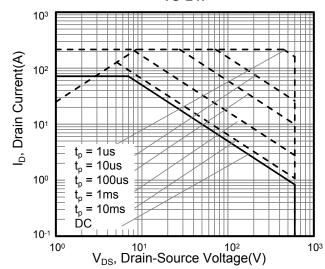




Figure A: Gate Charge Test Circuit and Waveform

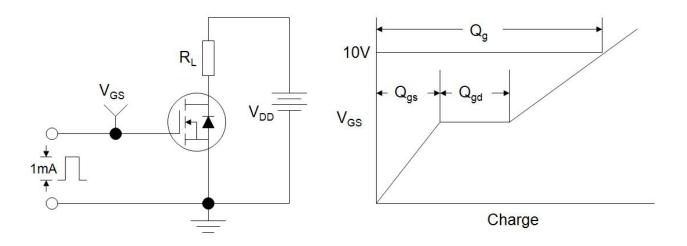


Figure B: Resistive Switching Test Circuit and Waveform

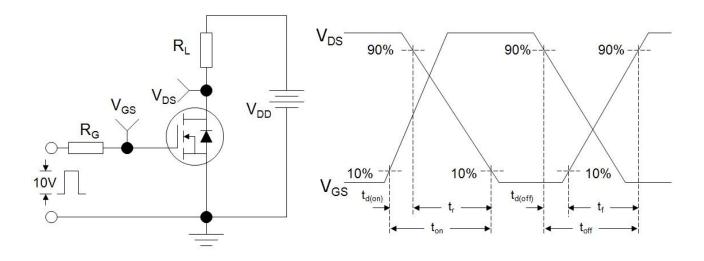
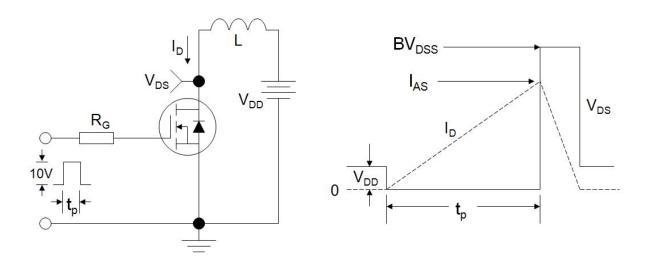
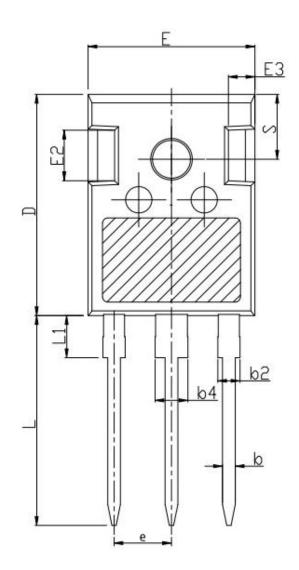
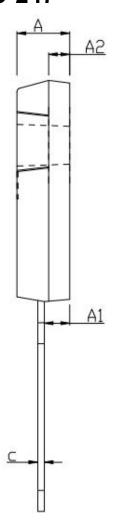


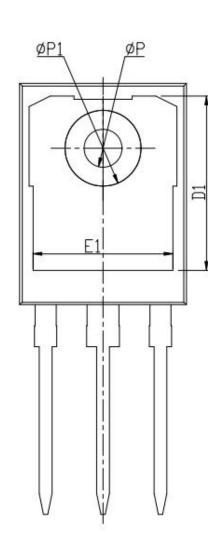
Figure C: Unclamped Inductive Switching Test Circuit and Waveform



TO-247







Unit:mm					
Symbol	Min.	Nom	Max.		
Α	4.80	5.00	5.20		
A1	2.21	2.41	2.61		
A2	1.85	2.00	2.15		
b	1.11	1.21	1.36		
b2	1.91	2.01	2.21		
b4	2.91	3.01	3.21		
С	0.51	0.61	0.75		
D	20.70	21.00	21.30		
D1	16.25	16.55	16.85		

Unit:mm					
Symbol	Min.	Nom.	Max.		
E	15.50	15.80	16.10		
E1	13.00	13.30	13.60		
E2	4.80	5.00	5.20		
E3	2.30	2.50	2.70		
е	5.44BSC				
L	19.62	19.92	20.22		
L1	-	-	4.30		
ΦР	3.40	3.60	3.80		
ФР1	-	-	7.30		
S	6.15BSC				



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