

700V Super-Junction Power MOSFET

DESCRIPTION

700V super-junction Power MOSFET

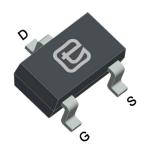
Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle. The SJ MOSFET is a price-performance optimized product enabling to target cost sensitive applications in Consumer and Lighting markets, designed by Wuxi Unigroup Microelectronics Company.

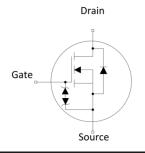
FEATURES

- Very low FOM $R_{DS(on)} \times Q_g$
- 100% avalanche tested
- RoHS compliant
- Integrated ESD protection diode

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)







Device Marking and Package Information

Device	Package	Marking
TPY70R1K5MB	SOT-223-2L	70R1K5M

Key Performance Parameters

Parameter	Value	Unit			
V _{DS} @ T _{j,max}	700	V			
R _{DS(on),max}	1.5	Ω			
I _D	3	А			
$Q_{g,typ}$	7	nC			
I _{DM}	9	А			
ESD class (HBM)	1C				



Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted				
Parameter		Symbol	Value	Unit
Drain-Source Voltage (V _{GS} = 0V)		V _{DSS}	700	V
Continuous Drain Current	$T_{\rm C} = 25^{\rm o}{\rm C}$		3	A
Continuous Drain Current	TC = 100°C	. I _D	1.8	
Pulsed Drain Current	(note1)	I _{DM}	9	А
Gate-Source Voltage		V _{GSS}	±20	V
Single Pulse Avalanche Energy	(note2)	E _{AS}	26	mJ
Repetitive Avalanche Energy (note2)		E _{AR}	0.10	mJ
Avalanche Current		I _{AR}	0.6	А
MOSFET dv/dt ruggedness, V _{DS} = 0480V		dv/dt	50	V/ns
Power Dissipation		P _D	6.2	W
Continuous Body Diode Current		I _S	2.5	A
Pulsed Diode Forward Current (note1)		I _{SM}	33	
Reverse diode dv/dt (note3)		dv/dt	15	V/ns
Maximum diode commutation speed (note3)		di _f /dt	500	A/us
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55~+150	°C

Thermal Resistance			
Parameter Symbol Value			
Thermal Resistance, Junction-to-Case	R _{thJC}	20	°C/W
Thermal Resistance, Junction-to-Ambient	R _{thJA}	160	3



			Value				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	•	-		•	•		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = 250\mu A$	700			V	
		$V_{DS} = 700V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 700V, V _{GS} = 0V, T _J = 150°C			100	μA	
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 20V$			±1	μΑ	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5		4.0	V	
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 1.5A		1.3	1.5	Ω	
Gate resistance	R_{G}	f = 1.0MHz open drain		5.5		Ω	
Dynamic	-!			!			
Input Capacitance	C _{iss}	V 0V		225		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 100V,$		10			
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		0.4			
Total Gate Charge	Q _g			7		nC	
Gate-Source Charge	Q_{gs}	$V_{DD} = 560 \text{V}, I_{D} = 3 \text{A}, $ $V_{GS} = 10 \text{V}$		1.3			
Gate-Drain Charge	Q_{gd}	. 65		3.5			
Turn-on Delay Time	t _{d(on)}			8.5			
Turn-on Rise Time	t _r	$V_{DD} = 400V, I_{D} = 3A,$		7.7			
Turn-off Delay Time	t _{d(off)}	$R_G = 25\Omega$		19.0		ns	
Turn-off Fall Time	t _f			16.5			
Drain-Source Body Diode Characte	ristics						
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}\text{C}, I_{SD} = 1.5\text{A}, V_{GS} = 0\text{V}$		0.9	1.2	V	
Reverse Recovery Time	t _{rr}			155		ns	
Reverse Recovery Charge	Q _{rr}	$V_R = 400V, I_F = 3A,$ $di_F/dt = 100A/\mu s$		0.85		μC	
Peak Reverse Recovery Current	I _{rrm}	- F		11		Α	

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. I_{AS} = 0.6A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C
- 3. Identical low side and high side switch with identical ${\rm R}_{\rm G}$



Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

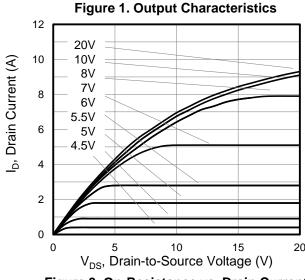


Figure 3. On-Resistance vs. Drain Current

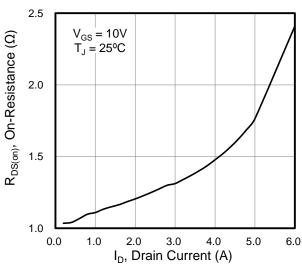


Figure 5. Gate Charge

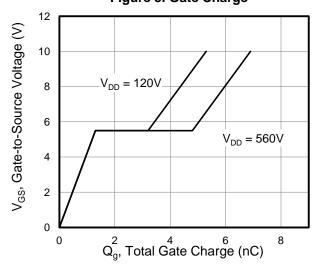


Figure 2. Transfer Characteristics

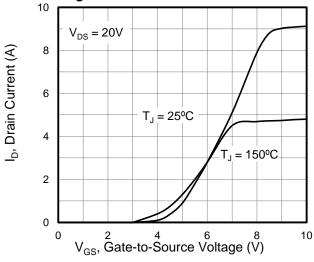


Figure 4. Capacitance

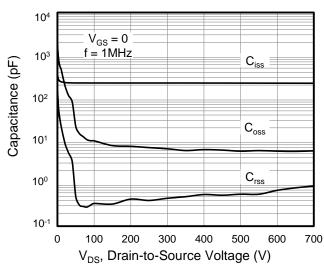
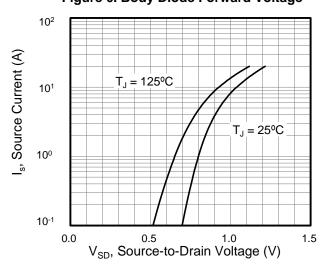


Figure 6. Body Diode Forward Voltage





Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted



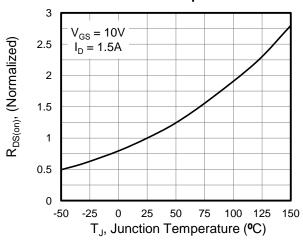


Figure 9. Transient Thermal Impedance for SOT-223-2L

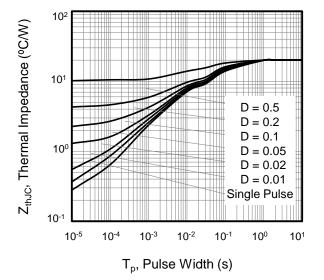


Figure 8. Breakdown voltage vs. Junction Temperature

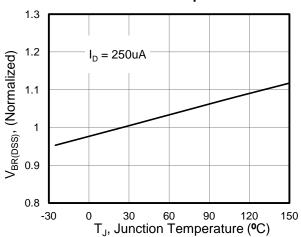


Figure 10. Safe operation area for SOT-223-2L

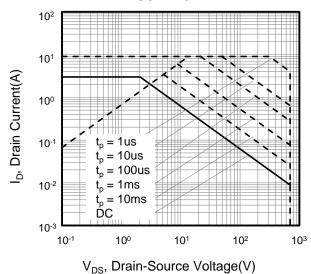




Figure A: Gate Charge Test Circuit and Waveform

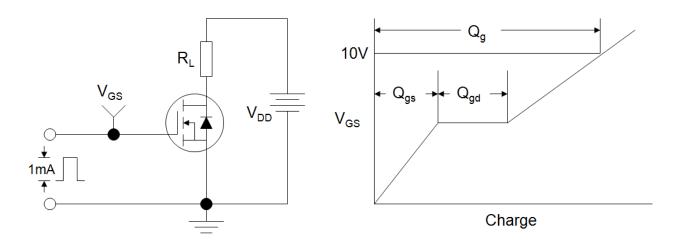


Figure B: Resistive Switching Test Circuit and Waveform

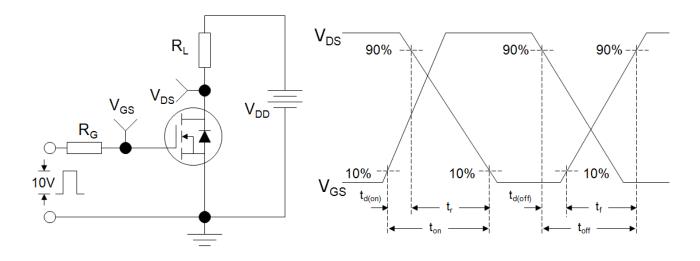
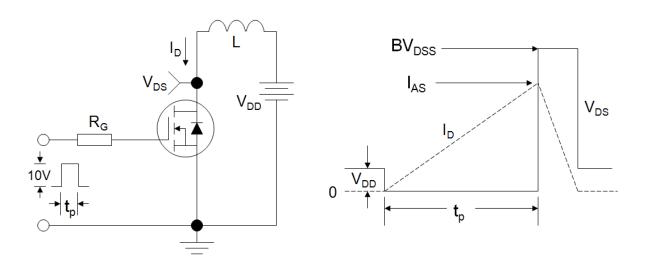
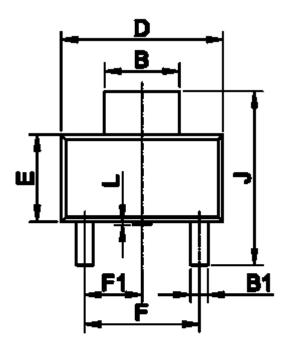


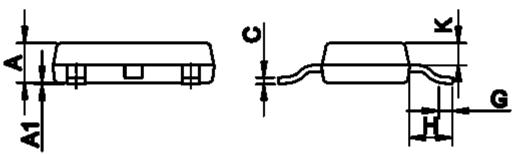
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





SOT-223-2L





Unit:mm					
Symbol	Min.	Тур.	Max.		
Α	1.50	1.60	1.80		
A1	0.01	0.06	0.10		
В	2.90	3.00	3.10		
B1	0.60	0.07	0.80		
С	0.22	0.254	0.32		
D	6.30	6.50	6.70		
Е	3.30	3.50	3.70		

Unit:mm					
Symbol	Min.	Тур.	Max.		
F		4.60			
F1		2.30			
G	0.70	0.90	1.10		
Н	1.50	1.75	2.00		
J	6.70	7.00	7.30		
К		0.90			
L	0	0.10	0.20		



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