

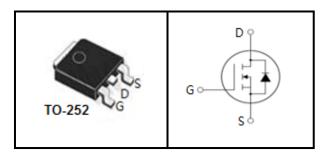
60V N-Channel DTMOS

FEATURES

- Trench Power DTMOS technology
- Low R_{DS(ON)}
- Low Gate Charge
- Optimized for fast-switching applications

APPLICATIONS

- Synchronous Rectification in DC/DC and AC/DC Converters
- Isolated DC/DC Converters in Telecom and Industrial





Absolute Maximum Ratings $T_c = 25^{\circ}C$, unless otherwise noted				
Parameter		Symbol	Value	Unit
Drain-Source Voltage (V _{GS} = 0V)		V _{DSS}	60	V
Continuous Drain Current		I _D	45	А
Pulsed Drain Current	(note1)	I _{DM}	180	А
Gate-Source Voltage		V _{GSS}	±20	V
Single Pulse Avalanche Energy	(note2)	E _{AS}	20	mJ
Avalanche Current	(note1)	I _{AS}	20	А
Power Dissipation (T _C = 25°C)		P _D	56.5	w
Operating Junction and Storage Temperatu	re Range	T _J , T _{stg}	-55~+175	°C

Thermal Resistance			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R _{thJC}	2.1	00044
Thermal Resistance, Junction-to-Ambient	R _{thJA}	50	°C/W



TSD10N06AT

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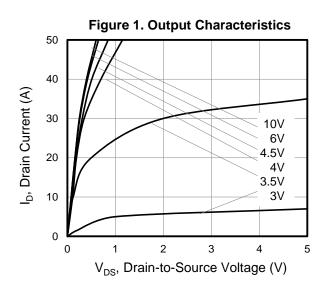
Specifications $T_J = 25^{\circ}C$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Тур.	Max.	Onic
Static			_			
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_{D} = 250\mu A$	60			V
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 60V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μA
		$V_{DS} = 60V, V_{GS} = 0V, T_{J} = 150^{\circ}C$			100	
Gate-Source Leakage	I _{GSS}	V_{GS} = $\pm 20V$			±100	nA
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.1		2.5	V
Drain Source On Desistence (Note2)	_	$V_{GS} = 10V, I_{D} = 20A$		12	15	
Drain-Source On-Resistance (Note3)	R _{DS(on)}	$V_{GS} = 4.5 V, I_{D} = 18 A$		15	19	mΩ
Forward Transconductance (Note3)	g _{fs}	$V_{DS} = 5V, I_{D} = 20A$		100		S
Dynamic						
Input Capacitance	C _{iss}			1134		pF
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 30V,$ f = 1.0MHz		123		
Reverse Transfer Capacitance	C _{rss}			12		
	Q _g (10V)	V _{DD} = 30V, I _D = 20A, V _{GS} = 10V		21		nC
Total Gate Charge	Q _g (4.5V)			11		
Gate-Source Charge	Q _{gs}			3.1		
Gate-Drain Charge	Q_{gd}			5.1		
Turn-on Delay Time	t _{d(on)}			7		
Turn-on Rise Time	t _r	V _{DD} = 30V, I _D = 20A,		3		ns
Turn-off Delay Time	t _{d(off)}	$R_{G} = 3\Omega$		20		
Turn-off Fall Time	t _f			3		
Drain-Source Body Diode Characteri	stics					
Continuous Body Diode Current	I _S	-			30	^
Pulsed Diode Forward Current	I _{SM}	$T_{\rm C} = 25^{\circ}{\rm C}$			90	A
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}C, I_{SD} = 1A, V_{GS} = 0V$		0.72	1	V
Reverse Recovery Time	t _{rr}	I _F = 20A,		17		ns
Reverse Recovery Charge	Q _{rr}	di _F /dt = 500A/µs		60		nC

Notes

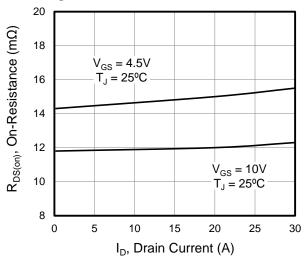
- 1. Repetitive Rating: Pulse Width limited by maximum junction temperature
- 2. I_{AS} = 20A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25°C
- 3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 1%



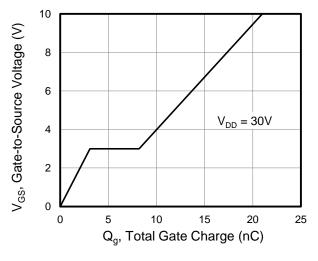
Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

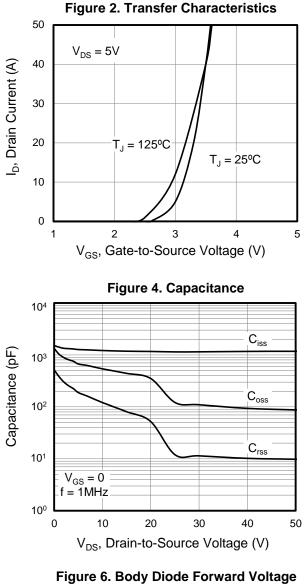


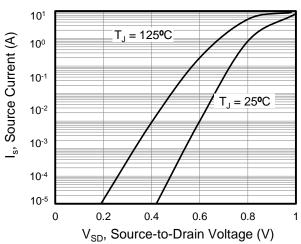












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Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

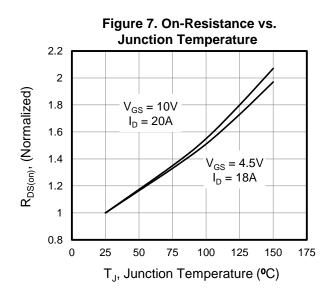
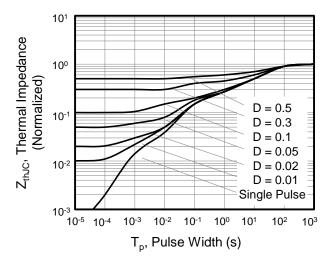
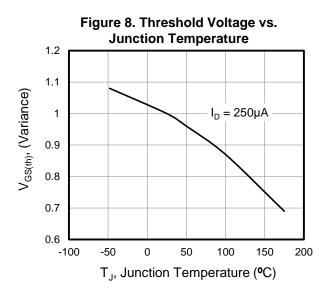
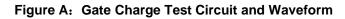


Figure 9. Transient Thermal Impedance







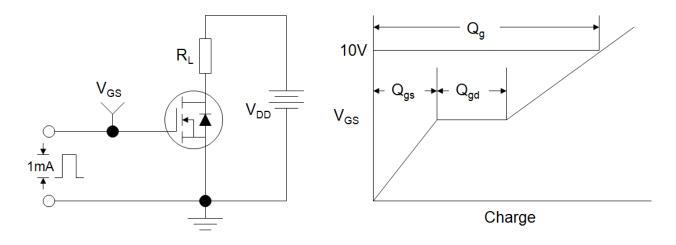


Figure B: Resistive Switching Test Circuit and Waveform

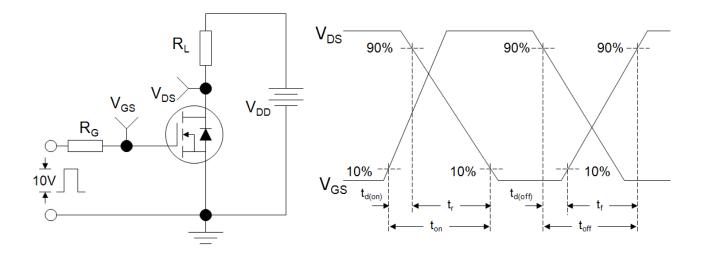
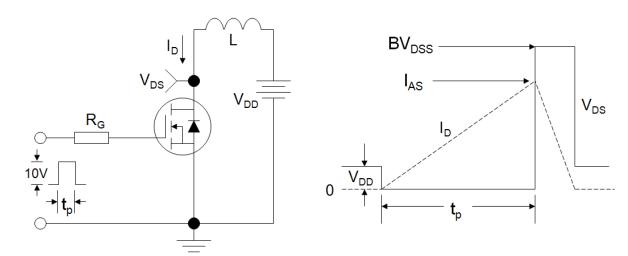
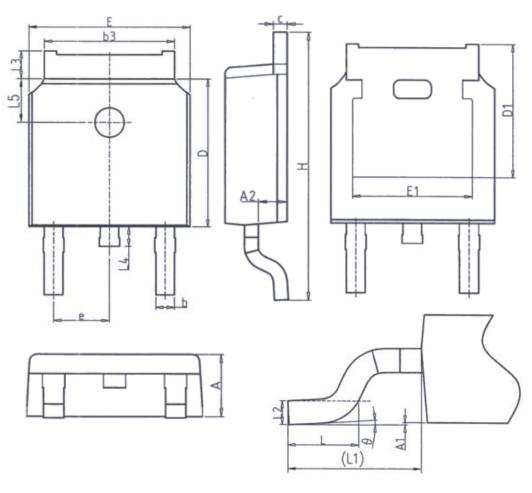


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





TO-252



Unit: mm			
Symbol	Min.	Max.	
A	2.20	2.40	
A1	0.00	0.20	
A2	0.97	1.17	
b	0.68	0.90	
b3	5.20	5.50	
с	0.43	0.63	
D	5.98	6. 22	
D1	5. 30REF		
E	6.40	6.80	
E1	4.63	_	

Unit: mm				
Symbol	Min. Max.			
е	2. 286BSC			
Н	9.40	10.50		
L	1.38	1.75		
L1	2. 90REF			
L2	0. 51BSC			
L3	0.88	1.28		
L4	_	1.00		
L5	1.65	1.95		
θ	0°	8°		



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