60V N-Channel DTMOS

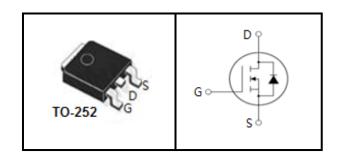
FEATURES

- Trench Power DTMOS technology
- Low R_{DS(ON)}
- Low Gate Charge
- Optimized for fast-switching applications

APPLICATIONS

- Synchronous Rectification in DC/DC and AC/DC Converters
- Isolated DC/DC Converters in Telecom and Industrial

Device Marking and Package Information				
Device	Package	Marking		
TSD12N06AT	TO-252	12N06AT		





Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted				
Parameter	Symbol	Value	Unit	
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	60	V	
Continuous Drain Current	I _D	60	Α	
Pulsed Drain Current (note1)	I _{DM}	240	Α	
Gate-Source Voltage	V_{GSS}	±20	V	
Single Pulse Avalanche Energy (note2)	E _{AS}	65	mJ	
Avalanche Current (note1)	I _{AS}	36	Α	
Power Dissipation (T _C = 25°C)	P _D	56.5	W	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+175	°C	

Thermal Resistance			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R _{thJC}	1.7	000
Thermal Resistance, Junction-to-Ambient	R _{thJA}	50	°C/W



Specifications T _J = 25°C, unless otherwise noted							
Parameter	Symbol Test Conditions		Value	1			
			Min.	Тур.	Max.		
Static			1				
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_{D} = 250\mu A$	60			V	
Zero Gate Voltage Drain Current	,	$V_{DS} = 60V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1		
Zero Gate Voltage Drain Guirent	I _{DSS}	$V_{DS} = 60V, V_{GS} = 0V, T_{J} = 150^{\circ}C$			100	μA	
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 20V$			±100	nA	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.1		2.5	V	
	D	$V_{GS} = 10V, I_D = 20A$		6.5	9	mΩ	
Drain-Source On-Resistance (Note3)	R _{DS(on)}	$V_{GS} = 4.5V, I_{D} = 20A$		10.7	13.5		
Forward Transconductance (Note3)	g _{fs}	$V_{DS} = 5V, I_{D} = 20A$		85		S	
Dynamic							
Input Capacitance	C _{iss}	V 0V		2455		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 30V,$		240			
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		34			
T	Q _g (10V)			45		nC	
Total Gate Charge	Q _g (4.5V)	$V_{DD} = 30V, I_{D} = 20A,$ $V_{GS} = 10V$		24			
Gate-Source Charge	Q_{gs}			6.8			
Gate-Drain Charge	Q_{gd}			11.5			
Turn-on Delay Time	t _{d(on)}	$V_{DD} = 30V$, $I_D = 20A$, $R_G = 3\Omega$		8			
Turn-on Rise Time	t _r			3		ns	
Turn-off Delay Time	t _{d(off)}			25			
Turn-off Fall Time	t _f			4			
Drain-Source Body Diode Characteris	stics		•	•			
Continuous Body Diode Current	Is	$T_{\rm C} = 25^{\rm o}{\rm C}$			46	А	
Pulsed Diode Forward Current	I _{SM}				138		
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}C$, $I_{SD} = 1A$, $V_{GS} = 0V$		0.72	1	V	
Reverse Recovery Time	t _{rr}	I _F = 20A,		25		ns	
Reverse Recovery Charge	Q _{rr}	$di_{F}/dt = 500A/\mu s$		110		nC	

Notes

- 1. Repetitive Rating: Pulse Width limited by maximum junction temperature
- 2. I_{AS} = 36A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 1%



Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

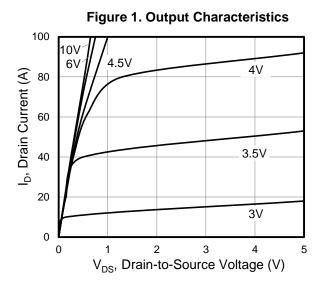


Figure 3. On-Resistance vs. Drain Current

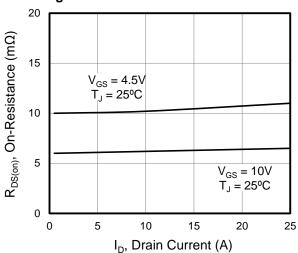


Figure 5. Gate Charge

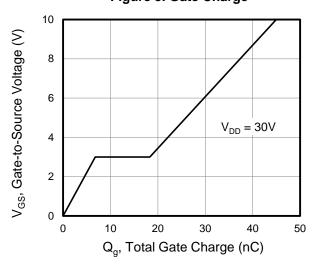


Figure 2. Transfer Characteristics

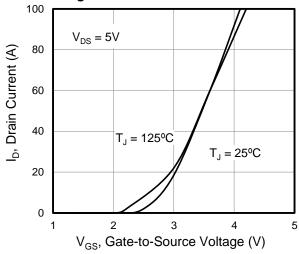


Figure 4. Capacitance

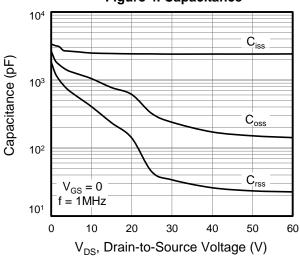
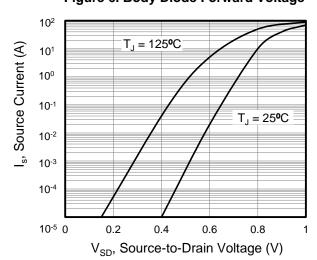


Figure 6. Body Diode Forward Voltage





Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

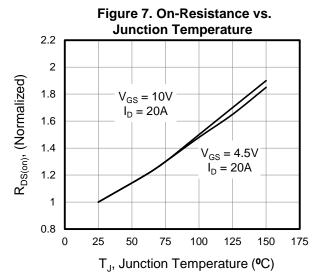
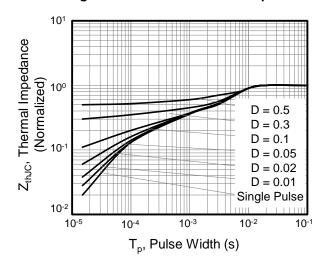


Figure 9. Transient Thermal Impedance



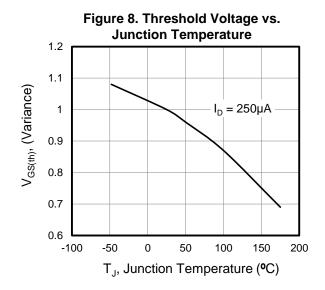


Figure A: Gate Charge Test Circuit and Waveform

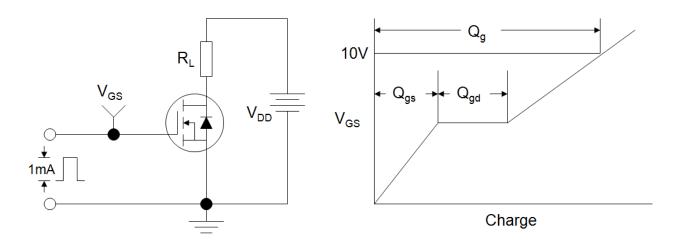


Figure B: Resistive Switching Test Circuit and Waveform

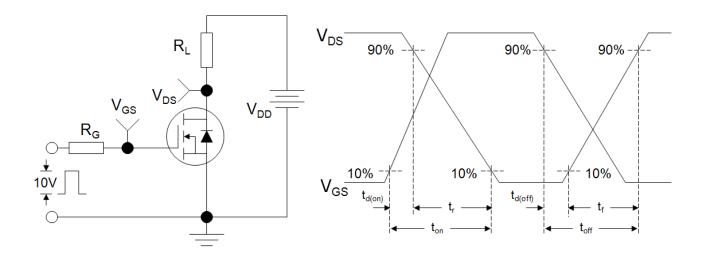
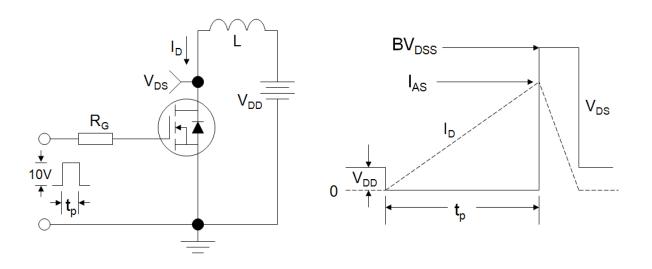
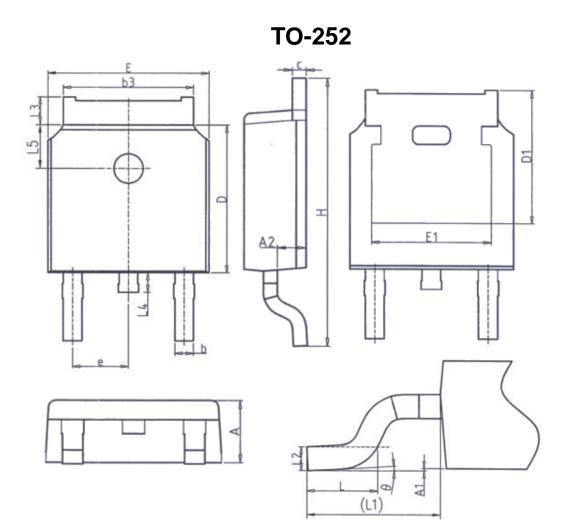


Figure C: Unclamped Inductive Switching Test Circuit and Waveform







Unit: mm			
Symbol	Min.	Max.	
Α	2. 20	2. 40	
A1	0.00	0. 20	
A2	0. 97	1. 17	
b	0. 68	0. 90	
b3	5. 20	5. 50	
С	0. 43	0. 63	
D	5. 98	6. 22	
D1	D1 5. 30REF		
E	6. 40	6. 80	
E1	4. 63	_	

Unit: mm			
Symbol	Min.	Max.	
е	2. 286BSC		
Н	9. 40	10.50	
L	1. 38	1. 75	
L1	2. 90REF		
L2	0. 51BSC		
L3	0.88	1. 28	
L4	- 1.00		
L5	1. 65	1. 95	
θ	0°	8°	



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