

100V N-Channel DTMOS

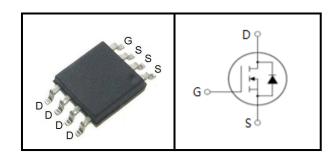
FEATURES

- Trench Power DTMOS technology
- Low R_{DS(ON)}
- Low Gate Charge
- Optimized for fast-switching applications

APPLICATIONS

- Synchronous Rectification in DC/DC and AC/DC Converters
- Isolated DC/DC Converters in Telecom and Industrial

Device Marking	g and Package l	Information
Device	Package	Marking
TSJ10N10AT	SOP-8	10N10AT





Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted			
Parameter	Symbol	Value	Unit
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	100	٧
Continuous Drain Current	I _D	8	Α
Pulsed Drain Current (note1)	I _{DM}	32	Α
Gate-Source Voltage	V _{GSS}	±20	V
Single Pulse Avalanche Energy (note2)	E _{AS}	10	mJ
Avalanche Current (note1)	I _{AS}	14	Α
Power Dissipation (T _C = 25°C)	P _D	3.1	W
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150	°C

Thermal Resistance			
Parameter	Symbol	Value	Unit
Junction-to-Drain Lead	R _{thJC}	24	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{th,IA}$	40	



			Value				
Parameter	Symbol Test Conditions		Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = 250\mu A$	100			V	
		V _{DS} = 95V, V _{GS} = 0V, T _J = 25°C			1	, . A	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 95V, V_{GS} = 0V, T_{J} = 150^{\circ}C$			100	μA	
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 20V$			±100	nA	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.1		2.5	V	
Dunin Course On Desigtance (Note2)	Б	V _{GS} = 10V, I _D = 8A		19	23	mΩ	
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	$V_{GS} = 4.5V, I_{D} = 6A$		23	33		
Forward Transconductance (Note3)	9 _{fs}	$V_{DS} = 5V, I_{D} = 1A$		30		S	
Dynamic				•	•		
Input Capacitance	C _{iss}	V 0V		1134		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 50V,$		92			
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		10.3			
T	Q _g (10V)			21		nC	
Total Gate Charge	Q _g (4.5V)	$V_{DD} = 50V, I_D = 8A,$		11			
Gate-Source Charge	Q_{gs}	$V_{GS} = 10V$		3.1			
Gate-Drain Charge	Q_{gd}			5.1			
Turn-on Delay Time	t _{d(on)}			7			
Turn-on Rise Time	t _r	$V_{DD} = 50V, I_D = 8A,$		3		ns	
Turn-off Delay Time	t _{d(off)}	$R_G = 3\Omega$		20			
Turn-off Fall Time	t _f			3			
Drain-Source Body Diode Characteris	stics			!	!		
Continuous Body Diode Current	I _S				4	_	
Pulsed Diode Forward Current	I _{SM}	$T_C = 25^{\circ}C$			12	A	
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}C$, $I_{SD} = 1A$, $V_{GS} = 0V$		0.72	1	V	
Reverse Recovery Time	t _{rr}	I _F = 8A,		20		ns	
Reverse Recovery Charge	Q _{rr}	$di_F/dt = 500A/\mu s$		90		nC	

Notes

- 1. Repetitive Rating: Pulse Width limited by maximum junction temperature
- 2. I_{AS} = 14A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 1%
- 4. When mounted on 1" in square copper board



Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

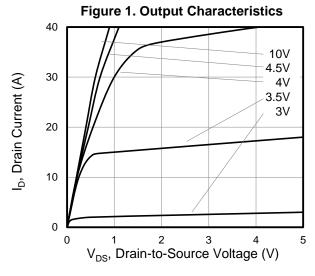
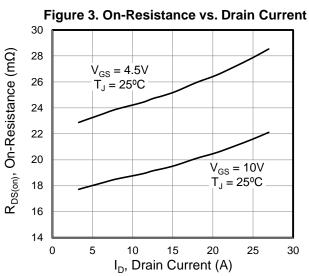
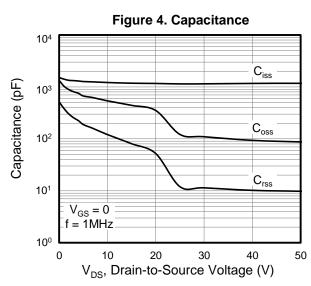
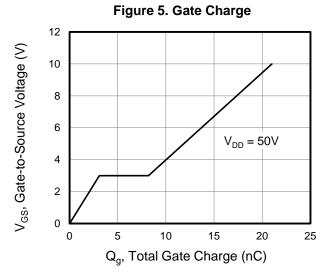


Figure 2. Transfer Characteristics

40 $V_{DS} = 5V$ $V_{DS} = 5V$ $T_{J} = 125^{\circ}C$ $T_{J} = 25^{\circ}C$ $V_{GS}, Gate-to-Source Voltage (V)$







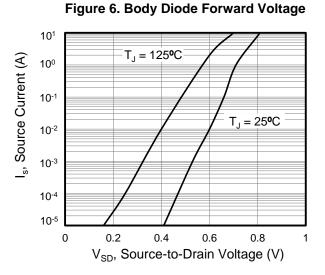




Figure 8. Threshold Voltage vs.

Junction Temperature

1.2

Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

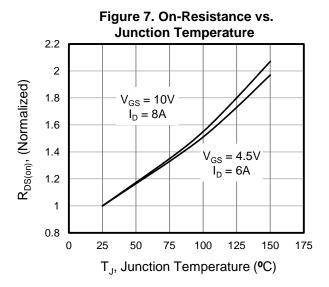


Figure 9. Transient Thermal Impedance

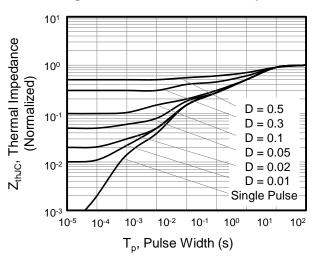


Figure A: Gate Charge Test Circuit and Waveform

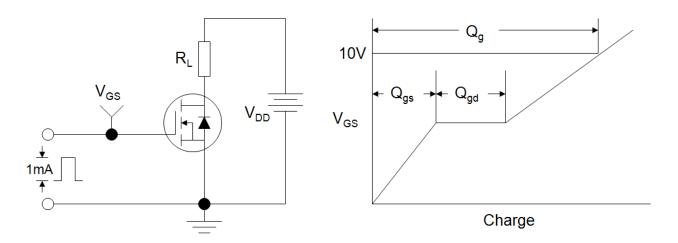


Figure B: Resistive Switching Test Circuit and Waveform

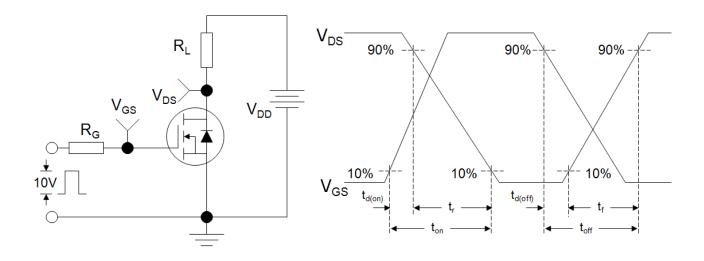
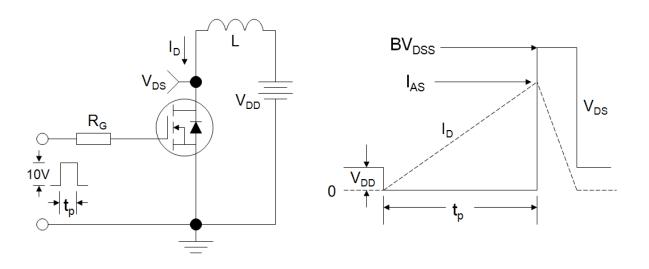
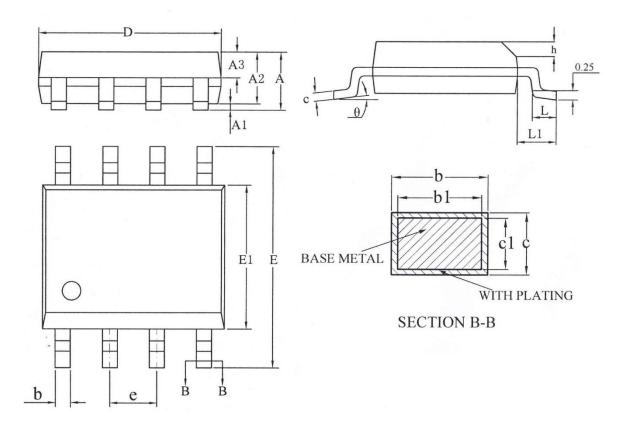


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





SOP-8



SYMBOL	MILLIMETER			
STMBOL	MIN	NOM	MAX	
A	_	_	1.75	
A1	0.10		0.225	
A2	1.30	1.40	1.50	
A3	0.60	0.65	0.70	
b	0.39	_	0.48	
b1	0.38	0.41	0.43	
С	0.21	_	0.26	
c1	0.19	0.20	0.21	

SYMBOL	MILLIMETER		
STMBOL	MIN	NOM	MAX
D	4.70	4.90	5.10
Е	5.80	6.00	6.20
E1	3.70	3.90	4.10
e	1.27BSC		
h	0.25	_	0.50
L	0.50		0.80
L1	1.05BSC		
θ	0	_	8°



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