

# **60V N-Channel Trench MOSFET**

## **General Description**

- Trench Power technology
- Low R<sub>DS(ON)</sub>
- Low Gate Charge
- Optimized for fast-switching applications

## **Applications**

- Synchronous Rectification in DC/DC and AC/DC Converters
- Isolated DC/DC Converters in Telecom and Industrial

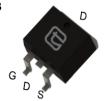
### **Product Summary**

 $\begin{array}{ll} V_{DS} & 60V \\ I_{D} \mbox{ (at $V_{GS}$=$10V)} & 145A \\ R_{DS(ON)} \mbox{ (at $V_{GS}$=$10V)} & < 4.8 m \Omega \end{array}$ 

100% UIS Tested

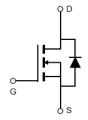


**TO-263** 



**TO-220** 





Part Number	Package Type	Form	Marking
TTB145N06A	TO-263	Tape & Reel	TTB145N06A
TTP145N06A	TO-220	Tube	TTP145N06A

# Absolute Maximum Ratings (T<sub>A</sub> =25°C unless otherwise noted)

Parameter		Symbol	Maximum	Units
Drain-Source Voltage		V <sub>DS</sub>	60	V
Gate-Source Voltage		V <sub>GS</sub>	±20	V
Continuous Drain Current B	T <sub>C</sub> =25°C	I <sub>D</sub>	105	Δ.
	T <sub>C</sub> =100°C		105	А
Pulsed Drain Current <sup>A</sup>		I <sub>DM</sub>	435	А
Avalanche Current A		I <sub>AS</sub>	57	А
Single Pulse Avalanche Energy L =0.3mH A		E <sub>AS</sub>	487	mJ
Power Dissipation <sup>C</sup>	T <sub>C</sub> =25°C	P <sub>D</sub>	217	W
	T <sub>C</sub> =100°C		108	W
Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to 175	°C
Thermal Characteristics				

#### **Thermal Characteristics**

Parameter		Symbol	Maximum	Units		
Maximum Junction-to-Case	Steady-State	R <sub>eJC</sub>	0.69	°C/W		
Maximum Junction-to-Ambient	Steady-State	$R_{\Theta JA}$	100	°C/VV		



Electrical Characteristics(T <sub>J</sub> =25°C unless otherwise noted)								
Symbol	Daramatar	Conditions		Value			11-26	
Symbol	Parameter			Min	Тур	Max	Units	
STATIC P	ARAMETERS					-		
$BV_{DSS}$	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$		60	-		V	
ı	Zero Gate Voltage Drain Current	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V	T <sub>J</sub> =25°C	-	-	1		
I <sub>DSS</sub>	Zelo Gale Vollage Dialii Guiletti	V <sub>DS</sub> =00 V, V <sub>GS</sub> =0 V	T <sub>J</sub> =125°C	1	-	100	μA	
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$		1	1	±100	nA	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		2	3	4	V	
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =30A			3.8	4.8	mΩ	
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =20A			38		S	
$V_{SD}$	Diode Forward Voltage	I <sub>S</sub> =30A, V <sub>GS</sub> =0V	I <sub>S</sub> =30A, V <sub>GS</sub> =0V			1	V	
I <sub>s</sub>	Maximum Body-Diode Continuous Current B					105	Α	
DYNAMIC	PARAMETERS							
C <sub>iss</sub>	Input Capacitance	$V_{GS} = 0V$ , $V_{DS} = 30V$ , $f = 1MH_Z$ $f = 1MH_Z$			6819		pF	
C <sub>oss</sub>	Output Capacitance				481			
C <sub>rss</sub>	Reverse Transfer Capacitance				461			
$R_g$	Gate Resistance				1.6		Ω	
SWITCHIN	NG PARAMETERS	•						
$Q_g$	Total Gate Charge		V <sub>GS</sub> =10V,V <sub>DS</sub> =30V, I <sub>D</sub> =20A		125			
$Q_{gs}$	Gate Source Charge	$V_{GS} = 10V, V_{DS} = 30V, V_{DS} = 10V, $			31.6		nC	
$Q_{gd}$	Gate Drain Charge				36.7			
t <sub>D(on)</sub>	Turn-On Delay Time	$V_{GS} = 10V, V_{DS} = 30V, I_{D} = 20A,$ $R_{G} = 2.5\Omega$			25			
t <sub>r</sub>	Turn-On Rise Time				20		ns	
T <sub>D(off)</sub>	Turn-Off Delay Time				72			
t <sub>f</sub>	Turn-Off Fall Time				31			
t <sub>rr</sub>	Body Diode Reverse Recovery Time	1 -200 4:/44 400 4/			36		ns	
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge	I <sub>F</sub> =20A, di/dt =100A/μs			60		nC	

- A. Single pulse width limited by maximum junction temperature.
- B. The maximum current rating is package limited.
- C. The power dissipation  $P_D$  is based on  $T_{J(MAX)}$  =175°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

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#### TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

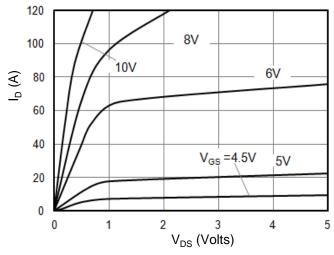


Figure 1: On-Region Characteristics

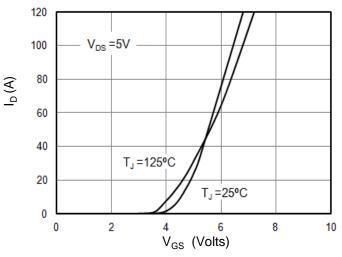


Figure 2: Transfer Characteristics

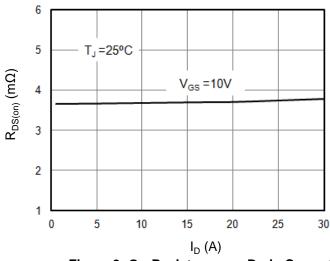
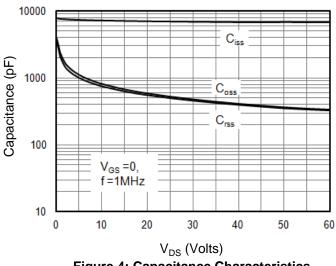


Figure 3: On-Resistance vs. Drain Current



**Figure 4: Capacitance Characteristics** 

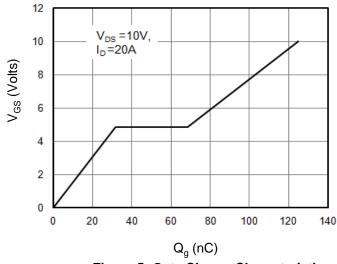


Figure 5: Gate Charge Characteristics

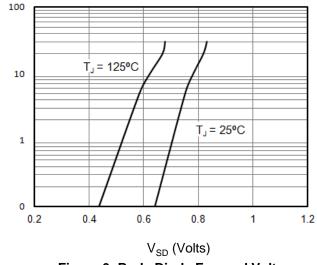


Figure 6: Body Diode Forward Voltage

I<sub>s</sub> (A)



V<sub>DS</sub> (Volts)

#### TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

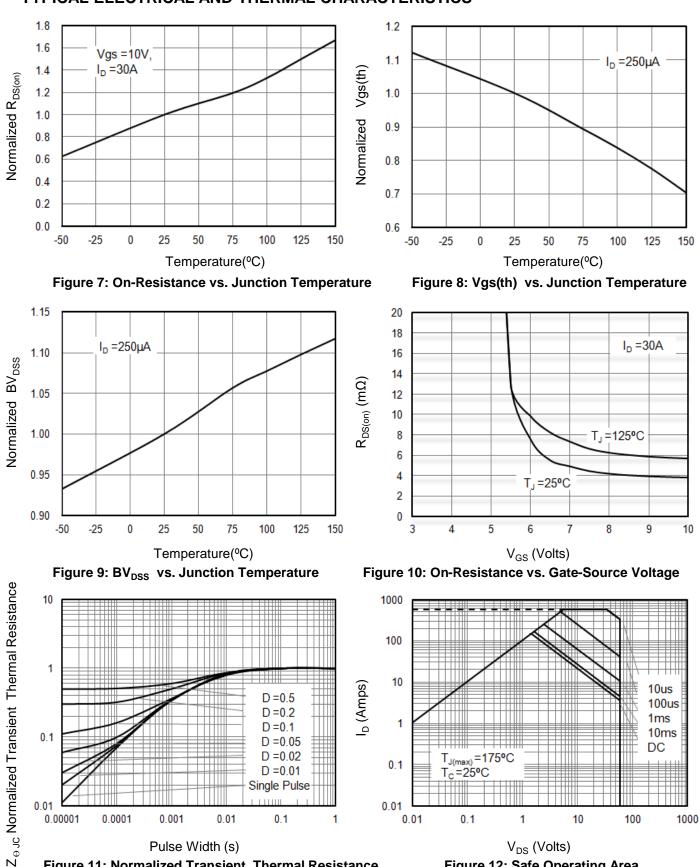


Figure 11: Normalized Transient Thermal Resistance Figure 12: Safe Operating Area

Pulse Width (s)

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Figure A: Gate Charge Test Circuit and Waveforms

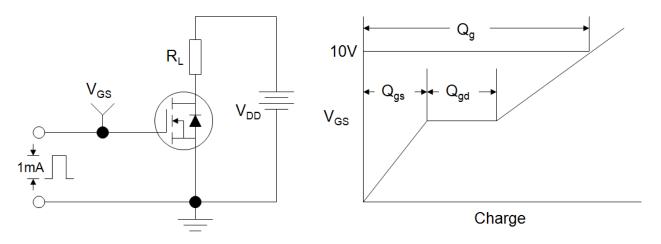


Figure B: Resistive Switching Test Circuit and Waveforms

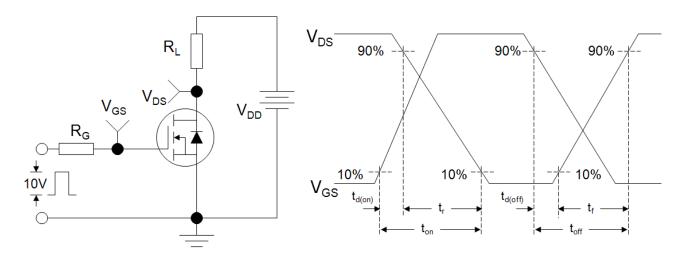
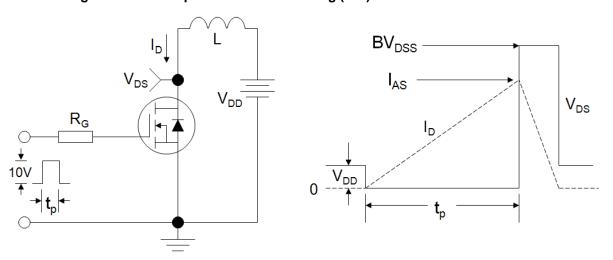


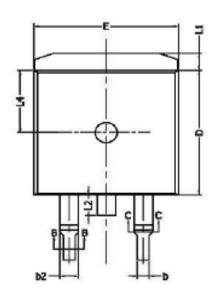
Figure C: Unclamped Inductive Switching (UIS) Test Circuit and Waveforms

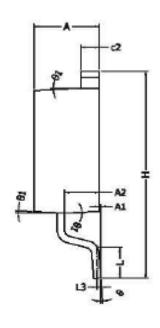


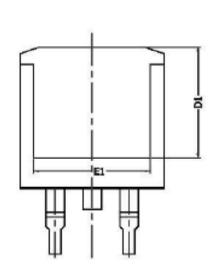
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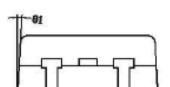


# TO-263(集佳)







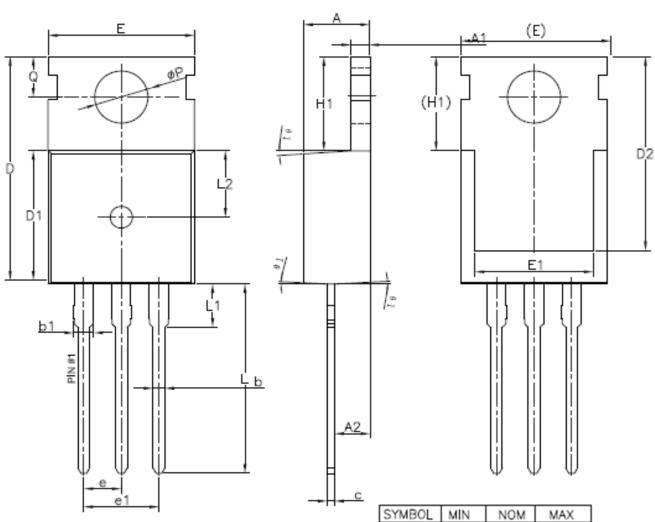


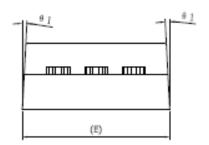
### COMMON DIMENSIONS (UNITS OF MEASURE =MILLIMETER)

SYMBOL	MIN	NOM	MAX		
A	4.40	4.50	4.60		
A1	0	0.10	0.25		
A2	2.20	2.40	2.60		
b	0.76		0.89		
b1	0.75	0.80	0.85		
b2	1.23		1.37		
b3	1.22	1.27	1.32		
C	0.47		0.60		
c1	0.46	0.51	0.56		
c2	1.25	1.30	1.35		
D.	9.10	9.20	9.30		
D1	8.00		1000		
E	9.80	9.90	10.00		
E1	7.80		-		
е	2.54 BSC				
Н	14.90	15.30	15.70		
L	2.00	2.30	2.60		
L1	1.17	1.27	1.40		
12			1.75		
L3	0.25BSC				
L4	4.60 REF				
0	Oo.	80			
01	1°	3°	5°		



# TO-220 (集佳)





SYMBOL	MIN	NOM	MAX		
Α	4.40	4.50	4.60		
A1	1.27	1.30	1.33		
A2	2.30	2.40	2.50		
b	0.70	_	0.90		
b1	1.27	1	1.40		
С	0.45	0.50	0.60		
D	15.30	15.70	16.10		
D1	9.10	9.20	9.30		
D2	13.10	_	13.70		
Ε	9.70	9.90	10.20		
E1	7.80	8.00	8.20		
е	2.54BSC				
e1		5.08BSC	;		
H1	6.30	6.50	6.70		
L	12.78	13.08	13.38		
L1	_	_	3.50		
L2	4.60REF				
øΡ	3.55 3.60		3.65		
Q	2.73	_	2.87		
0 1	1*	3*	5*		



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