

85V N-Channel Trench MOSFET(Preliminary)

| General Description | 1 | Product Summa | Product Summary | | |
|---|---|--|-----------------|--|--|
| Trench Power Technolo | ЭУ | Vds | 85V | | |
| Low R_{DS(ON)} Low Gate Charge | | I _D (at V _{GS} =10V) | 85A | | |
| Optimized for fast-switch | ing Applications | $R_{DS(ON)}$ (at V _{GS} =10V) | < 9mΩ | | |
| - | on in DC/DC and AC/DC Conv ers in Telecom and Industrial | 100% UIS Tested | RoHS | | |
| TO-263 G D S | | 220 Ref 0 s | Drain Gate | | |
| Device | Package | Form | Marking | | |
| TTB85N08A | TO-263 | Tape & Reel | 85N08A | | |
| TTP85N08A | TO-220 | Tube | 85N08A | | |

| Absolute Maximum Ratings (T _A =25°C unless otherwise noted) | | | | | |
|--|---------------------------------|-----------------------------------|------------|-------|--|
| Parameter | | Symbol | Maximum | Units | |
| Drain-Source Voltage | | V _{DS} | 85 | V | |
| Gate-Source Voltage | | V _{GS} | ±20 | V | |
| Continuous Drain Current ^B | $T_{\rm C} = 25^{\rm o}{\rm C}$ | I _D | 85 | | |
| | T _C = 100°C | | 55 | A | |
| Pulsed Drain Current ^A | | I _{DM} | 255 | A | |
| Avalanche Current ^A | | I _{AS} | 40 | A | |
| Single Pulse Avalanche Energy L =0.3mH ^A | | E _{AS} | 240 | mJ | |
| Dower Dissingtion | $T_{\rm C} = 25^{\rm o}{\rm C}$ | | 160 | W | |
| Power Dissipation ^C | T _C = 100°C | P _D | 78 | W | |
| Operating Junction and Storage Temperature Range | | T _J , T _{SGT} | -55 to 175 | °C | |

Thermal Resistance Parameter Symbol Maximum Units Thermal Resistance, Junction-to-Case Steady-State R_{thJC} 0.95 \circ C/W Thermal Resistance, Junction-to-Ambient Steady-State R_{thJA} 100 \circ C/W



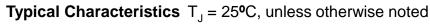
| Electric | cal Characteristics(T _J =25°C ur | nless otherwise r | noted) | | | | |
|---|---|---|-----------------------|-------|------|------|-------|
| 0 | Demonster | Conditions | | Value | | | |
| Symbol | Parameter | | | Min | Тур | Max | Units |
| STATIC P | ARAMETERS | • | | - | | | - |
| BV_{DSS} | Drain-Source Breakdown Voltage | $I_D = 250 \mu A, V_{GS} = 0 V$ | | 85 | | | V |
| 1 | | $V_{DS} = 85V, V_{GS} = 0V$ | T _J =25°C | | | 1 | |
| I _{DSS} | Zero Gate Voltage Drain Current | | T _J =100°C | | | 25 | μA |
| I _{GSS} | Gate-Body Leakage Current | $V_{DS} = 0V, V_{GS} = \pm 20V$ | | | | ±100 | nA |
| V _{GS(th)} | Gate Threshold Voltage | $V_{\rm DS} = V_{\rm GS}, \ I_{\rm D} = 250 \mu A$ | | 2 | 3 | 4 | V |
| R _{DS(ON)} | Static Drain-Source On-Resistance | $V_{GS} = 10V, I_{D} = 30A$ | | | 8.4 | 9 | mΩ |
| 9 _{FS} | Forward Transconductance | $V_{\rm DS} = 5V, I_{\rm D} = 20A$ | | 30 | | | S |
| V _{SD} | Diode Forward Voltage | I _S = 20A, V _{GS} = 0V | | | | 1 | V |
| I _S Maximum Body-Diode Continuous Current ^B | | | | | 85 | А | |
| DYNAMIC | PARAMETERS | | | | | | |
| C _{iss} | Input Capacitance | V _{GS} =0V, V _{DS} = 40V, f =1MH _Z | | | 5400 | | pF |
| C _{oss} | Output Capacitance | | | | 245 | | |
| C _{rss} | Reverse Transfer Capacitance | | | | 204 | | |
| SWITCHII | NG PARAMETERS | • | | - | - | • | |
| Q _g (10V) | Total Gate Charge | | | | 92 | | |
| Q _{gs} | Gate Source Charge | V _{GS} =10V,V _{DS} = 40V, I _D = 20A | | | 27 | | nC |
| Q _{gd} | Gate Drain Charge | | | | 21 | | |
| t _{D(on)} | Turn-On Delay Time | $V_{GS} = 10V, V_{DS} = 40V, I_{D} = 20A,$ $R_{G} = 2.5\Omega$ | | | 24 | | |
| t _r | Turn-On Rise Time | | | | 19 | | ns |
| T _{D(off)} | Turn-Off Delay Time | | | | 70 | | |
| t _f | Turn-Off Fall Time | | | | 30 | | |
| t _{rr} | Body Diode Reverse Recovery Time | I _F =20A, di/dt =100A/μs | | | 37 | | ns |
| Q _{rr} | Body Diode Reverse Recovery Charge | | | | 58 | | nC |

A. Single pulse width limited by maximum junction temperature.

- B. The maximum current rating is package limited.
- C. The power dissipation P_D is based on $T_{J(MAX)} = 175^{\circ}$ C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.







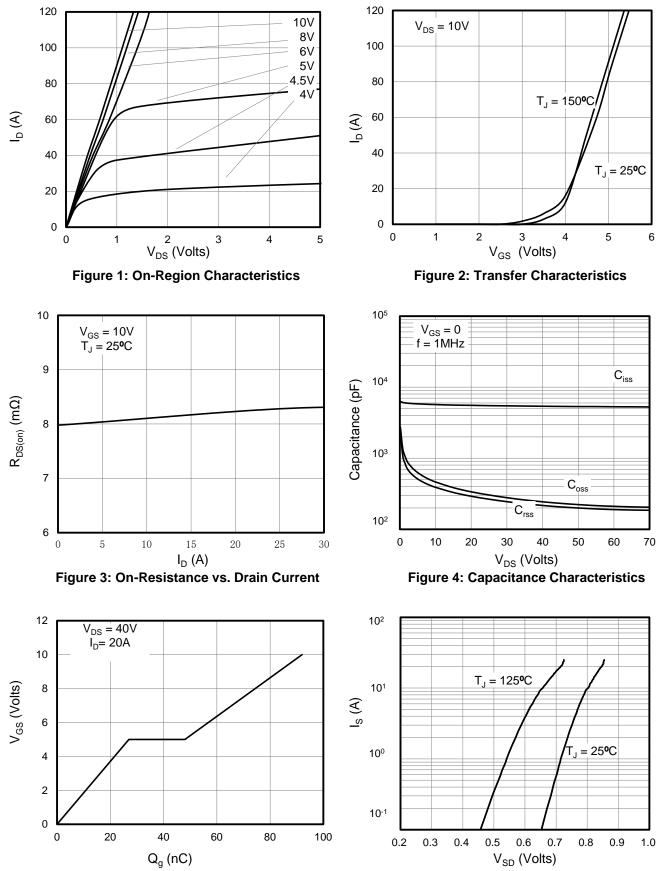
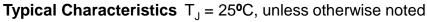
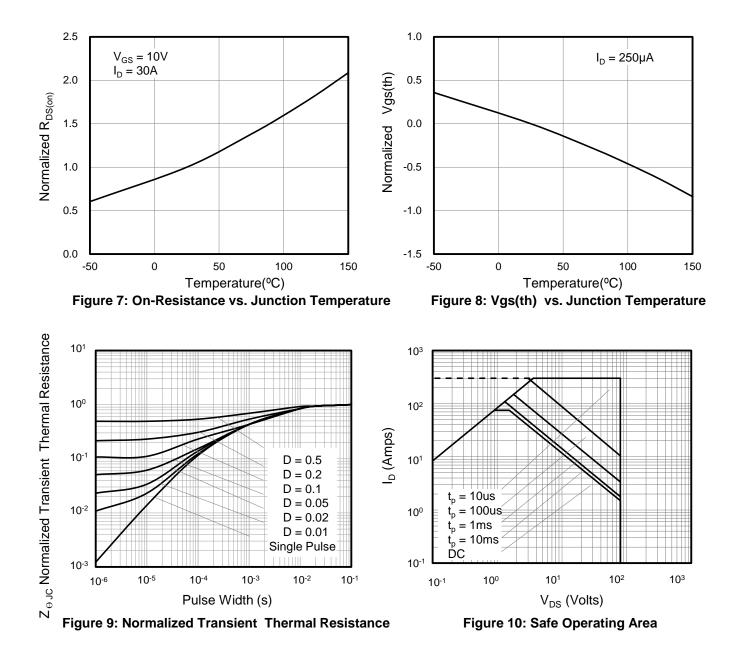
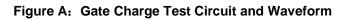


Figure 5: Gate Charge Characteristics Figure 6: Body Diode Forward Voltage









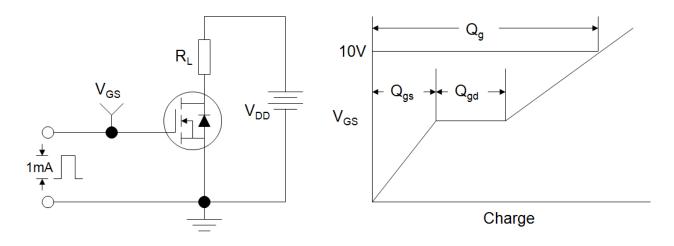


Figure B: Resistive Switching Test Circuit and Waveform

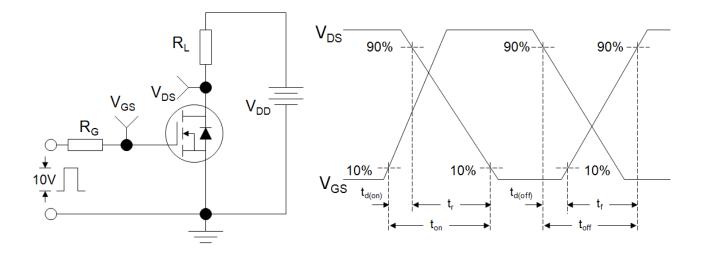
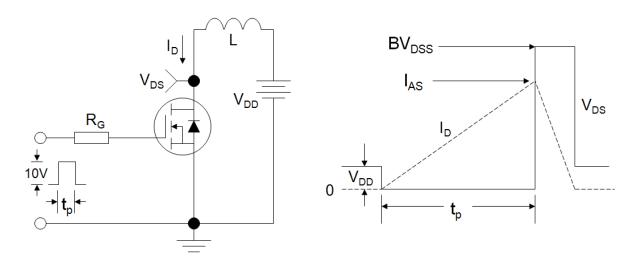
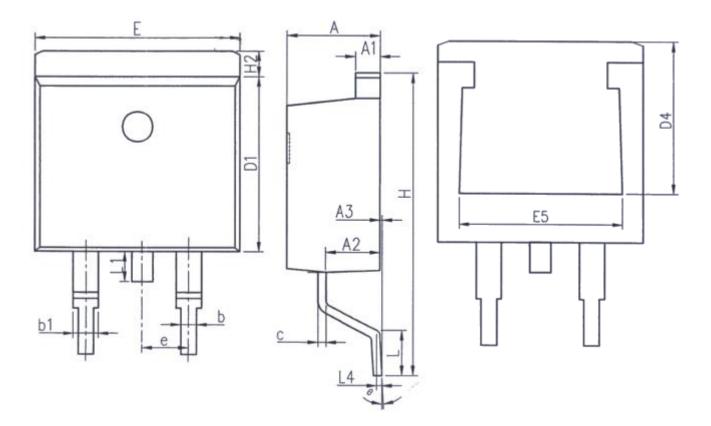


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





TO-263



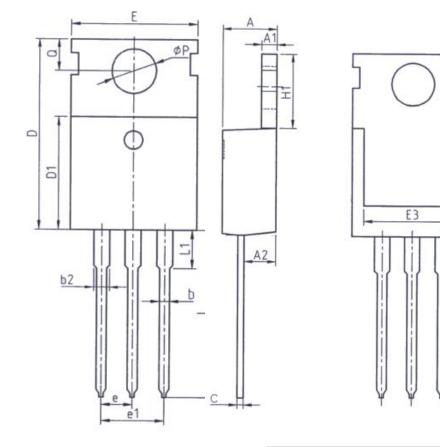
| | Unit: mm | | l | Unit: mm | n |
|--------|----------|-------|--------|----------|------------|
| Symbol | Min. | Max. | Symbol | Min. | Max. |
| Α | 4. 37 | 4. 77 | E | 9.86 | 10.36 |
| A1 | 1.22 | 1.42 | E5 | 7.06 | - |
| A2 | 2.49 | 2.89 | e | 2. 54BSC | |
| A3 | 0.00 | 0. 25 | Н | 14. 70 | 15. 50 |
| b | 0.70 | 0.96 | H2 | 1.07 | 1.47 |
| b1 | 1.17 | 1.47 | L | 2.00 | 2.60 |
| с | 0.30 | 0.53 | L1 | 1.40 | 1.70 |
| D1 | 8.50 | 8.90 | L4 | 0. 25BSC | |
| D4 | 6. 60 | - | θ | 0° | 9 ° |

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E

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| Unit: mm | | | | |
|----------|--------|--------|--|--|
| Symbol | Min. | Max. | | |
| Α | 4. 37 | 4.77 | | |
| A1 | 1.25 | 1.45 | | |
| A2 | 2.20 | 2.60 | | |
| b | 0.70 | 0.95 | | |
| b2 | 1.17 | 1.47 | | |
| С | 0.40 | 0.65 | | |
| D | 15. 10 | 16. 10 | | |
| D1 | 8.80 | 9.40 | | |
| D2 | 5, 50 | - | | |

| Unit: mm | | | | |
|----------|------------|-------|--|--|
| Symbol | Min. Max. | | | |
| E | 9.70 10.30 | | | |
| E3 | 7.00 - | | | |
| e | 2. 54BSC | | | |
| e1 | 5. 08BSC | | | |
| H1 | 6. 25 | 6.85 | | |
| L | 12. 75 | 13.80 | | |
| L1 | _ | 3. 40 | | |
| Р | 3.40 3.80 | | | |
| Q | 2.60 | 3.00 | | |



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