

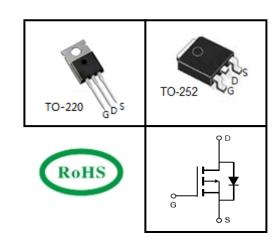
# **100V P-Channel Trench MOSFET**

#### **FEATURES**

- Trench Power MOSFET Technology
- Low R<sub>DS(ON)</sub>
- Low Gate Charge
- Optimized For Fast-switching Applications

#### **APPLICATIONS**

- Load Switches
- Battery Switch



Device Marking and Package Information			
Device	Package	Marking	
TTP18P10AT	TO-220	18P10AT	
TTD18P10AT	TO-252	18P10AT	

<b>Absolute Maximum Ratings</b> $T_C = 25^{\circ}C$ , unless otherwise noted				
Parameter		O	Value	
Parameter		Symbol	TO-220, TO-252	Unit
Drain-Source Voltage (V <sub>GS</sub> = 0V)		V <sub>DSS</sub>	-100	V
Continuous Drain Current		I <sub>D</sub>	-18	А
Pulsed Drain Current	(note1)	I <sub>DM</sub>	-90	А
Gate-Source Voltage		$V_{GSS}$	±20	V
Single Pulse Avalanche Energy	(note2)	E <sub>AS</sub>	60	mJ
Avalanche Current	(note1)	I <sub>As</sub>	-20	А
Power Dissipation (T <sub>C</sub> = 25°C)		P <sub>D</sub>	113	W
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55~+175	°C

Thermal Resistance				
Parameter	Symbol	Value	Unit	
Farameter	Symbol	TO-220, TO-252	Onit	
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	1.32	12001	
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	60	K/W	

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		rwise noted	Value			
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V, I_{D} = -250\mu A$	-100			V
Zeus Cota Valta de Dusia Cumant		$V_{DS} = -100V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			-1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -100V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 150°C			-100	μΑ
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS} = \pm 20V$			±100	nA
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-1.0	-1.7	-3.0	V
D : 0	5	V <sub>GS</sub> = -10V, I <sub>D</sub> = -9A		80	96	mΩ
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	$V_{GS} = -4.5V, I_{D} = -9A$		90	108	mΩ
Forward Transconductance (Note3)	g <sub>fs</sub>	V <sub>DS</sub> = -50V, I <sub>D</sub> = -10A		5.9		S
Dynamic		-				
Input Capacitance	C <sub>iss</sub>	V 0V		4182		
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = -50V,$		102		pF
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		53		
Total Gate Charge	$Q_g$			70		
Gate-Source Charge	$Q_{gs}$	$V_{DD} = -50V, I_{D} = -18A, V_{GS} = -10V$		13		nC
Gate-Drain Charge	$Q_{gd}$	93		16		
Turn-on Delay Time	t <sub>d(on)</sub>			16		
Turn-on Rise Time	t <sub>r</sub>	$V_{DD} = -50V, I_{D} = -18A,$		73		
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 2.5\Omega$		34		ns
Turn-off Fall Time	t <sub>f</sub>			57		
Drain-Source Body Diode Characteris	stics					
Continuous Body Diode Current	I <sub>s</sub>	T 0500			-18	Δ.
Pulsed Diode Forward Current	I <sub>SM</sub>	$T_C = 25^{\circ}C$			-90	А
Body Diode Voltage	V <sub>SD</sub>	$T_J = 25^{\circ}\text{C}, I_{SD} = -18\text{A}, V_{GS} = 0\text{V}$			-1.2	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = -18A,		89		ns
Reverse Recovery Charge	Q <sub>rr</sub>	di <sub>F</sub> /dt = 100A/μs		66		nC

#### Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2.  $I_{AS}$  = -20A,  $V_{DD}$  = 50V,  $R_{G}$  = 25 $\Omega$ , Starting  $T_{J}$  = 25 $^{\circ}$ C
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%

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### **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

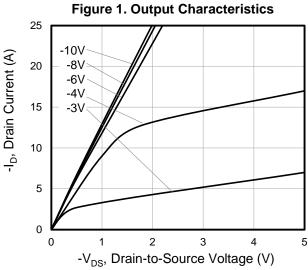
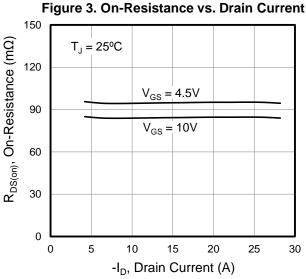
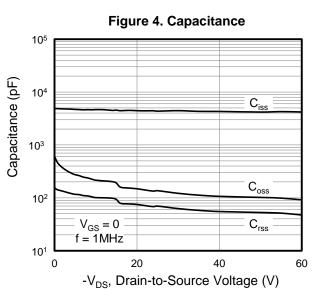
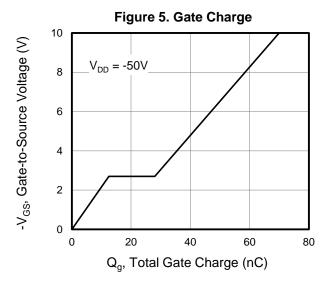
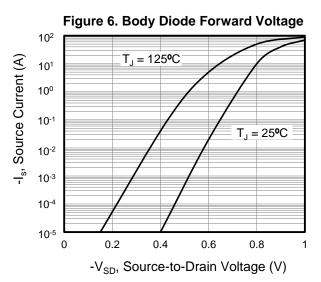


Figure 2. Transfer Characteristics 20  $V_{DS} = -5V$ -I<sub>D</sub>, Drain Current (A) 16 12  $T_J = 125^{\circ}C$ 8  $T_J = 25^{\circ}C$ 4 0 0 5 -V<sub>GS</sub>, Gate-to-Source Voltage (V)











## **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

Figure 7. On-Resistance vs.

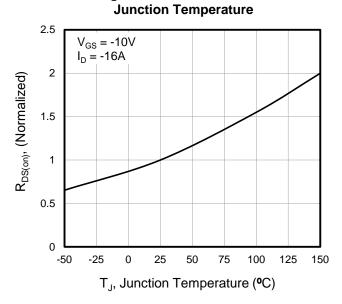


Figure 8. Threshold Voltage vs. Junction Temperature

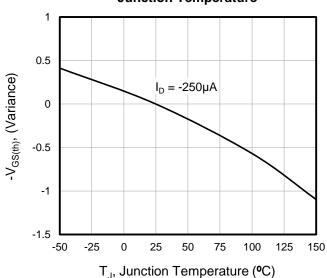
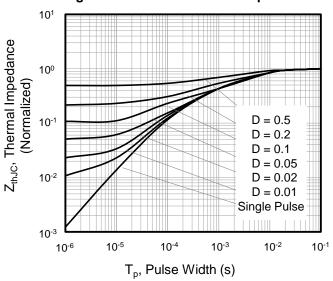


Figure 9. Transient Thermal Impedance



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Figure A: Gate Charge Test Circuit and Waveform

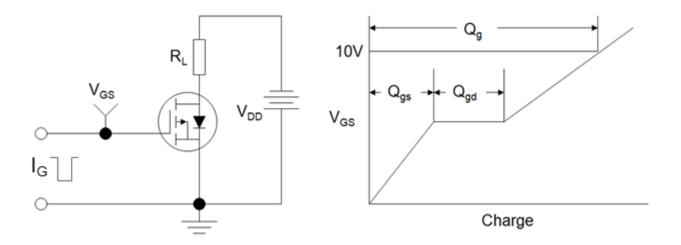


Figure B: Resistive Switching Test Circuit and Waveform

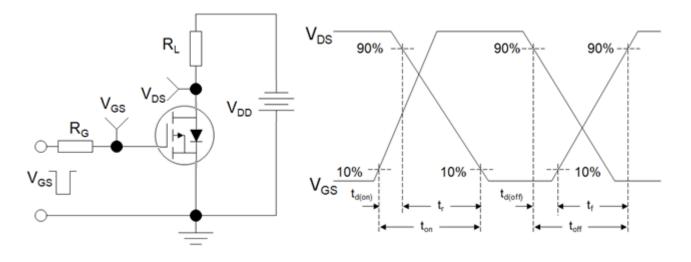
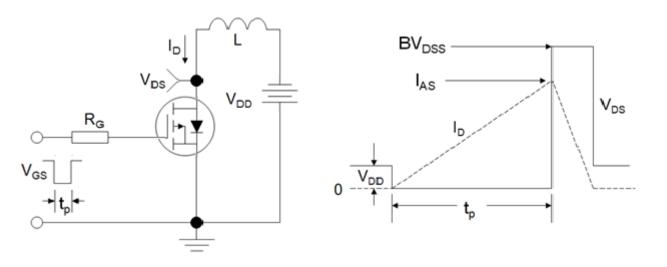
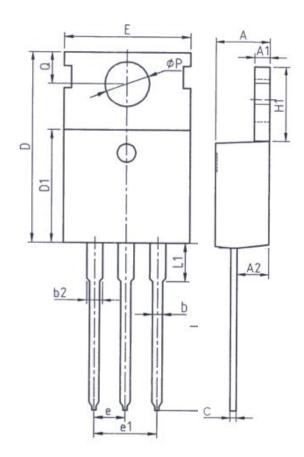


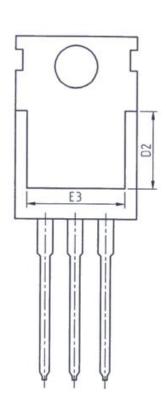
Figure C: Unclamped Inductive Switching Test Circuit and Waveform



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# **TO-220**

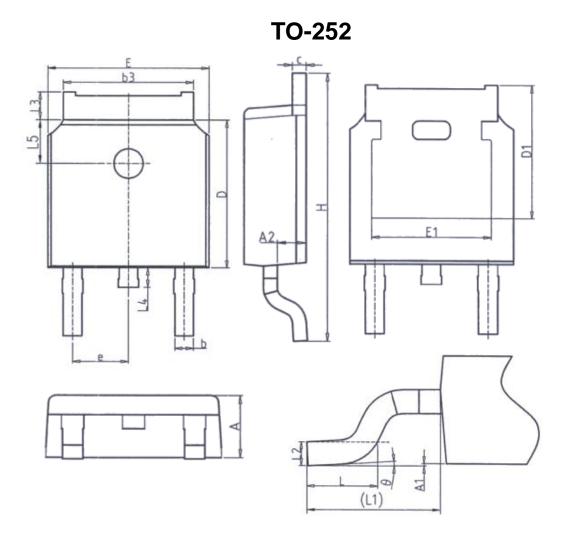




Unit: mm		
Symbol	Min.	Max.
Α	4. 37	4. 77
A1	1. 25	1. 45
A2	2. 20	2. 60
b	0. 70	0. 95
b2	1. 17	1. 47
С	0. 40	0. 65
D	15. 10	16. 10
D1	8. 80	9. 40
D2	5. 50	_

Unit: mm			
Symbol	Min.	Max.	
E	9. 70	10. 30	
E3	7. 00	-	
е	2. 54BSC		
e1	5. 08BSC		
H1	6. 25	6. 85	
L	12. 75 13. 8		
L1	-	3. 40	
P	3. 40	3. 80	
Q	2. 60	3. 00	





Unit: mm			
Symbol	Min.	Max.	
Α	2. 20	2. 40	
A1	0.00	0. 20	
A2	0. 97	1. 17	
b	0. 68	0.90	
b3	5. 20	5. 50	
С	0. 43	0. 63	
D	5. 98	6. 22	
D1	D1 5. 30REF		
E	6. 40	6. 80	
E1	4. 63	_	

Unit: mm			
Symbol	Min.	Max.	
е	2. 28	6BSC	
Н	9. 40	10.50	
L	1. 38	1. 75	
L1	L1 2. 90REF		
L2	0. 51BSC		
L3	0.88	1. 28	
L4	_	1.00	
L5	1. 65	1. 95	
θ	0°	8°	



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