

30V P-Channel Trench MOSFET(Preliminary)

General Description

- Trench Power technology
- Low R_{DS(ON)}
- Low Gate Charge
- Optimized for fast-switching applications

Applications

- Synchronous Rectification in DC/DC and AC/DC Converters
- Isolated DC/DC Converters in Telecom and Industrial

Product Summary

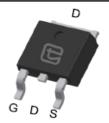
 $V_{DS} -30V$ $I_{D} (at V_{GS} = 10V) -40A$

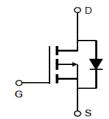
 $R_{DS(ON)}$ (at V_{GS} =-10V) < 19m Ω $R_{DS(ON)}$ (at V_{GS} =-4.5V) < 32m Ω

100% UIS Tested



TO-252





| Part Number | Package Type | Form | Marking | |
|-------------|--------------|-----------|---------|--|
| TTD40P03AT | TO-252 | Tape&Reel | 40P03AT | |

Absolute Maximum Ratings (T_A =25°C unless otherwise noted)

| Parameter | | Symbol | Maximum | Units | |
|--|-----------------------|-----------------------------------|------------|-------|--|
| Drain-Source Voltage | | V _{DS} | - 30 | V | |
| Gate-Source Voltage | | V _{GS} | ±20 | V | |
| Continuous Drain Current B | T _C =25°C | I _D | -40 | ۸ | |
| Continuous Drain Current B | T _C =100°C | | -28 | A | |
| Pulsed Drain Current ^ | | I _{DM} | -120 | Α | |
| Avalanche Current A | | I _{AS} | -20 | А | |
| Single Pulse Avalanche Energy L =0.3mH A | | E _{AS} | 60 | mJ | |
| Power Dissipation ^C | T _C =25°C | - P _D | 60 | W | |
| | T _C =100°C | | 30 | W | |
| Junction and Storage Temperature Range | | T _J , T _{STG} | -55 to 175 | °C | |

Thermal Characteristics

| Parameter | | Symbol | Maximum | Units | |
|-----------------------------|--------------|------------------|---------|--------|--|
| Maximum Junction-to-Case | Steady-State | R _{eJC} | 2.5 | 00.004 | |
| Maximum Junction-to-Ambient | Steady-State | R _{eJA} | 100 | °C/W | |



| | | Conditions | | Value | | | |
|----------------------|---|---|-----------------------|-------|------|------|-------|
| Symbol | Parameter | | | Min | Тур | Max | Units |
| STATIC P | ARAMETERS | 1 | - | | | | |
| BV _{DSS} | Drain-Source Breakdown Voltage | I _D =-250μA,V _{GS} =0V | | -30 | | | V |
| | | $V_{DS} = -30V$, $V_{GS} = 0V$ | T _J =25°C | | | -1 | μА |
| I _{DSS} | Zero Gate Voltage Drain Current | | T _J =100°C | | | -100 | |
| I _{GSS} | Gate-Body Leakage Current | $V_{DS} = 0V, V_{GS} = \pm 20V$ | • | | | ±100 | nA |
| V _{GS(th)} | Gate Threshold Voltage | $V_{DS} = V_{GS}, I_{D} = -250 \mu A$ | | -1 | -1.7 | -2.4 | V |
| D | Ctatia Duais Course On Resistance | V _{GS} =-10V, I _D =-20A | | | 14 | 19 | mΩ |
| R _{DS(ON)} | Static Drain-Source On-Resistance | V _{GS} =-4.5V, I _D =-20A | | | 27 | 32 | mΩ |
| g _{FS} | Forward Transconductance | $V_{DS} = -5V, I_{D} = -20A$ | | | 20 | | S |
| V_{SD} | Diode Forward Voltage | I _S =-15A, V _{GS} =0V | | | | -1 | V |
| I _S | Maximum Body-Diode Continuous Current B | | | | | -40 | Α |
| DYNAMIC | PARAMETERS | | | | | _ | |
| C _{iss} | Input Capacitance | $V_{GS} = 0V, V_{DS} = -20V, f = 1MH_Z$ | | | 2500 | | pF |
| C _{oss} | Output Capacitance | | | | 288 | | |
| C _{rss} | Reverse Transfer Capacitance | | | | 254 | | |
| SWITCHI | NG PARAMETERS | | | | | | |
| Q _g (10V) | Total Gate Charge | V _{GS} =-10V,V _{DS} =-20V, I _D =-20A | | | 50 | | |
| Q_{gs} | Gate Source Charge | | | | 12 | | nC |
| Q_{gd} | Gate Drain Charge | | | | 8 | | |
| t _{D(on)} | Turn-On Delay Time | $V_{GS} = -10V, V_{DS} = -20V, I_{D} = -20A,$ $R_{G} = 2.5\Omega$ | | | 12 | | |
| t _r | Turn-On Rise Time | | | | 17 | | ns |
| $T_{D(off)}$ | Turn-Off Delay Time | | | | 54 | | |
| t _f | Turn-Off Fall Time | | | | 24 | | |
| t _{rr} | Body Diode Reverse Recovery Time | 204 di/dt -1004 | /ue | | 28 | | ns |
| Q _{rr} | Body Diode Reverse Recovery Charge | ∏ _F =-20A, di/dt =100A/μs | | | 19 | | nC |

- A. Single pulse width limited by maximum junction temperature.
- B. The maximum current rating is package limited.
- C. The power dissipation P_D is based on $T_{J(MAX)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.



Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

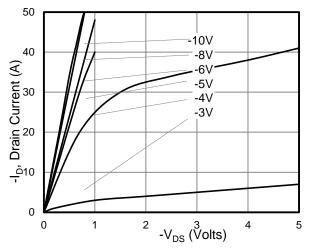


Figure 1: On-Region Characteristics

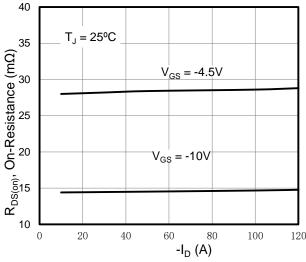


Figure 3: On-Resistance vs. Drain Current

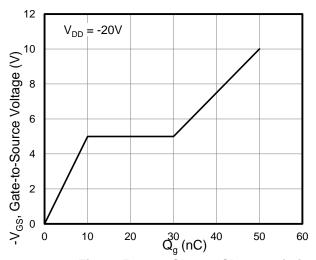
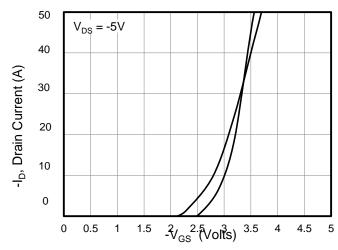


Figure 5: Gate Charge Characteristics



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Figure 2: Transfer Characteristics

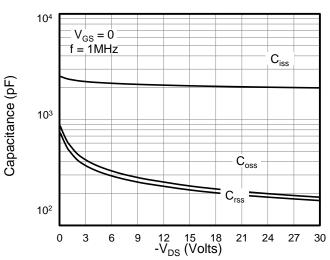


Figure 4: Capacitance Characteristics

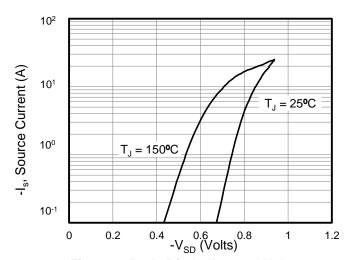
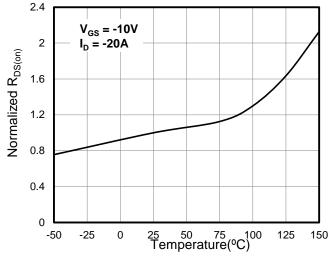


Figure 6: Body Diode Forward Voltage

 $Z_{\theta,JC}$ Normalized Transient Thermal Resistance

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Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted



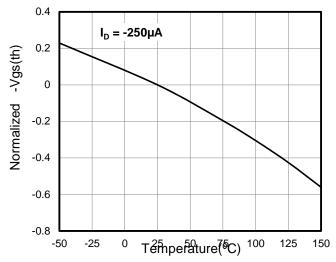
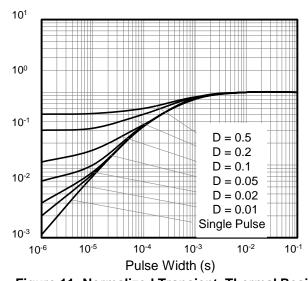


Figure 7: On-Resistance vs. Junction Temperature

Figure 8: Vgs(th) vs. Junction Temperature



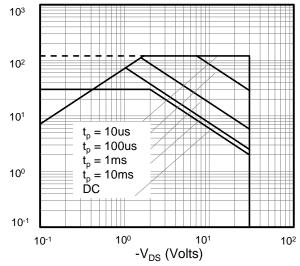


Figure 11: Normalized Transient Thermal Resistance

Figure 12: Safe Operating Area

-I_D (Amps)

Figure A: Gate Charge Test Circuit and Waveform

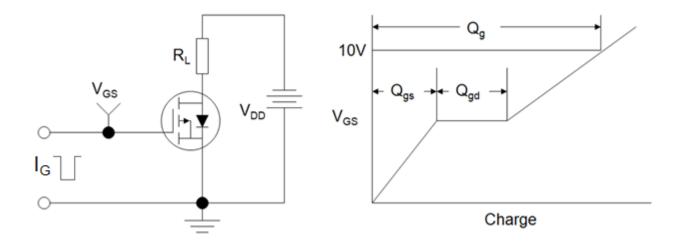


Figure B: Resistive Switching Test Circuit and Waveform

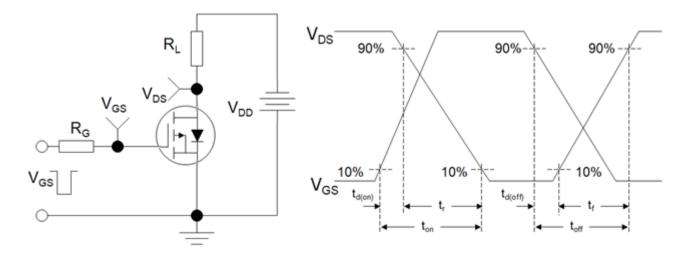
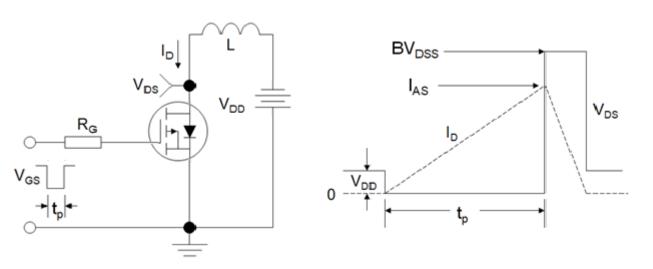
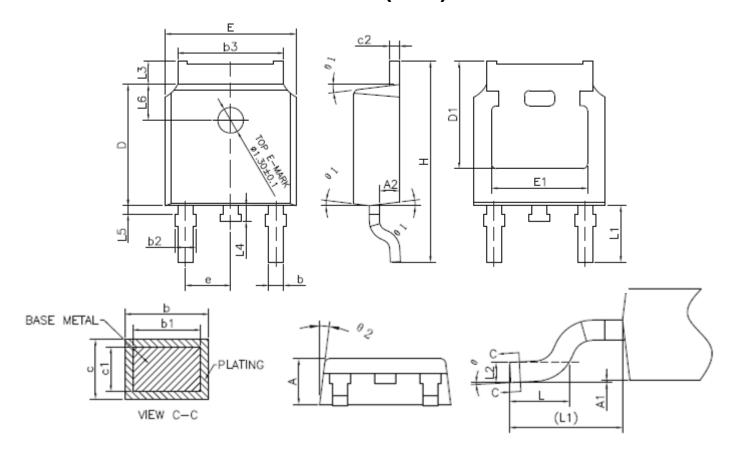


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





TO-252(集佳)



| SYMBOL | MIN | NOM | MAX | |
|--------|-----------|----------|-------|--|
| A | 2,20 | 2,30 | 2,38 | |
| A1 | 0 | _ | 0,10 | |
| A2 | 0,90 | 1,01 | 1,10 | |
| b | 0.72 | _ | 0.85 | |
| b1 | 0.71 | 0.76 | 0.81 | |
| b2 | 0.72 | _ | 0.90 | |
| b3 | 5.13 | 5.33 | 5.46 | |
| c | 0.47 | _ | 0.60 | |
| c1 | 0,46 | 0.51 | 0,56 | |
| c2 | 0,47 | _ | 0,60 | |
| D | 6,00 | 6,10 | 6,20 | |
| D1 | 5,25 | _ | | |
| E | 6.50 | 6.60 | 6.70 | |
| E1 | 4.70 | _ | _ | |
| e | 2.186 | 2.286 | 2.386 | |
| Н | 9.80 | 10.10 | 10.40 | |
| L | 1,40 | 1,50 | 1,70 | |
| L1 | 2.90 REF | | | |
| L2 | 0,508 BSC | | | |
| L3 | 0,90 | _ | 1,25 | |
| L4 | 0,60 | 0,80 | 1.00 | |
| L5 | 0.15 | <u> </u> | 0.75 | |
| L6 | 1.80 REF | | | |
| θ | 0° | <u> </u> | 8° | |
| θ1 | 5° | 7° | 9° | |
| 92 | 5° | 7° | 9° | |



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