

40V N-Channel Trench MOSFET(Preliminary)

General Description

- Trench Power technology
- Low R_{DS(ON)}
- Low Gate Charge
- Optimized for fast-switching applications

Applications

- Synchronous Rectification in DC/DC and AC/DC Converters
- Isolated DC/DC Converters in Telecom and Industrial

Product Summary

 V_{DS} 40V I_{D} (at $V_{GS} = 10V$) 65A $R_{DS(ON)}$ (at $V_{GS} = 10V$) < 8.0m Ω

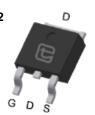
100% UIS Tested

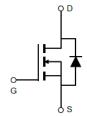
 $R_{DS(ON)}$ (at $V_{GS} = 4.5V$)



 $< 12.5 m\Omega$







Part Number	Package Type	Form	Marking
TTD65N04AT	TO-252	Tape&Reel	65N04AT

Absolute Maximum Ratings (T_A =25°C unless otherwise noted)

Parameter		Symbol	Maximum	Units
Drain-Source Voltage		V _{DS}	40	V
Gate-Source Voltage		V _{GS}	±20	V
Continuous Drain Commant B	T _C =25°C	I _D	46	٨
Continuous Drain Current B	T _C =100°C		45	A
Pulsed Drain Current ^A		I _{DM}	195	Α
Avalanche Current ^A		I _{AS}	21	А
Single Pulse Avalanche Energy L =0.3mH A		E _{AS}	66	mJ
Power Dissipation ^C	T _C =25°C		65	W
	T _C =100°C	P _D	32	W
Junction and Storage Temperature Range		T _J , T _{STG}	-55 to 175	°C

Thermal Characteristics

Parameter		Symbol	Maximum	Units
Maximum Junction-to-Case Steady-State		$R_{\Theta JC}$	2.3	00.444
Maximum Junction-to-Ambient	Steady-State	R _{eJA}	100	°C/W



		Conditions		Value			
Symbol	Parameter			Min	Тур	Max	Units
STATIC P	ARAMETERS	•					
BV _{DSS}	Drain-Source Breakdown Voltage	I _D =250μA,V _{GS} =0V		40			V
		-	T _J =25°C			1	μA
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 40V, V_{GS} = 0V$	T _J =100°C			25	
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$,			±100	nA
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		1	1.7	2.4	V
D	Ctatia Duais Course On Resistance	V _{GS} =10V, I _D =30A			6.7	8	mΩ
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =4.5V, I _D =30A			10.5	12.5	mΩ
g _{FS}	Forward Transconductance	$V_{DS} = 10V, I_{D} = 20A$			26.4		S
V_{SD}	Diode Forward Voltage	I _S =30A, V _{GS} =0V				1	٧
I _S	Maximum Body-Diode Continuous Curre	nt ^B				46	Α
DYNAMIC	PARAMETERS						
C _{iss}	Input Capacitance	$V_{GS} = 0V, V_{DS} = 20V, f = 1MH_Z$			2025		pF
C _{oss}	Output Capacitance				190		
C_{rss}	Reverse Transfer Capacitance				177		
SWITCHI	NG PARAMETERS						
Q _g (10V)	Total Gate Charge	V _{GS} =10V,V _{DS} =20V, I _D =30A			48		
Q_{gs}	Gate Source Charge				8.5		nC
Q_{gd}	Gate Drain Charge				10		
t _{D(on)}	Turn-On Delay Time	$V_{GS} = 10V, V_{DS} = 20V, I_{D} = 30A,$ $R_{G} = 3\Omega$			7		
t _r	Turn-On Rise Time				4		ns
$T_{D(off)}$	Turn-Off Delay Time				25		
t _f	Turn-Off Fall Time				5		
t _{rr}	Body Diode Reverse Recovery Time	1 -204 di/dt -1004/	118		15.5		ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =20A, di/dt =100A/μs			31		nC

- A. Single pulse width limited by maximum junction temperature.
- B. The maximum current rating is package limited.
- C. The power dissipation P_D is based on $T_{J(MAX)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

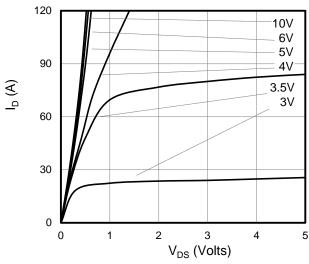


Figure 1: On-Region Characteristics

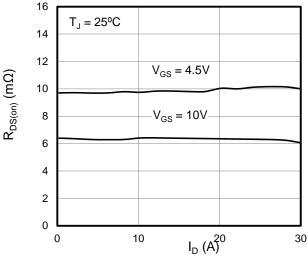


Figure 3: On-Resistance vs. Drain Current

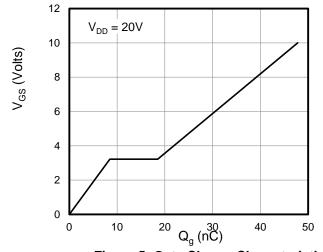


Figure 5: Gate Charge Characteristics

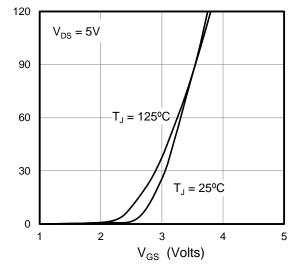


Figure 2: Transfer Characteristics

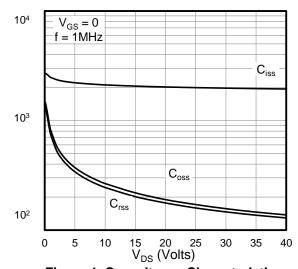


Figure 4: Capacitance Characteristics

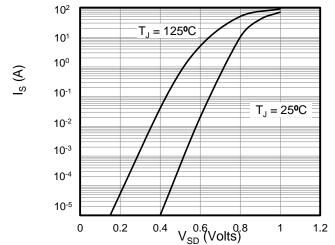


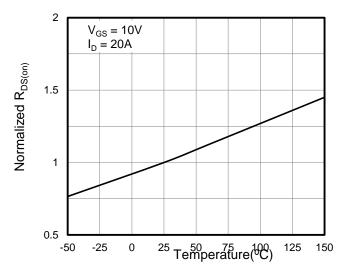
Figure 6: Body Diode Forward Voltage

Capacitance (pF)



 $Z_{\theta\,\text{JC}}$ Normalized Transient Thermal Resistance

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



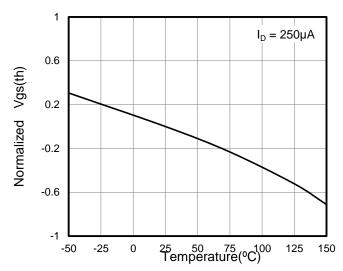
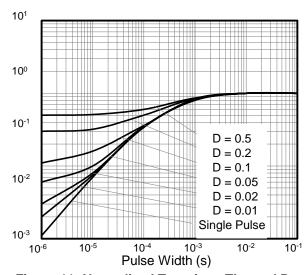


Figure 7: On-Resistance vs. Junction Temperature

Figure 8: Vgs(th) vs. Junction Temperature



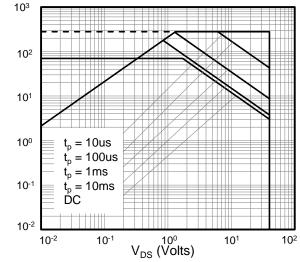
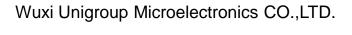


Figure 11: Normalized Transient Thermal Resistance

Figure 12: Safe Operating Area

I_D (Amps)





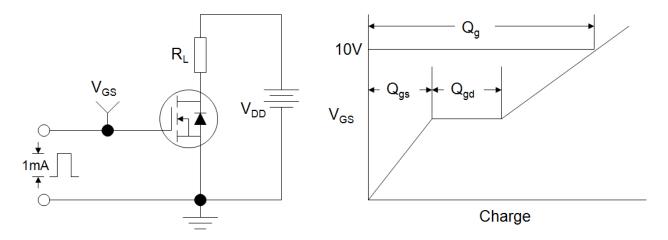


Figure A: Gate Charge Test Circuit and Waveforms

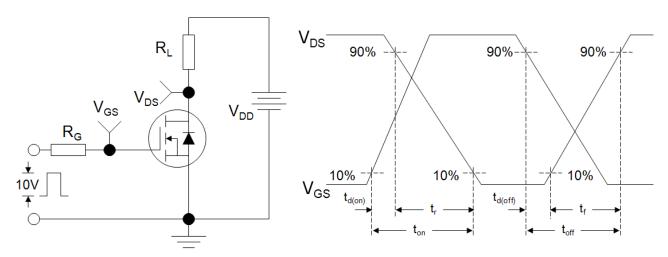


Figure B: Resistive Switching Test Circuit and Waveforms

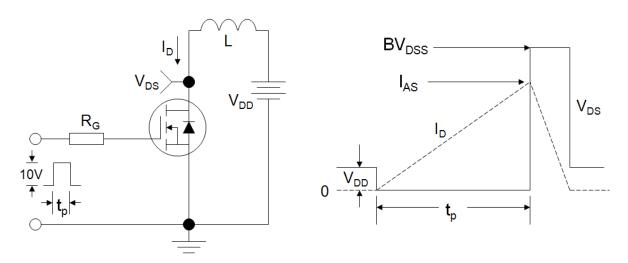
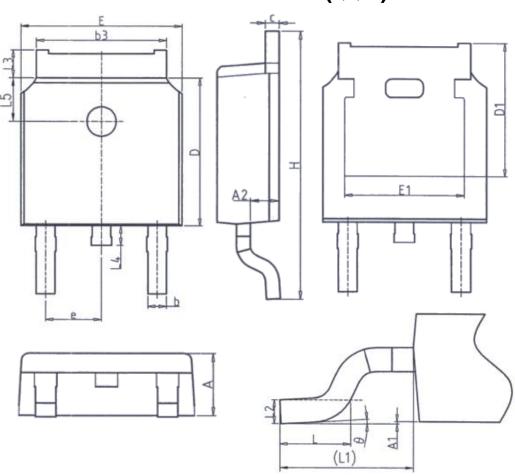


Figure C: Unclamped Inductive Switching (UIS) Test Circuit and Waveforms



TO-252(华天)

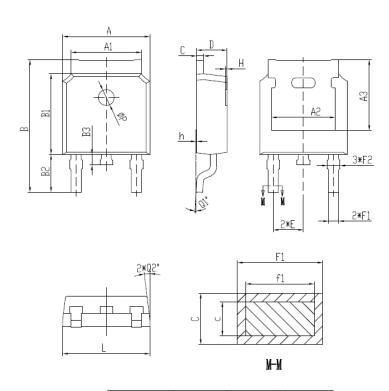


Unit: mm				
Symbol	Min.	Max.		
Α	2. 20	2. 40		
A1	0.00	0. 20		
A2	0. 97	1. 17		
b	0. 68	0. 90		
b3	5. 20	5. 50		
С	0. 43	0. 63		
D	5. 98	6. 22		
D1	5. 30REF			
E	6. 40	6. 80		
E1	4. 63	_		

Unit: mm				
Symbol	Min. Max.			
е	2. 286BSC			
Н	9. 40 10. 50			
L	1. 38 1. 75			
L1	2. 90REF			
L2	0. 51BSC			
L3	0.88	1. 28		
L4	- 1.00			
L5	1. 65 1. 95			
θ	0° 8°			



TO-252(海天)



SYMBOL	MIN	NOM	MAX
A	6. 50	6. 60	6. 70
A1	5. 16	5. 31	5. 46
A2		4.83 REF	
A3		5.30 REF	
В	9. 77	9. 97	10.17
B1	6.00	6. 10	6. 20
B2	2.60	2.80	3. 00
В3	0.70	0.80	0.90
С	0.41	_	0.61
С	0.40	0.50	0.60
D	2. 20	2.30	2. 40
E	2. 186	2. 286	2. 386
F1	0.67	_	0.87
fl	0.66	0.76	0.86
F2	0.76	0.86	0.96
Н	0.00	_	0.30
h	0.00	_	0. 20
L	6. 50	6.60	6. 70
øP	1.10	1.20	1.30
Q1°	0°	_	8°
Q2°	6°	7°	8°



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