

60V N-Channel Trench MOSFET

General Description

- Trench Power technology
- Low R_{DS(ON)}
- Low Gate Charge
- Optimized for fast-switching applications

Applications

- Synchronous Rectification in DC/DC and AC/DC Converters
- Isolated DC/DC Converters in Telecom and Industrial

Product Summary

 V_{DS} 60V I_{D} (at $V_{GS} = 10V$) 105A $R_{DS(ON)}$ (at $V_{GS} = 10V$) < 7.5m Ω

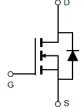
100% UIS Tested











Part Number	Package Type	Form	Marking
TTB105N06A	TO-263	Tape&Reel	105N06A
TTP105N06A	TO-220	Tube	105N06A

Absolute Maximum Ratings (T_A =25°C unless otherwise noted)

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Parameter		Symbol	Maximum	Units	
Drain-Source Voltage		V _{DS}	60	V	
Gate-Source Voltage		V _{GS}	±20	V	
Continuous Drain Current B	T _C =25°C		105	^	
Continuous Drain Current B	T _C =100°C	l _D	73.5	А	
Pulsed Drain Current ^A		I _{DM}	315	А	
Avalanche Current A		I _{AS}	30	А	
Single Pulse Avalanche Energy L =0.3mH A		E _{AS}	135	mJ	
Dower Discipation C	T _C =25°C		187.5	W	
Power Dissipation ^C	T _C =100°C	P _D	93.8	W	
Junction and Storage Temperature Range		T _J , T _{STG}	-55 to 175	°C	

Thermal Characteristics

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Parameter		Symbol	Maximum	Units	
Maximum Junction-to-Case Steady-State		$R_{\Theta JC}$	0.8	°C/W	
Maximum Junction-to-Ambient	Steady-State	$R_{\Theta JA}$	100	3C/VV	



Electric	cal Characteristics(T _J =25°C u	nless otherwise	noted)				
0	Barranatar	rameter Conditions		Value			
Symbol	Parameter			Min	Тур	Max	Units
STATIC P	ARAMETERS	•					
BV _{DSS}	Drain-Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$		60			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =60V, V _{GS} =0V	T _J =25°C T _J =100°C			1 25	μA
I _{GSS}	Gate-Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$				±100	nA
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		2	3	4	V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =30A			6.2	7.5	mΩ
g _{FS}	Forward Transconductance	V _{DS} =10V, I _D =25A			25		S
V_{SD}	Diode Forward Voltage	I _S =30A, V _{GS} =0V				1	V
I _S	Maximum Body-Diode Continuous Current B				105	Α	
DYNAMIC	PARAMETERS					•	
C _{iss}	Input Capacitance				4211		
C _{oss}	Output Capacitance	$V_{GS} = 0V, V_{DS} = 30V, f$	$V_{GS} = 0V, V_{DS} = 30V, f = 1MH_{Z}$		298		pF
C _{rss}	Reverse Transfer Capacitance				264		
SWITCHI	NG PARAMETERS	•				•	
Q _g (10V)	Total Gate Charge				82		
Q_{gs}	Gate Source Charge	$V_{GS} = 10V, V_{DS} = 30V,$	V _{GS} =10V,V _{DS} =30V, I _D =30A		22		nC
Q_{gd}	Gate Drain Charge				23		
t _{D(on)}	Turn-On Delay Time		$V_{GS} = 10V, V_{DS} = 30V, I_{D} = 30A,$ $R_{G} = 3\Omega$		20		
t _r	Turn-On Rise Time	$V_{GS} = 10V, V_{DS} = 30V.$			18		_
$T_{D(off)}$	Turn-Off Delay Time				50		ns
t _f	Turn-Off Fall Time				80		
t _{rr}	Body Diode Reverse Recovery Time	I 00A I'/II 100A			50		ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =20A, di/dt =100A/μs			95		nC

- A. Single pulse width limited by maximum junction temperature.
- B. The maximum current rating is package limited.
- C. The power dissipation P_D is based on $T_{J(MAX)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

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Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

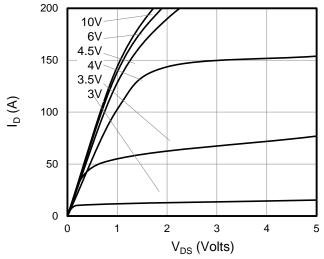


Figure 1: On-Region Characteristics

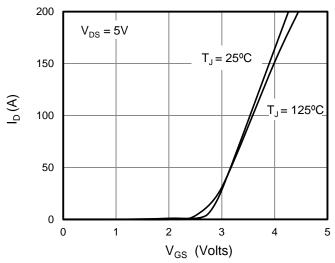


Figure 2: Transfer Characteristics

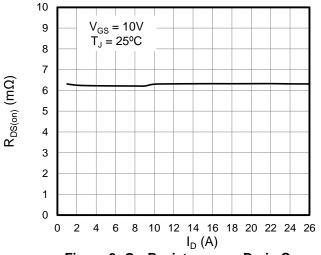


Figure 3: On-Resistance vs. Drain Current

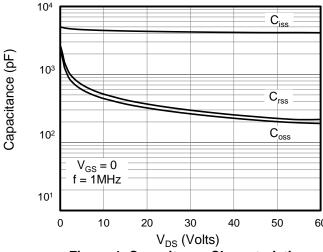


Figure 4: Capacitance Characteristics

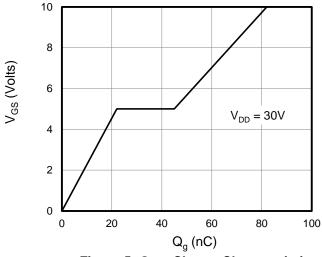


Figure 5: Gate Charge Characteristics

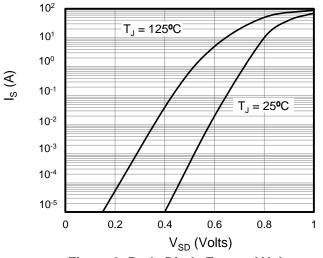
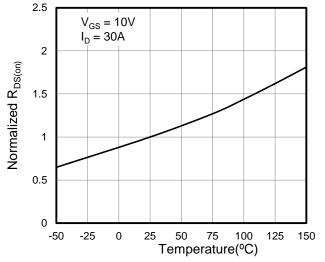


Figure 6: Body Diode Forward Voltage



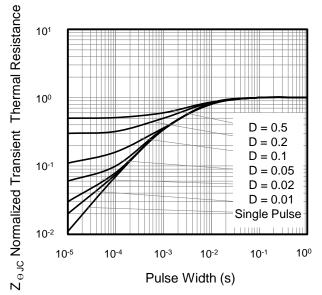
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1.4 $I_{D} = 250 \mu A$ Normalized Vgs(th) 1.2 8.0 0.6 0.4 -50 -25 0 25 50 75 100 125 150 Temperature(°C)

Figure 7: On-Resistance vs. Junction Temperature

Figure 8: Vgs(th) vs. Junction Temperature



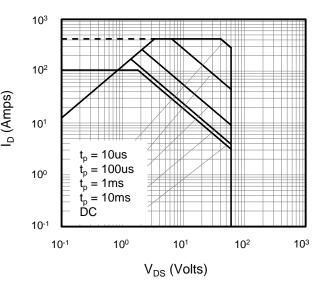


Figure 9: Normalized Transient Thermal Resistance

Figure 10: Safe Operating Area

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Figure A: Gate Charge Test Circuit and Waveform

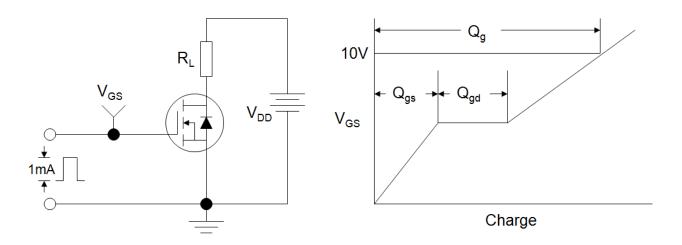


Figure B: Resistive Switching Test Circuit and Waveform

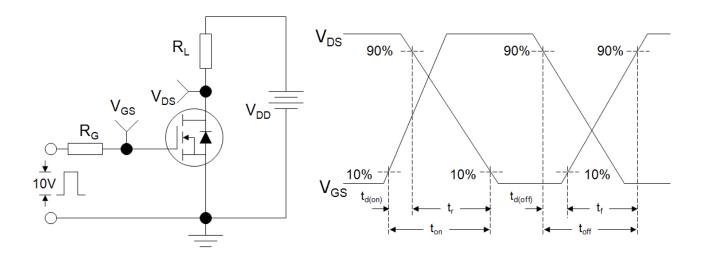
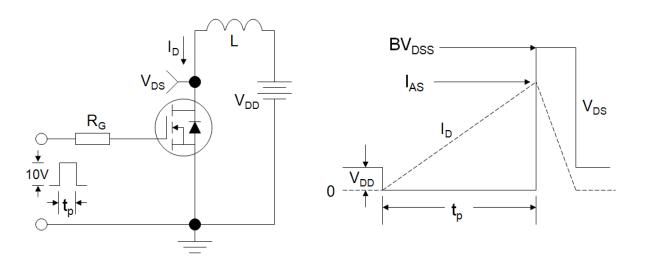


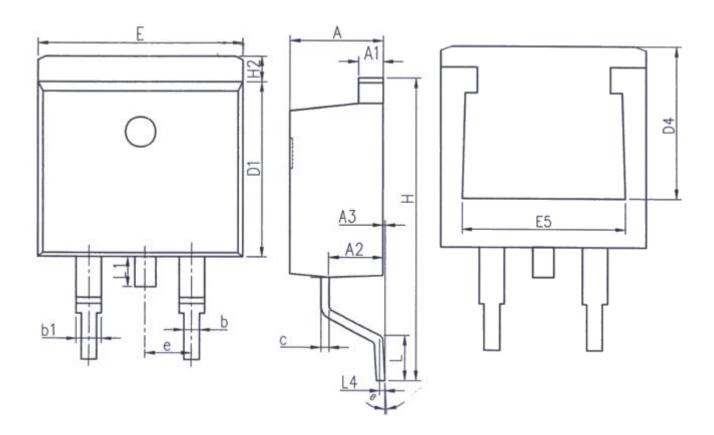
Figure C: Unclamped Inductive Switching Test Circuit and Waveform



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TO-263(华天)

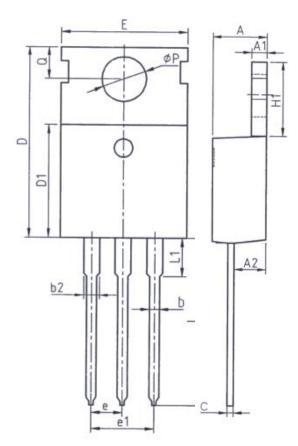


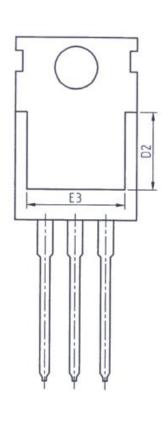
Unit: mm				
Symbol	Min	Nom	Max	
Α	4.40	4.50	4.60	
A1	1.25	1.30	1.35	
A2	2.20	2.40	2.60	
A3	0	0.10	0.25	
b	0.76	-	0.89	
b1	1.23	-	1.37	
С	0.47	-	0.60	
D1	9.10	9.20	9.30	
D4	8.00	-	-	

Unit: mm				
Symbol	Min	Nom	Max	
Е	9.80	9.90	10.00	
E5	7.80	-	-	
е	2.54BSC			
Н	14.90	15.30	15.70	
H2	1.17	1.27	1.40	
L	2.00	2.30	2.60	
L1	-	-	1.75	
L4	0.25BSC			
θ	0°		8°	



TO-220(华天)





Unit: mm				
Symbol	Min.	Max.		
Α	4. 37	4. 77		
A1	1. 25	1. 45		
A2	2. 20	2. 60		
b	0. 70	0. 95		
b2	1. 17	1. 47		
С	0. 40	0. 65		
D	15. 10	16. 10		
D1	8. 80	9. 40		
D2	5. 50	_		

Unit: mm				
Symbol	Min. Max.			
E	9. 70	10. 30		
E3	7. 00	ı		
е	2. 54BSC			
e1	5. 08BSC			
H1	6. 25 6. 85			
L	12. 75	13.80		
L1	- 3.40			
Р	3. 40	3. 80		
Q	2. 60	3. 00		

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