

# **UTC** UNISONIC TECHNOLOGIES CO., LTD

## 15N10

## Power MOSFET

# 14.7A, 100V (D-S) N-CHANNEL **POWER MOSFET**

#### DESCRIPTION

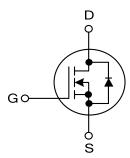
The UTC 15N10 is an N-Channel enhancement MOSFET, it uses UTC's advanced technology to provide customers with a minimum on-state resistance, high switching speed and low gate charge.

The UTC 15N10 is suitable for high efficiency switching DC/DC converter, LCD display inverter and load switch.

#### **FEATURES**

- \*  $R_{DS(ON)}$ =0.08 $\Omega$  @V<sub>GS</sub>=10V,I<sub>D</sub>=8A
- \* Low gate charge (Typ=24nC)
- \* Low C<sub>RSS</sub> (Typ=23pF)
- \* High switching speed

SYMBOL 



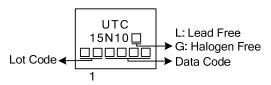
## **ORDERING INFORMATION**

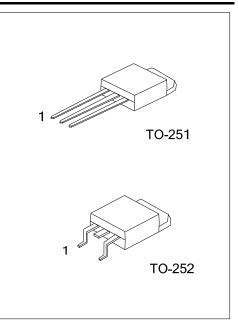
Ordering Number		Deekege	Pin Assignment			Deaking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
15N10L-TM3-T	15N10G-TM3-T	TO-251	G	D	S	Tube	
15N10L-TN3-R	15N10G-TN3-R	TO-252	G	D	S	Tape Reel	
Note: Pin Assignment: G: Gate D: Drain S: Source							

Assignment: G: Gate D: Drain 15N10L-TM3-T (1)Packing Type (1) T: Tube, R: Tape Reel

(2)Package Type (2) TM3: TO-251, TN3: TO-252 (3)Green Package (3) L: Lead Free, G: Halogen Free and Lead Free

#### MARKING





#### ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>=25°C, unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V <sub>DSS</sub>	100	V	
Gate-Source Voltage		V <sub>GSS</sub>	±20	V	
Drain Current	Continuous	T <sub>C</sub> =25°C, TJ=150°C	I <sub>D</sub>	14.7	А
	Continuous	T <sub>C</sub> =70°C, T <sub>J</sub> =150°C		13.6	А
	Pulsed	Pulsed		59	А
Power Dissipation $\frac{T_{C}=25^{\circ}C}{T_{C}=70^{\circ}C}$		T <sub>C</sub> =25°C	D	34.7	W
		P <sub>D</sub>	22.2	W	
Operating Junction Temperature		TJ	-55 ~ +150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### ■ THERMAL CHARACTERISTICS (T<sub>A</sub>=25°C, unless otherwise noted)

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Case (Note)	θ <sub>JC</sub>	3.6	°C/W

Note: The device mounted on 1in<sup>2</sup> FR4 board with 2 oz copper.

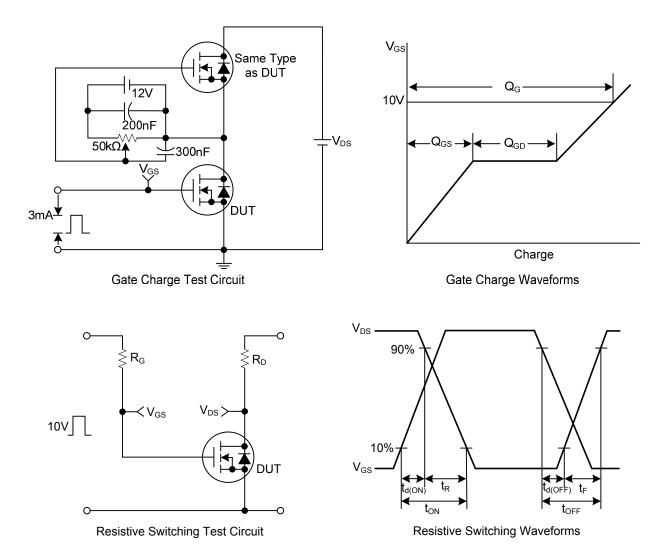
#### ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	BOL TEST CONDITIONS		TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V	100			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V			1	μA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =+20V, V <sub>DS</sub> =0V			+100	nA
		V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V			-100	nA
ON CHARACTERISTICS			_			
Gate Threshold Voltage	eshold Voltage V <sub>GS(TH)</sub> V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA		1.0		3.0	V
Drain-Source On-State Resistance (Note)	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =8A		80	100	mΩ
DYNAMIC PARAMETERS			•			-
Input Capacitance	C <sub>ISS</sub>			890		pF
Output Capacitance	Coss	V <sub>GS</sub> =0V, V <sub>DS</sub> =15V, f=1MHz		58		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			23		рF
SWITCHING PARAMETERS			•			-
Total Gate Charge	Q <sub>G</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =80V, I <sub>D</sub> =10A		24		nC
Total Gate Charge	Q <sub>G</sub>			13		nC
Gate to Source Charge	Q <sub>GS</sub>	V <sub>GS</sub> =4.5V, V <sub>DS</sub> =80V, I <sub>D</sub> =10A		4.6		nC
Gate to Drain Charge	Q <sub>GD</sub>			7.6		nC
Gate-Resistance	R <sub>G</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz		0.9		Ω
Turn-ON Delay Time	t <sub>D(ON)</sub>			14		ns
Rise Time	t <sub>R</sub>	$V_{DS}$ =50V, R <sub>L</sub> =5 $\Omega$ , V <sub>GEN</sub> =10V,		33		ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>	R <sub>G</sub> =1Ω		39		ns
Fall-Time	t <sub>F</sub>			5		ns
SOURCE- DRAIN DIODE RATINGS AND (	CHARACTER	ISTICS				
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =8A, V <sub>GS</sub> =0V		0.9	1.2	V

Note: Pulse test: pulse width≤300us, duty cycle≤2%, Guaranteed by design, not subject to production testing.

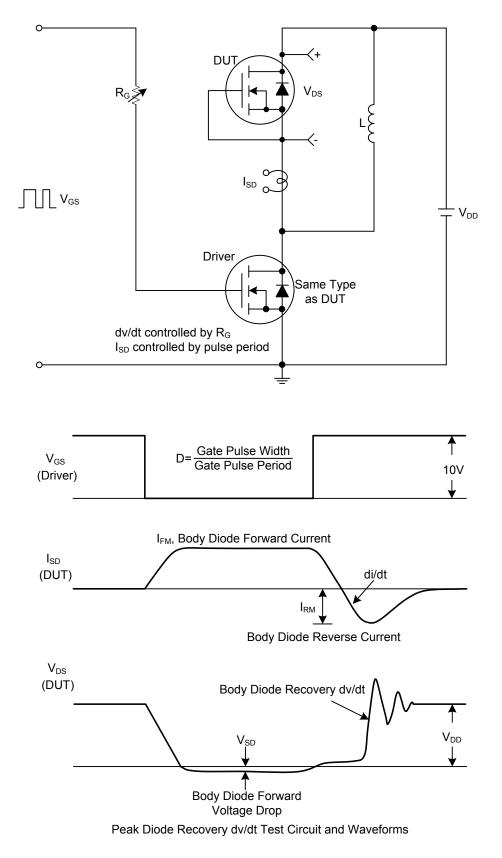


#### TEST CIRCUITS AND WAVEFORMS



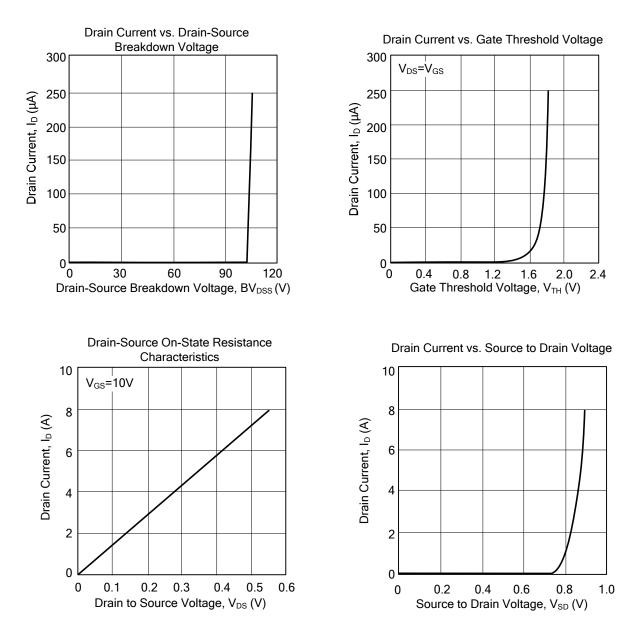


#### TEST CIRCUITS AND WAVEFORMS





### **TYPICAL CHARACTERISTICS**



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